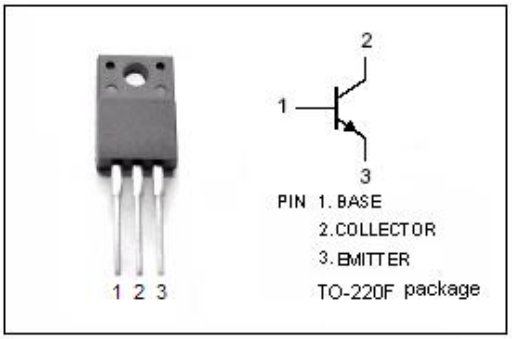
isc Silicon NPN Power Transistor 2SC4508

DESCRIPTION

- Fast switching speed
- Silicon NPN planar diffused planar transistor
- 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

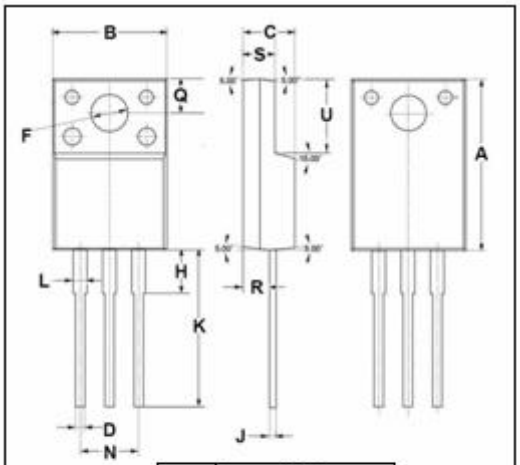


APPLICATIONS

- Audio temperature compensation and general purpose

ABSOLUTE MAXIMUM RATINGS(T_a=25℃)

SYMBOL	PARAMETER	VALUE	UNIT
V_{CBO}	Collector-Base Voltage	500	V
V_{CEO}	Collector-Emitter Voltage	400	V
V_{EBO}	Emitter-Base Voltage	7	V
I_C	Collector Current-Continuous	10	A
I_B	Base Current-Continuous	2	A
P_C	Collector Power Dissipation @T_C=25℃	40	W
T_J	Junction Temperature	150	℃
T_{stg}	Storage Temperature	-55~150	℃



DIM	mm MIN	mm MAX
A	14.95	15.05
B	10.00	10.10
C	4.40	4.60
D	0.75	0.90
F	3.10	3.30
H	3.70	3.90
J	0.50	0.70
K	13.4	13.6
L	1.10	1.30
N	5.00	5.20
Q	2.70	2.90
R	2.20	2.40
S	2.65	2.90
U	6.40	6.60

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ELECTRICAL CHARACTERISTICS

Tj=25℃ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
$V_{(BR)CEO}$	Collector-Emitter Breakdown Voltage	I_C= 25mA; I_B= 0	400			V
$V_{CE(sat)}$	Collector-Emitter Saturation Voltage	I_C= 4A; I_B= 0.8A			0.8	V
$V_{BE(sat)}$	Collector-Emitter Saturation Voltage	I_C= 4A; I_B= 0.8A			1.5	V
I_{CBO}	Collector Cutoff Current	V_{CB}= 500V; I_E= 0			100	μA
I_{EBO}	Emitter Cutoff Current	V_{EB}= 6V; I_C= 0			100	μA
h_{FE-1}	DC Current Gain	I_C= 1A; V_{CE}= 5V	25		65	
h_{FE-2}	DC Current Gain	I_C= 4A; V_{CE}= 5V	20			