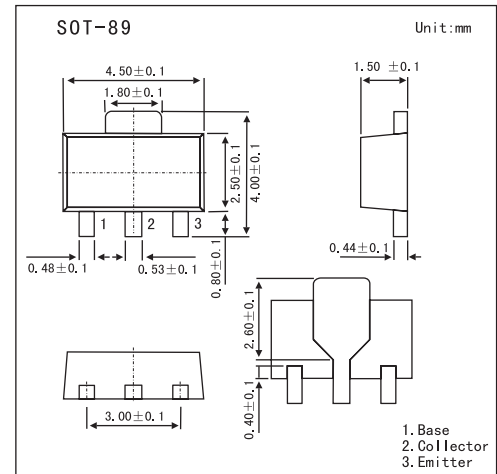


NPN Epitaxial Planar Silicon Transistor

2SC4521

■ Features

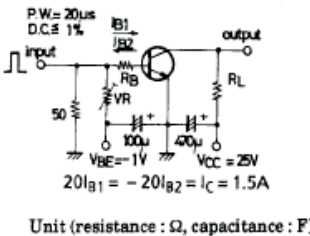
- Adoption of FBET, MBIT process.
- Large current capacity.
- Low collector-to-emitter saturation voltage.
- Fast switching speed.
- Small-sized package.

■ Absolute Maximum Ratings $T_a = 25^\circ\text{C}$

Parameter	Symbol	Rating	Unit
Collector-base voltage	V_{CB0}	60	V
Collector-emitter voltage	V_{CE0}	45	V
Emitter-base voltage	V_{EB0}	5	V
Collector current	I_C	3	A
Collector current (pulse)	I_{CP}	6	A
Collector dissipation, mounted on ceramic board(250mm ² X0.8mm)	P_C	1.5	W
Junction temperature	T_j	150	$^\circ\text{C}$
Storage temperature	T_{stg}	-55 to +150	$^\circ\text{C}$

2SC4521

■ Electrical Characteristics Ta = 25°C

Parameter	Symbol	Testconditions	Min	Typ	Max	Unit	
Collector cutoff current	I _{CBO}	V _{CB} = 45V, I _E =0			1	μA	
Emitter cutoff current	I _{EBO}	V _{EB} = 2V, I _C =0			10	μA	
DC current gain	h _{FE}	V _{CE} = 2V, I _C = 500mA	100		400		
Gain bandwidth product	f _T	V _{CE} = 2V, I _C = 500mA		300		MHz	
Output capacitance	C _{ob}	V _{CB} = 10V, f = 1.0MHz		25		pF	
Collector-emitter saturation voltage	V _{CE(sat)}	I _C = 1.5 A, I _B = 75 mA		0.25	0.7	V	
Base-emitter saturation voltage	V _{BE(sat)}	I _C = 1.5 V, I _B = 75 mA		0.95	1.3	V	
Collector-base breakdown voltage	V _{(BR)CBO}	I _C = 10μA, I _E = 0	60			V	
Collector-emitter breakdown voltage	V _{(BR)CEO}	I _C = 1mA, R _{BE} = ∞	45			V	
Emitter-base breakdown voltage	V _{(BR)EBO}	I _E = 100μA, I _C = 0	5			V	
Turn-on time	t _{on}	 <p>PW = 20μs D.C. = 1%</p> <p>Unit (resistance : Ω, capacitance : F)</p>		50	100	ns	
Storage time	t _{stg}				150	270	ns
Fall time	t _f				180	350	ns

■ hFE Classification

Marking	CL		
	R	S	T
hFE	100~200	140~280	200~400