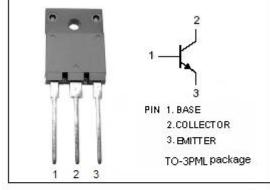


isc Silicon NPN Power Transistor

2SC4880

DESCRIPTION

- · High Breakdown Voltage
- · High Switching Speed
- 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

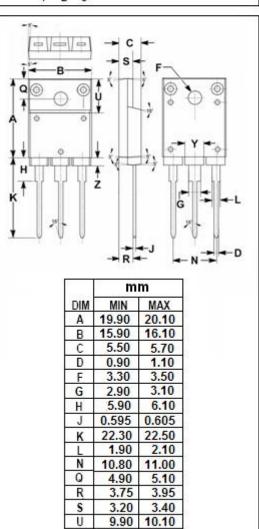


APPLICATIONS

 Very high-definition CRT display horizontal deflection output applications

ABSOLUTE MAXIMUM RATINGS(Ta=25℃)

SYMBOL	PARAMETER	VALUE	UNIT	
V _{CBO}	Collector-Base Voltage	1700	V	
Vceo	Collector-Emitter Voltage	900	V	
V _{EBO}	Emitter-Base Voltage	5	V	
lc	Collector Current-Continuous	12	А	
I _{CP}	Collector Current-Peak	20	Α	
P _C	Collector Power Dissipation @ T _a =25℃	20	w	
	Collector Power Dissipation @ T _C =25℃	100		
TJ	Junction Temperature	150	°C	
T _{stg}	Storage Temperature Range	-55~150	$^{\circ}$	



4.70

Z

1.90

4.90

2.10



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ELECTRICAL CHARACTERISTICS

T_C=25℃ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{CEO(SUS)}	Collector-Emitter Sustaining Voltage	I _C = 10mA; I _B = 0	900			V
V _{CE(sat)}	Collector-Emitter Saturation Voltage	I _C = 10A; I _B = 2.5A			5.0	V
$V_{BE(sat)}$	Base-Emitter Saturation Voltage	I _C = 10A; I _B = 2.5A			1.5	V
I _{CEO}	Collector Cutoff Current	V _{CE} = 1700V ; R _{BE} = 0			0.5	mA
I _{EBO}	Emitter Cutoff Current	V _{EB} = 5V ; I _C = 0			0.1	mA
h _{FE}	DC current gain	I _C = 1A; V _{CE} = 5V	8		40	



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