

isc Silicon NPN Power Transistor

2SC5043

DESCRIPTION

- · NPN triple diffused planar silicon transistor
- · High Breakdown Voltage
- · High Switching Speed
- 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

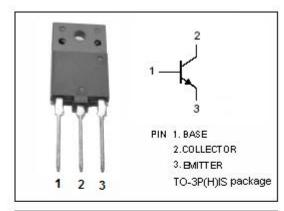


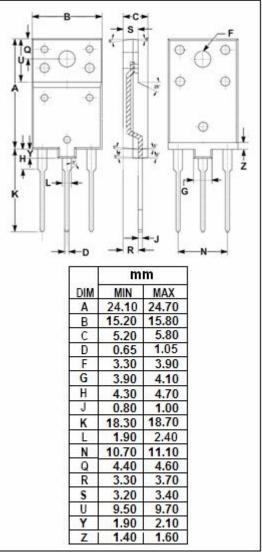
APPLICATIONS

 Very high-definition CRT display horizontal deflection output applications



SYMBOL	PARAMETER	VALUE	UNIT	
V _{CBO}	Collector-Base Voltage	1500	V	
V _{CEO}	Collector-Emitter Voltage	600	V	
V _{EBO}	Emitter-Base Voltage	5	V	
lc	Collector Current-Continuous	12	A	
Icp	Collector Current-Peak	24	А	
Pc	Collector Power Dissipation @ T _a =25℃	2	w	
	Collector Power Dissipation @ T _C =25℃	50		
TJ	Junction Temperature	150	$^{\circ}$	
T _{stg}	Storage Temperature Range	-55~150	°C	







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ELECTRICAL CHARACTERISTICS

T_C=25℃ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT	
V _{CEO(SUS)}	Collector-Emitter Sustaining Voltage	I _C = 10mA; I _B = 0	600			V	
V _{CE(sat)}	Collector-Emitter Saturation Voltage	I _C = 8A; I _B = 2A			3.0	V	
V _{BE(sat)}	Base-Emitter Saturation Voltage	I _C = 8A; I _B = 2A			1.5	V	
I _{CEO}	Collector Cutoff Current	V _{CE} = 800V ; R _{BE} = 0			100	uA	
I _{EBO}	Emitter Cutoff Current	V _{EB} = 5V ; I _C = 0			10	uA	
h _{FE-1}	DC current gain	I _C = 1A; V _{CE} = 5V	10		30		
h _{FE-2}	DC current gain	I _C = 8A; V _{CE} = 5V	4		8		
Сов	Output Capacitance	I _E = 0; V _{CB} = 10V; f _{test} =1.0MHz		160		pF	
Switching Times							

Switching Times

t _{stg}	Storage Time	L. = 60. L. = 1 50. L. = 1 50		4	μ S
t _f	Fall Time	I _{CP} = 6A; I _{B1} = 1.5A; I _{B2} = -1.5A		0.3	μS

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