

isc Silicon NPN Power Transistor
2SC5265
DESCRIPTION

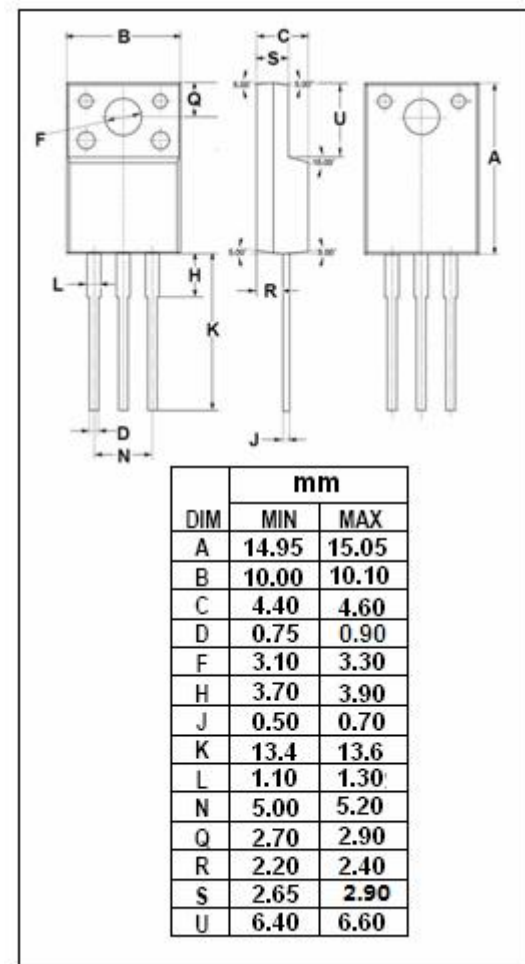
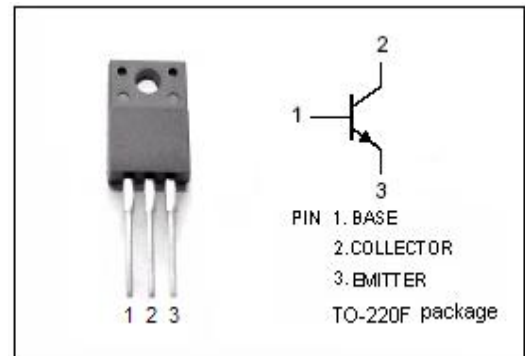
- High Breakdown Voltage-($V_{cb}=1200V$)
- High Reliability
- Adoption of MBIT process
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

APPLICATIONS

- Inverter-controlled
- Lighting

ABSOLUTE MAXIMUM RATINGS($T_a=25^{\circ}C$)

SYMBOL	PARAMETER	VALUE	UNIT
V_{CBO}	Collector-Base Voltage	1200	V
V_{CEO}	Collector-Emmitter Voltage	600	V
V_{EBO}	Emitter-Base Voltage	9	V
I_C	Collector Current- Continuous	4	A
I_{CM}	Collector Current-Peak	8	A
P_C	Collector Power Dissipation @ $T_c=25^{\circ}C$	30	W
T_J	Junction Temperature	150	$^{\circ}C$
T_{stg}	Storage Temperature Range	-55~150	$^{\circ}C$



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ELECTRICAL CHARACTERISTICS

T_c=25°C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{CEO(SUS)}	Collector-Emitter Sustaining Voltage	I _C = 10mA ; I _B =0	600			V
V _{CE(sat)}	Collector-Emitter Saturation Voltage	I _C = 2A; I _B = 0.4A			1.0	V
V _{BE(sat)}	Base-Emitter Saturation Voltage	I _C = 2A; I _B = 0.4A			1.5	V
I _{CES}	Collector Cutoff Current	V _{CE} = 1200V ; R _{BE} = 0			1.0	mA
I _{CBO}	Collector Cutoff Current	V _{CB} = 600V ; I _E = 0			10	uA
I _{EBO}	Emitter Cutoff Current	V _{EB} = 9V ; I _C = 0			1.0	mA
h _{FE-1}	DC Current Gain	I _C = 0.3A ; V _{CE} = 5V	30		50	
h _{FE-2}	DC Current Gain	I _C = 1.5A ; V _{CE} = 5V	10			

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