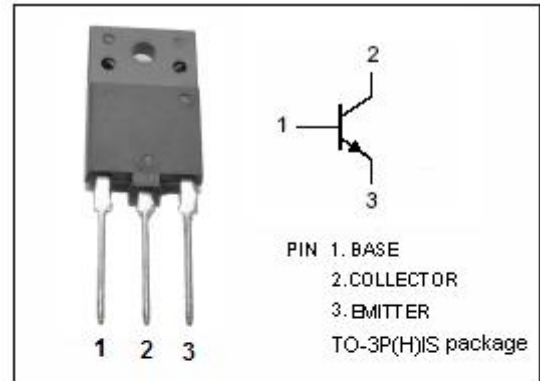


isc Silicon NPN Power Transistor
2SC5516
DESCRIPTION

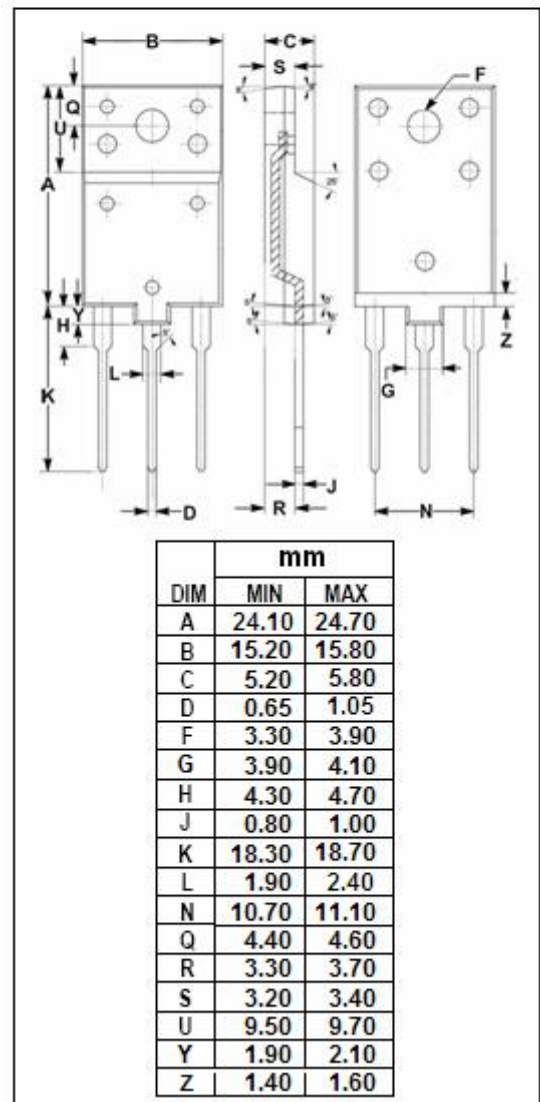
- High Breakdown Voltage
- High Switching Speed
- Low Saturation Voltage
- 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

APPLICATIONS

- Character display horizontal deflection output


ABSOLUTE MAXIMUM RATINGS (T_a=25°C)

SYMBOL	PARAMETER	VALUE	UNIT
V _{CBO}	Collector-Base Voltage	1500	V
V _{CEO}	Collector-Emitter Voltage	600	V
V _{EBO}	Emitter-Base Voltage	5	V
I _C	Collector Current- Continuous	20	A
I _{CM}	Collector Current- Continuous	30	A
I _B	Base Current- Continuous	8	A
P _C	Collector Power Dissipation @ T _C =25°C	70	W
T _J	Junction Temperature	150	°C
T _{stg}	Storage Temperature Range	-55~150	°C



isc Silicon NPN Power Transistor
2SC5516
ELECTRICAL CHARACTERISTICS

 T_c=25°C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{CE(sat)}	Collector-Emitter Saturation Voltage	I _C = 10A; I _B = 2.5A			3.0	V
V _{BE(sat)}	Base-Emitter Saturation Voltage	I _C = 10A; I _B = 2.5A			1.5	V
I _{CBO}	Collector Cutoff Current	V _{CB} = 1500V; I _E = 0			1.0	mA
I _{EBO}	Emitter Cutoff Current	V _{EB} = 6V; I _C = 0			50	uA
h _{FE}	DC Current Gain	I _C = 10A; V _{CE} = 5V	5		10	
f _T	Current-Gain—Bandwidth Product	I _E = 0.1A ; V _{CE} = 10V		30		MHz

Switching times

t _{stg}	Storage Time	I _C = 10A , I _{B1} =2.5A; I _{B2} = -5.0A;			2.7	μ s
t _f	Fall Time				0.2	μ s

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