

isc Silicon NPN Power Transistor

DESCRIPTION

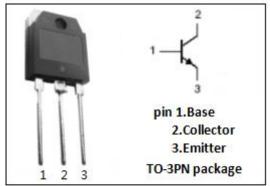
- · High Collector-Base Voltage-
 - : V_{CBO}= 900V(Min)
- · High Switching Speed
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

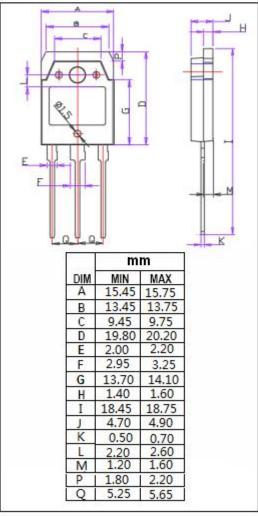
APPLICATIONS

• Designed for horizontal deflection output applications.

ABSOLUTE MAXIMUM RATINGS(Ta=25℃)

SYMBOL	PARAMETER	VALUE	UNIT
V _{CBO}	Collector-Base Voltage	900	V
V _{CEO}	Collector-Emitter Voltage	550	٧
V _{EBO}	Emitter-Base Voltage	9	V
lc	Collector Current-Continuous	5	А
Pc	Collector Power Dissipation @ T _C =25℃	70	W
TJ	Junction Temperature	150	$^{\circ}$ C
T _{stg}	Storage Temperature Range	-55~150	$^{\circ}$ C







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2SC5586

ELECTRICAL CHARACTERISTICS

Tc=25℃ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{CEO}	Collector-Emitter Voltage	I _C = 30mA; I _B = 0	550			V
V _{CE(sat)}	Collector-Emitter Saturation Voltage	I _C = 1.8A; I _B =0.36A			0.5	V
V _{BE(sat)}	Base-Emitter Saturation Voltage	I _C = 1.8A; I _B =0.36A			1.2	V
I _{CBO}	Collector Cutoff Current	V _{CB} = 800V; I _E = 0			100	μА
I _{EBO}	Emitter Cutoff Current	V _{EB} = 9V; I _C = 0			1	mA
h _{FE}	DC Current Gain	I _C = 1.8A; V _{CE} = 4V	10		40	

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