

isc Silicon NPN Power Transistor
2SC5696
DESCRIPTION

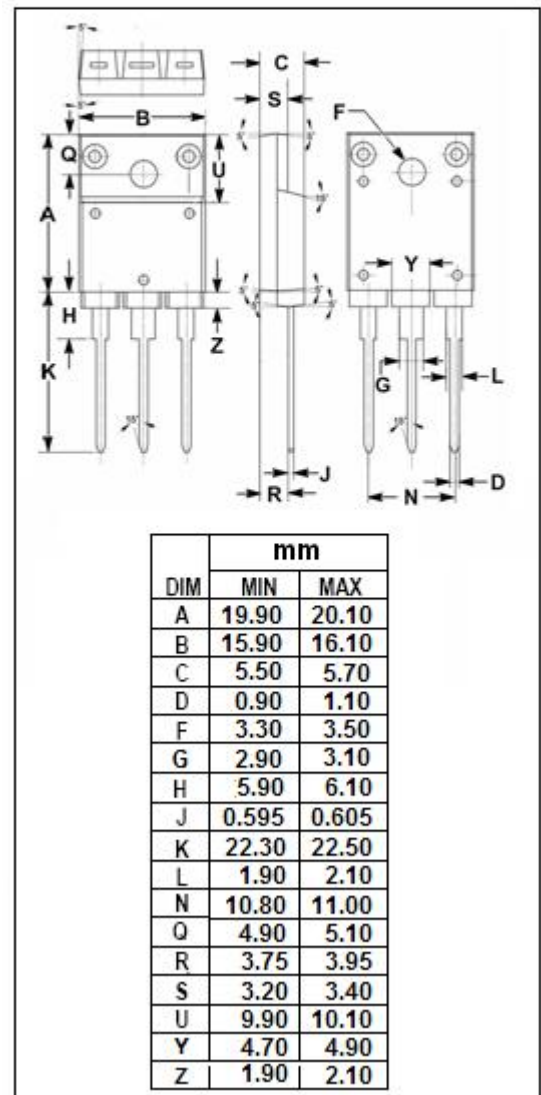
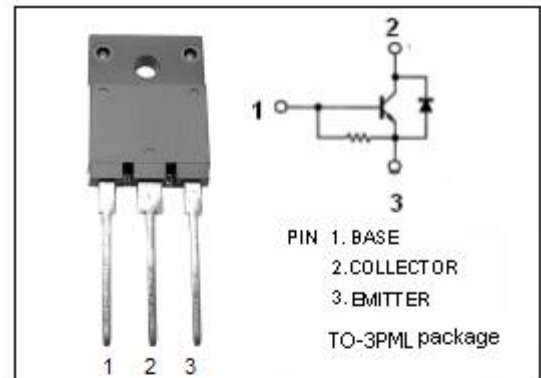
- High speed switching
- Built-in damper diode type
- 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

APPLICATIONS

- Designed for display horizontal deflection output Switching regulator and general purpose

ABSOLUTE MAXIMUM RATINGS(T_a=25°C)

SYMBOL	PARAMETER	VALUE	UNIT
V _{CBO}	Collector-Base Voltage	1600	V
V _{CEO}	Collector-Emitter Voltage	800	V
V _{EBO}	Emitter-Base Voltage	5	V
I _C	Collector Current-Continuous	12	A
I _B	Base Current-Continuous	3	A
P _C	Collector Power Dissipation @ T _C =25°C	85	W
T _J	Junction Temperature	150	°C
T _{stg}	Storage Temperature Range	-55~150	°C



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ELECTRICAL CHARACTERISTICS

T_c=25°C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{(BR)CEO}	Collector-Emitter Breakdown Voltage	I _c =100mA; I _B = 0	800			V
V _{(BR)EBO}	Emitter-Base Breakdown Voltage	I _E =400mA; I _C = 0	6			V
V _{CE(sat)}	Collector-Emitter Saturation Voltage	I _C =7.2A; I _B = 1.8A			3	V
V _{BE(sat)}	Base-Emitter Saturation Voltage	I _C =7.2A; I _B = 1.8A			1.8	V
I _{CBO}	Collector Cutoff Current	V _{CB} = 1500V; I _E = 0			100	μ A
h _{FE-1}	DC Current Gain	I _C = 1A; V _{CE} = 5V	3		11	
h _{FE-2}	DC Current Gain	I _C = 8A; V _{CE} = 5V	4		7	

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