

# SANYO Semiconductors DATA SHEET

## 2SC6096—NPN Epitaxial Planar Silicon Transistor

### **High-Voltage Switching Applications**

#### **Applications**

• DC / DC converter, relay drivers, lamp drivers, motor drivers, inverter.

#### **Features**

- · Adoption of FBET, MBIT process.
- · High current capacitance.
- · Low collector-to-emitter saturation voltage.
- · High-speed switching.
- · High allowable power dissipation.

#### **Specifications**

#### Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Collector-to-Base Voltage	VCBO		120	V
Collector-to-Emitter Voltage	VCES		120	V
Collector-to-Emitter Voltage	VCEO		100	V
Emitter-to-Base Voltage	VEBO		6.5	V
Collector Current	IC		2	Α
Collector Current (Pulse)	ICP		3	Α
Base Current	IΒ		400	mA
Collector Dissipation	PC	Mounted on a ceramic board (250mm <sup>2</sup> X0.8mm)	1.3	W
	PC PC	Tc=25°C	3.5	W
Junction Temperature	Tj		150	°C
Storage Temperature	Tstg		-55 to +150	°C

#### Electrical Characteristics at Ta=25°C

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Offic
Collector Cutoff Current	ICBO	V <sub>CB</sub> =80V, I <sub>E</sub> =0A			1	μΑ
Emitter Cutoff Current	IEBO	V <sub>EB</sub> =4V, I <sub>C</sub> =0A			1	μΑ
DC Current Gain	hFE	V <sub>CE</sub> =5V, I <sub>C</sub> =100mA	300		600	

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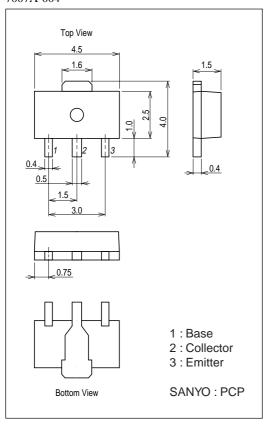
#### 2SC6096

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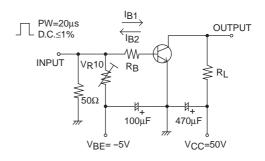
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Oill
Gain-Bandwidth Product	fŢ	V <sub>CE</sub> =10V, I <sub>C</sub> =300mA		300		MHz
Output Capacitance	Cob	V <sub>CB</sub> =10V, f=1MHz		13		pF
Collector-to-Emitter Saturation Voltage	VCE(sat)	IC=1A, IB=100mA		100	150	mV
Base-to-Emitter Saturation Voltage	VBE(sat)	IC=1A, IB=100mA		0.85	1.2	V
Collector-to-Base Breakdown Voltage	V(BR)CBO	I <sub>C</sub> =10μA, I <sub>E</sub> =0A	120			V
Collector-to-Emitter Breakdown Voltage	V(BR)CES	IC=100μA, RBE=0Ω	120			V
Collector-to-Emitter Breakdown Voltage	V(BR)CEO	IC=1mA, RBE=∞	100			V
Emitter-to-Base Breakdown Voltage	V(BR)EBO	I <sub>E</sub> =10μA, I <sub>C</sub> =0A	6.5			V
Turn-ON Time	ton	See specified Test Circuit.		40		ns
Storage Time	tstg	See specified Test Circuit.		1100		ns
Fall Time	tf	See specified Test Circuit.		40		ns

#### **Package Dimensions**

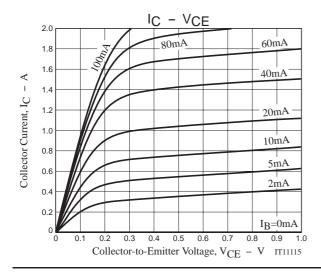
unit : mm 7007A-004

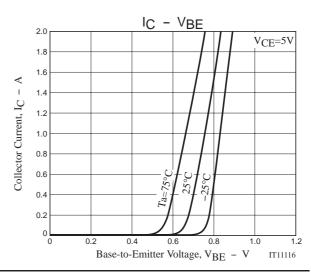


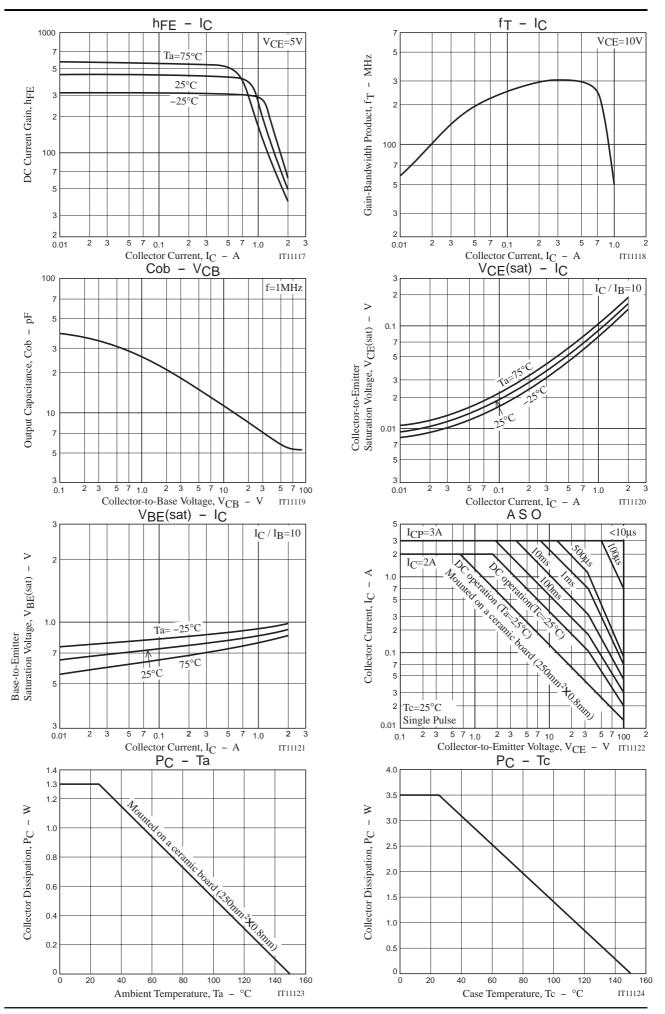
#### **Switching Time Test Circuit**



 $10I_{B1} = -10I_{B2} = I_{C} = 0.5A$ 







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