



SANYO Semiconductors

DATA SHEET

2SC6099 — NPN Epitaxial Planar Silicon Transistor

High-Voltage Switching Applications

Applications

- DC / DC converter, relay drivers, lamp drivers, motor drivers, inverter.

Features

- Adoption of FBET, MBIT process.
- High current capacitance.
- Low collector-to-emitter saturation voltage.
- High-speed switching.
- High allowable power dissipation.

Specifications

Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Collector-to-Base Voltage	V _{CB0}		120	V
Collector-to-Emitter Voltage	V _{CES}		120	V
Collector-to-Emitter Voltage	V _{CEO}		100	V
Emitter-to-Base Voltage	V _{EBO}		6.5	V
Collector Current	I _C		2	A
Collector Current (Pulse)	I _{CP}		3	A
Base Current	I _B		400	mA
Collector Dissipation	P _C		0.8	W
		T _C =25°C	15	W
Junction Temperature	T _J		150	°C
Storage Temperature	T _{stg}		-55 to +150	°C

Electrical Characteristics at Ta=25°C

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Collector Cutoff Current	I _{CBO}	V _{CB} =80V, I _E =0A			1	μA
Emitter Cutoff Current	I _{EBO}	V _{EB} =4V, I _C =0A			1	μA
DC Current Gain	h _{FE}	V _{CE} =5V, I _C =100mA	300		600	

Continued on next page.

■ Any and all SANYO Semiconductor products described or contained herein do not have specifications that can handle applications that require extremely high levels of reliability, such as life-support systems, aircraft's control systems, or other applications whose failure can be reasonably expected to result in serious physical and/or material damage. Consult with your SANYO Semiconductor representative nearest you before using any SANYO Semiconductor products described or contained herein in such applications.

■ SANYO Semiconductor assumes no responsibility for equipment failures that result from using products at values that exceed, even momentarily, rated values (such as maximum ratings, operating condition ranges, or other parameters) listed in products specifications of any and all SANYO Semiconductor products described or contained herein.

SANYO Semiconductor Co., Ltd.

TOKYO OFFICE Tokyo Bldg., 1-10, 1 Chome, Ueno, Taito-ku, TOKYO, 110-8534 JAPAN

2SC6099

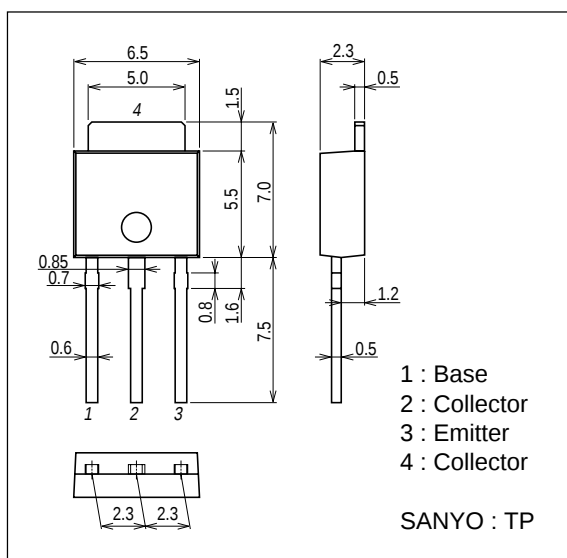
Continued from preceding page.

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Gain-Bandwidth Product	f_T	$V_{CE}=10V, I_C=300mA$		300		MHz
Output Capacitance	C_{ob}	$V_{CB}=10V, f=1MHz$		13		pF
Collector-to-Emitter Saturation Voltage	$V_{CE(sat)}$	$I_C=1A, I_B=100mA$		110	165	mV
Base-to-Emitter Saturation Voltage	$V_{BE(sat)}$	$I_C=1A, I_B=100mA$		0.9	1.2	V
Collector-to-Base Breakdown Voltage	$V_{(BR)CBO}$	$I_C=10\mu A, I_E=0A$	120			V
Collector-to-Emitter Breakdown Voltage	$V_{(BR)CES}$	$I_C=100\mu A, R_{BE}=0\Omega$	120			V
Collector-to-Emitter Breakdown Voltage	$V_{(BR)CEO}$	$I_C=1mA, R_{BE}=\infty$	100			V
Emitter-to-Base Breakdown Voltage	$V_{(BR)EBO}$	$I_E=10\mu A, I_C=0A$	6.5			V
Turn-ON Time	t_{on}	See specified Test Circuit.		40		ns
Storage Time	t_{stg}	See specified Test Circuit.		1100		ns
Fall Time	t_f	See specified Test Circuit.		40		ns

Package Dimensions

unit : mm

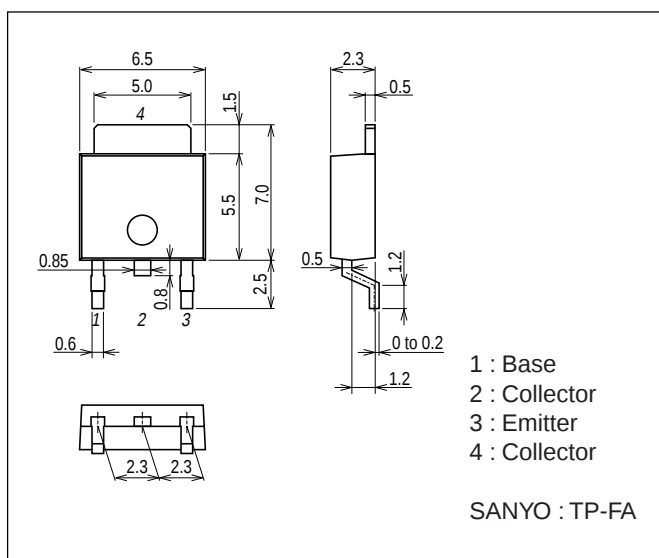
7518-003



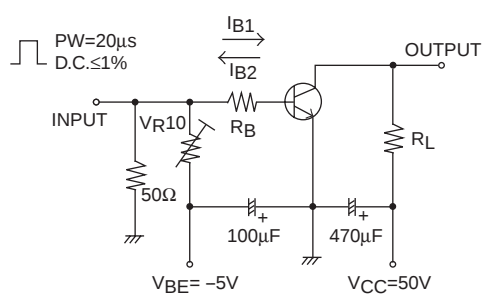
Package Dimensions

unit : mm

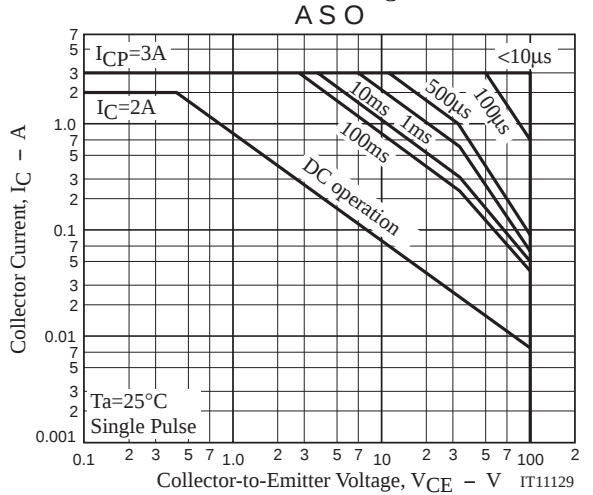
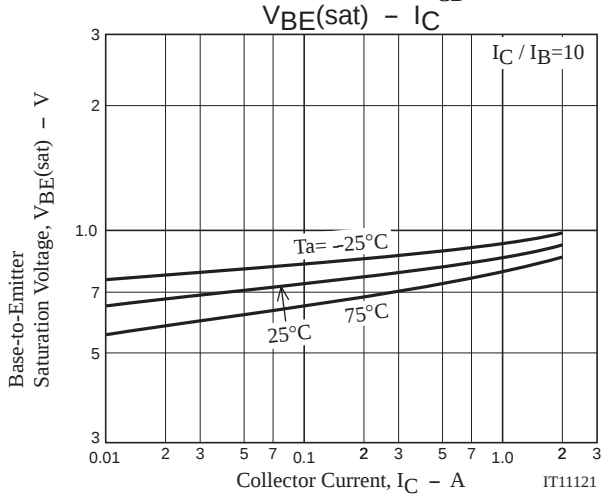
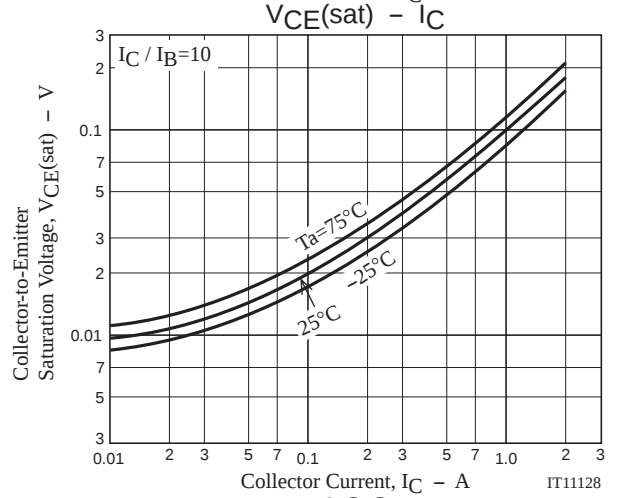
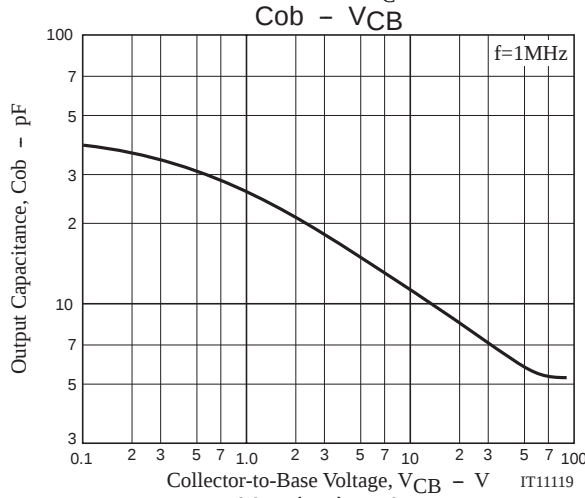
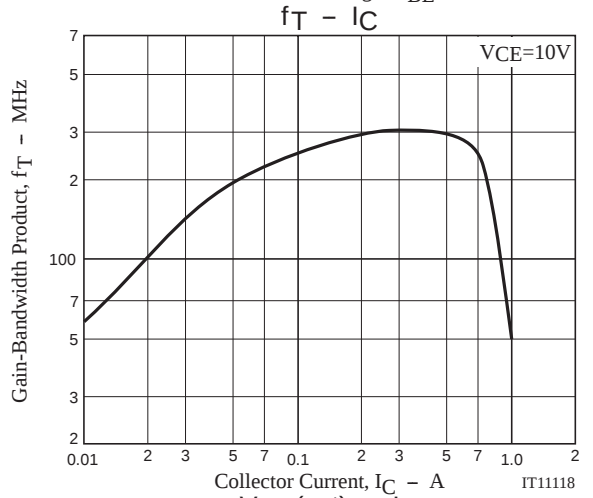
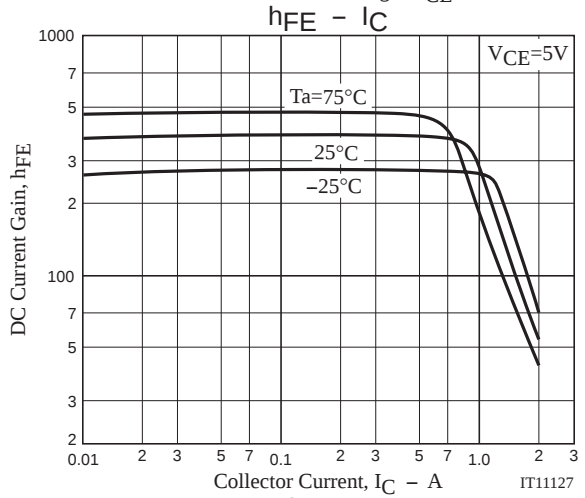
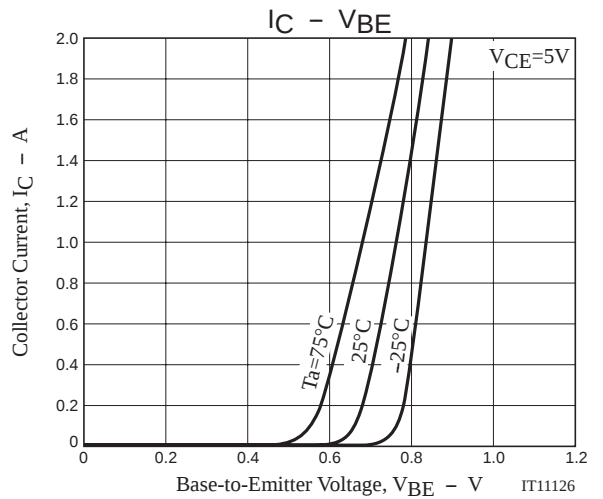
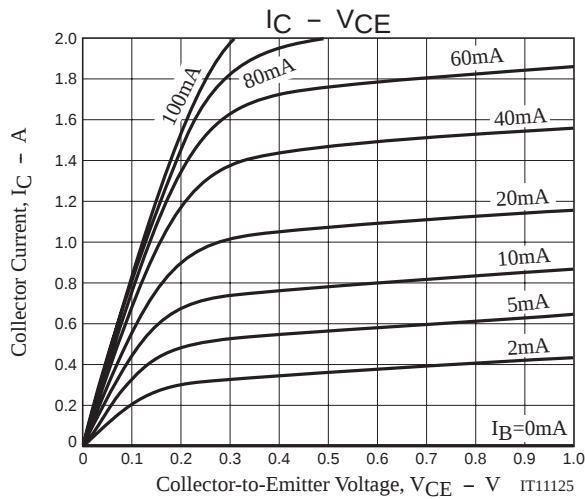
7003-003

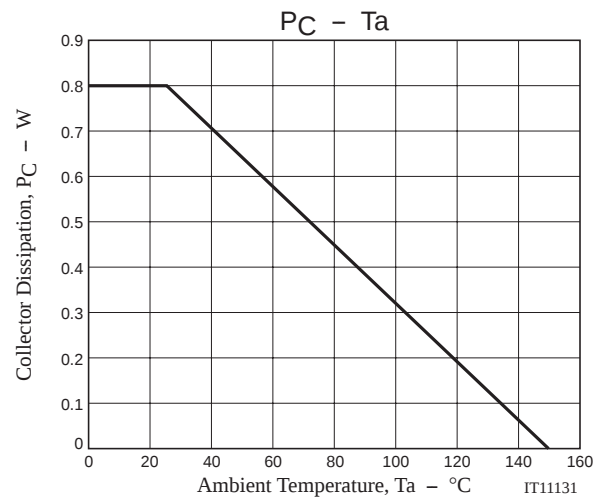
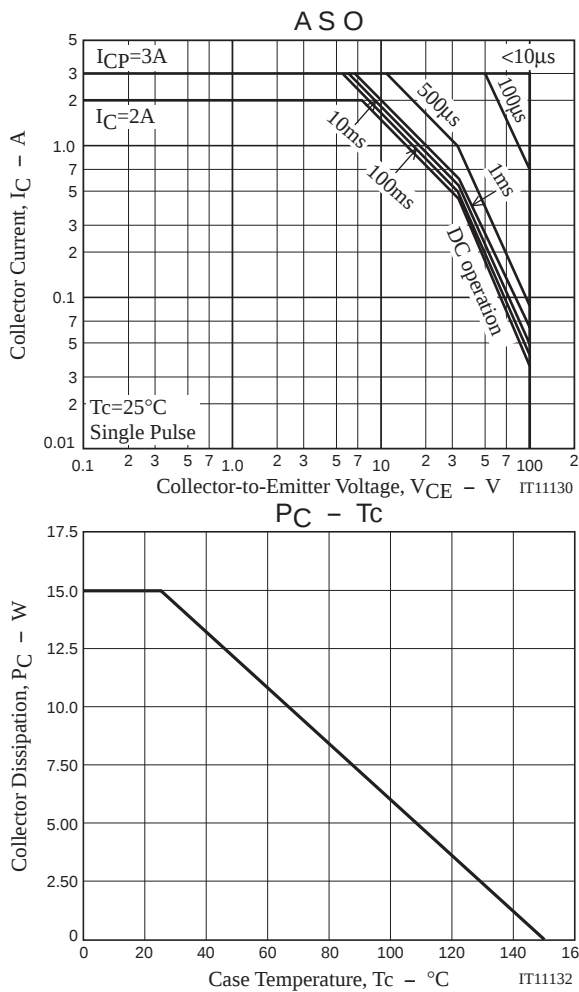


Switching Time Test Circuit



$$10I_{B1} = -10I_{B2} = I_C = 0.5A$$





- Specifications of any and all SANYO Semiconductor products described or contained herein stipulate the performance, characteristics, and functions of the described products in the independent state, and are not guarantees of the performance, characteristics, and functions of the described products as mounted in the customer's products or equipment. To verify symptoms and states that cannot be evaluated in an independent device, the customer should always evaluate and test devices mounted in the customer's products or equipment.
- SANYO Semiconductor Co., Ltd. strives to supply high-quality high-reliability products. However, any and all semiconductor products fail with some probability. It is possible that these probabilistic failures could give rise to accidents or events that could endanger human lives, that could give rise to smoke or fire, or that could cause damage to other property. When designing equipment, adopt safety measures so that these kinds of accidents or events cannot occur. Such measures include but are not limited to protective circuits and error prevention circuits for safe design, redundant design, and structural design.
- In the event that any or all SANYO Semiconductor products (including technical data, services) described or contained herein are controlled under any of applicable local export control laws and regulations, such products must not be exported without obtaining the export license from the authorities concerned in accordance with the above law.
- No part of this publication may be reproduced or transmitted in any form or by any means, electronic or mechanical, including photocopying and recording, or any information storage or retrieval system, or otherwise, without the prior written permission of SANYO Semiconductor Co., Ltd.
- Any and all information described or contained herein are subject to change without notice due to product/technology improvement, etc. When designing equipment, refer to the "Delivery Specification" for the SANYO Semiconductor product that you intend to use.
- Information (including circuit diagrams and circuit parameters) herein is for example only; it is not guaranteed for volume production. SANYO Semiconductor believes information herein is accurate and reliable, but no guarantees are made or implied regarding its use or any infringements of intellectual property rights or other rights of third parties.

This catalog provides information as of June, 2006. Specifications and information herein are subject to change without notice.