RENESAS

SILICON POWER TRANSISTOR

Phase-out/Discontinued

2SD1033

NPN SILICON EPITAXIAL TRANSISTOR

DESCRIPTION

The 2SD1033 is designed for Color TV vertical deflection output, especially in Hybrid Integrated Circuits.

FEATURES

- High Voltage V_{CEO} = 150 V
- Complement to 2SB768

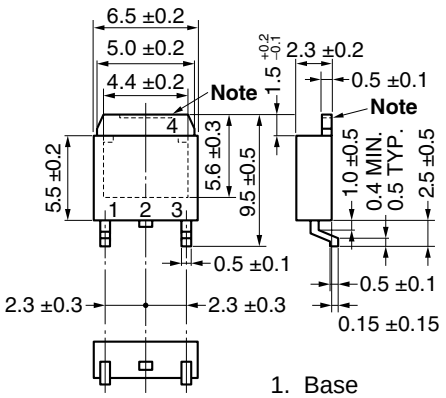
ABSOLUTE MAXIMUM RATINGS (T_A = 25°C)

Collector to Base Voltage	V_{CBO}	200	V
Collector to Emitter Voltage	V_{CEO}	150	V
Emitter to Base Voltage	V_{EBO}	5	V
Collector Current (DC)	$I_{C(DC)}$	2	A
Collector Current (pulse) Note 1	$I_{C(pulse)}$	3	A
Total Power Dissipation (T_A = 25°C) Note 2	P_T	2.0	W
Junction Temperature	T_j	150	°C
Storage Temperature	T_{stg}	−55 to +150	°C

Notes 1. PW ≤ 10 ms, Duty Cycle ≤ 50%

2. When mounted on ceramic substrate of 7.5 cm² × 0.7 mm

<R> **PACKAGE DRAWING (Unit: mm)**



TO-252 (MP-3Z)

1. Base
2. Collector
3. Emitter
4. Collector Fin

Note The depth of notch at the top of the fin is from 0 to 0.2 mm.

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The mark <R> shows major revised points.

The revised points can be easily searched by copying an "<R>" in the PDF file and specifying it in the "Find what:" field.

ELECTRICAL CHARACTERISTICS (T_a = 25 °C)

CHARACTERISTIC	SYMBOL	MIN.	TYP.	MAX.	UNIT	TEST CONDITIONS
Collector Cutoff Current	I_{CBO}			50	μA	V_{CB} = 150 V, I_E = 0
Emitter Cutoff Current	I_{EBO}			50	μA	V_{EB} = 4 V, I_C = 0
DC Current Gain	h_{FE} ***	40	100	200		V_{CE} = 10 V, I_C = 0.4 A
Collector Saturation Voltage	$V_{CE(sat)}$ ***		0.2	1.0	V	I_C = 500 mA, I_B = 50 mA
Gain Bandwidth Product	f_T		10		MHz	V_{CE} = 10 V, I_E = 0.4 A

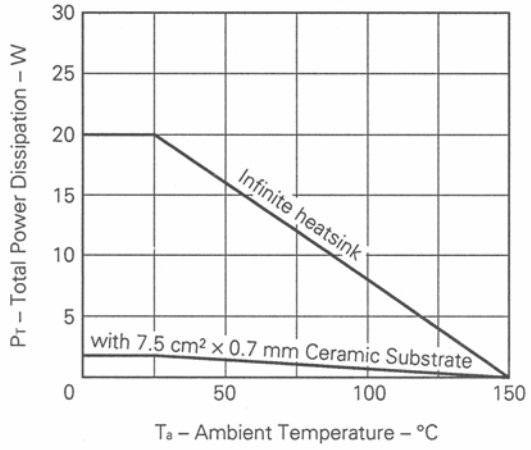
***Pulsed: PW ≦ 350 μs, Duty Cycle ≦ 2 %

h_{FE} Classification

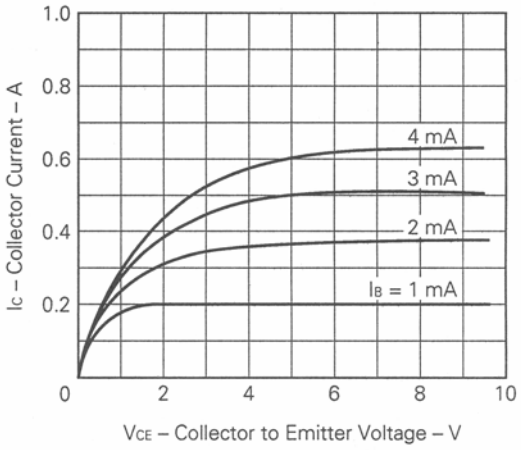
MARKING	M	L	K
h_{FE}	40 to 80	60 to 120	100 to 200

TYPICAL CHARACTERISTICS (T_a = 25 °C)

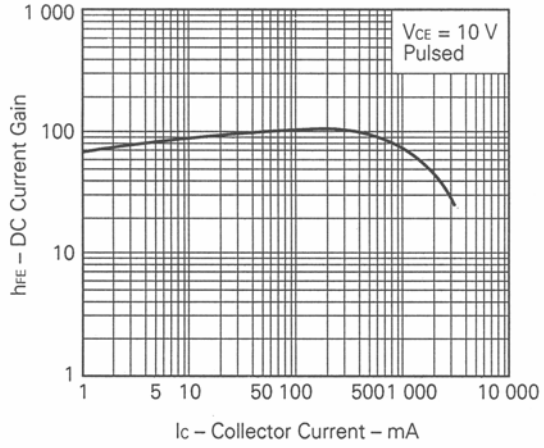
TOTAL POWER DISSIPATION vs. AMBIENT TEMPERATURE



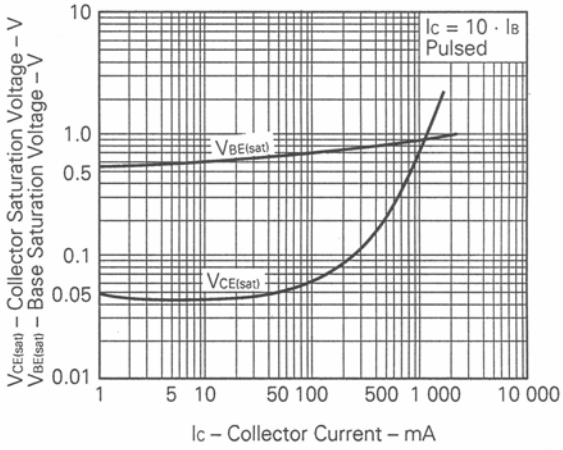
COLLECTOR CURRENT vs. COLLECTOR TO EMITTER VOLTAGE



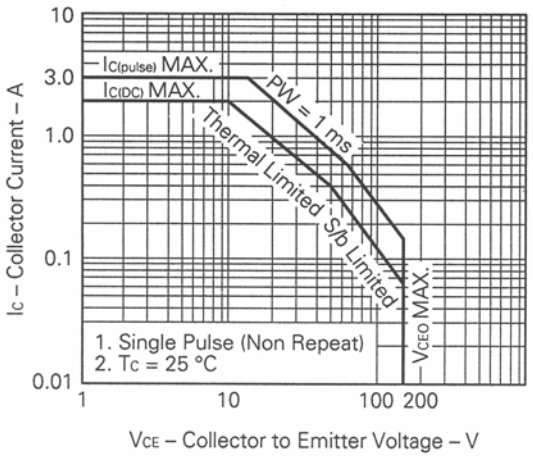
DC CURRENT GAIN vs. COLLECTOR CURRENT



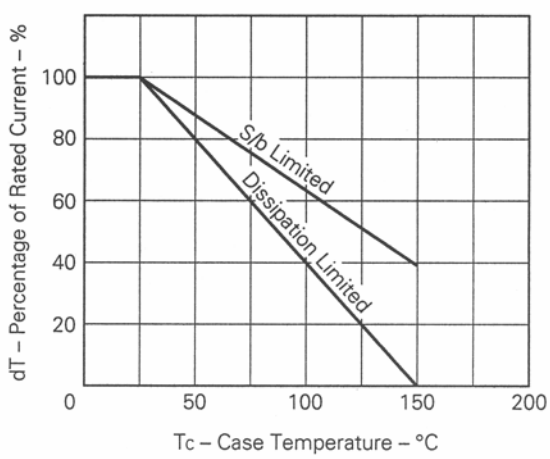
BASE AND COLLECTOR SATURATION VOLTAGE vs. COLLECTOR CURRENT



SAFE OPERATING AREA



DERATING CURVE OF SAFE OPERATING AREA



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