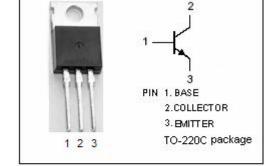


isc Silicon NPN Power Transistor

2SD1163

DESCRIPTION

- Collector Current: I_C= 7A
- · Collector-Emitter BreakdownVoltage-
 - : V_{(BR)CEO}= 120V(Min.)
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

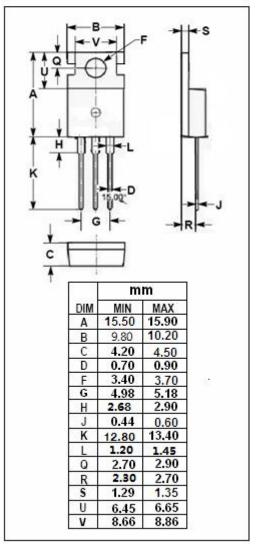


APPLICATIONS

• Designed for TV horizontal deflection applications.

ABSOLUTE MAXIMUM RATINGS(Ta=25℃)

SYMBOL	PARAMETER	VALUE	UNIT
V _{CBO}	Collector-Base Voltage	300	V
V _{CEO}	Collector-Emitter Voltage	120	V
V _{EBO}	Emitter-Base Voltage	6	V
lc	Collector Current-Continuous	7	Α
I _{CM}	Collector Current-Peak	10	Α
I _{C(surge)}	Collector Current-Surge	20	Α
Pc	Total Power Dissipation @ T _C =25°C	40	W
TJ	Junction Temperature	150	$^{\circ}$
T _{stg}	Storage Temperature Range	-55~150	$^{\circ}$ C





isc Silicon NPN Power Transistor

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ELECTRICAL CHARACTERISTICS

T_C=25℃ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _(BR) CEO	Collector-Emitter Breakdown Voltage	I _C = 10mA ; R _{BE} = ∞	120			V
V _{(BR)EBO}	Emitter-Base Breakdown Voltage	I _E = 1mA ; I _C = 0	6			V
V _{CE} (sat)	Collector-Emitter Saturation Voltage	I _C = 5A; I _B = 0.5A			2.0	V
V _{BE} (sat)	Base-Emitter Saturation Voltage	I _C = 5A; I _B = 0.5A			1.2	V
I _{CBO}	Collector Cutoff Current	V _{CB} = 300V ; I _E = 0			5	mA
h _{FE}	DC Current Gain	I _C = 5A ; V _{CE} = 5V	25			
t _f	Fall Time	I _{CP} = 3.5A; I _{B1} = 0.45A			0.5	μ S

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