

# 2SD1164-Z

R07DS0254EJ0400

Rev.4.00

## SILICON POWER TRANSISTOR

Feb 24, 2011

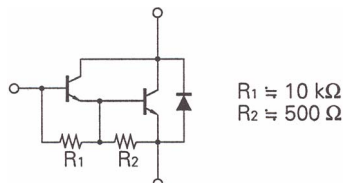
### DESCRIPTION

The 2SD1164-Z is designed for Low Frequency Amplifier and Switching, especially in Hybrid Integrated Circuits.

### FEATURES

- High  $h_{FE} = 2\,000$  to  $30\,000$

<R>



### ABSOLUTE MAXIMUM RATINGS ( $T_A = 25^\circ\text{C}$ )

CHARACTERISTICS	SYMBOL	RATINGS	UNIT
Collector to Base Voltage	$V_{CBO}$	150	V
Collector to Emitter Voltage	$V_{CEO}$	60	V
Base to Emitter Voltage	$V_{EBO}$	8.0	V
Collector Current (DC)	$I_{C(DC)}$	2	A
Collector Current (pulse) <sup>Note 1</sup>	$I_{C(pulse)}$	4	A
Total Power Dissipation ( $T_A = 25^\circ\text{C}$ ) <sup>Note 2</sup>	$P_T$	2.0	W
Junction Temperature	$T_j$	150	$^\circ\text{C}$
Storage Temperature	$T_{stg}$	-55 to +150	$^\circ\text{C}$

**Notes** 1.  $PW \leq 10\text{ ms}$ , Duty Cycle  $\leq 50\%$

2. When mounted on ceramic substrate of  $7.5\text{ cm}^2 \times 0.7\text{ mm}$

The mark <R> shows major revised points.

The revised points can be easily searched by copying an "<R>" in the PDF file and specifying it in the "Find what:" field.

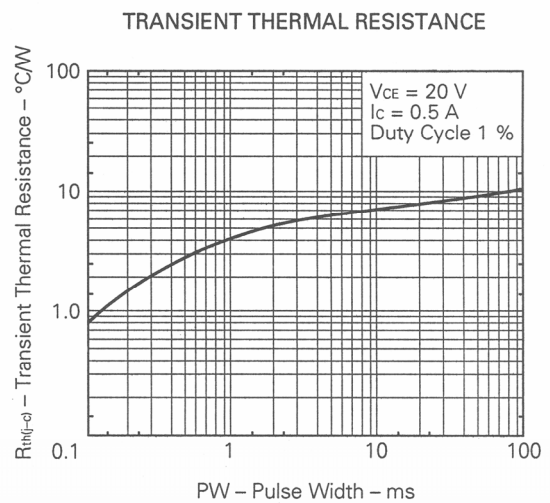
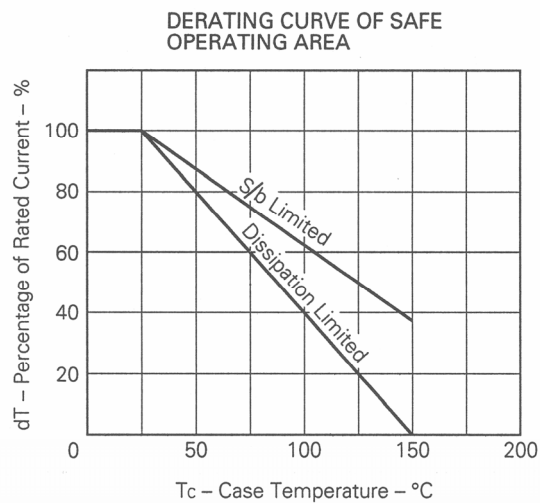
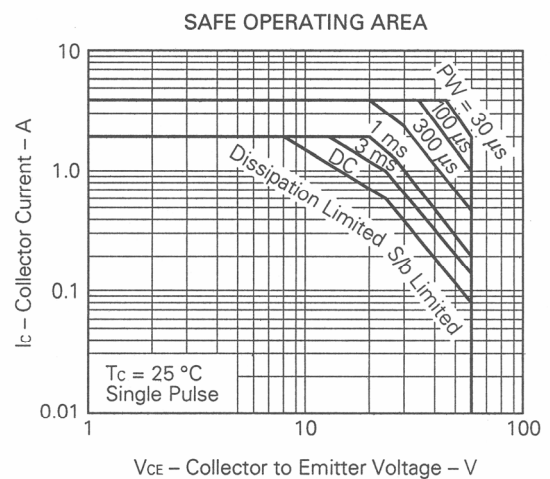
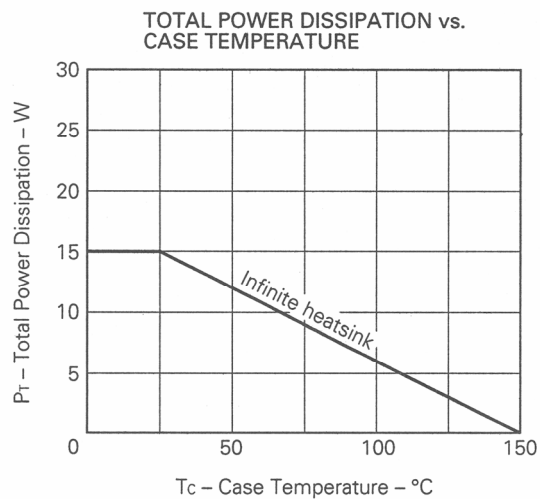
ELECTRICAL CHARACTERISTICS ( $T_a = 25\text{ }^{\circ}\text{C}$ )

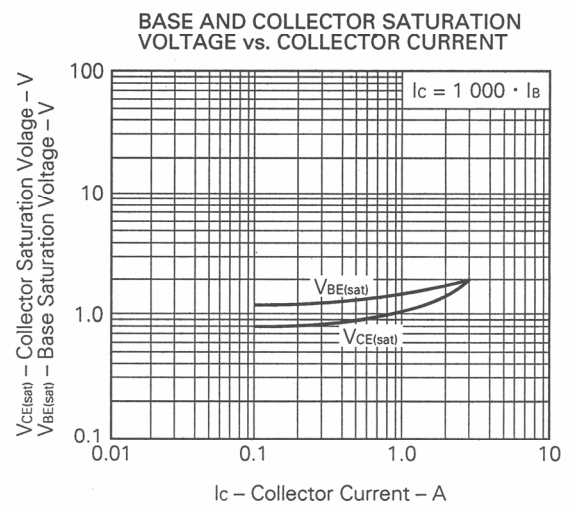
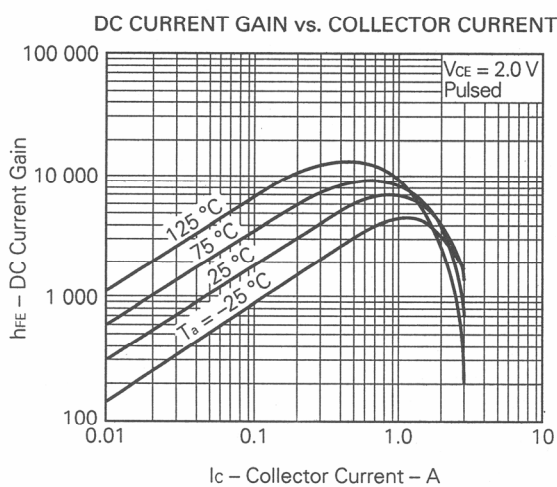
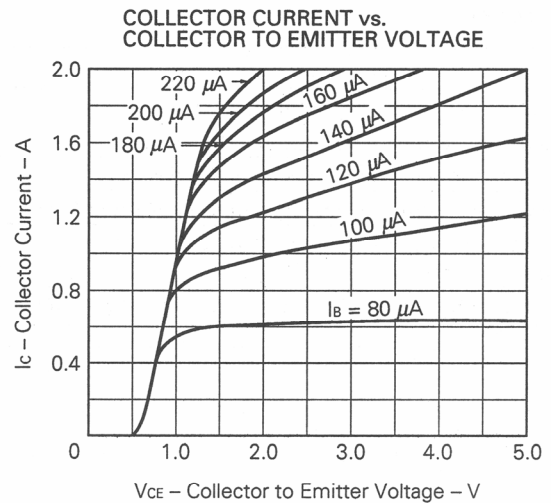
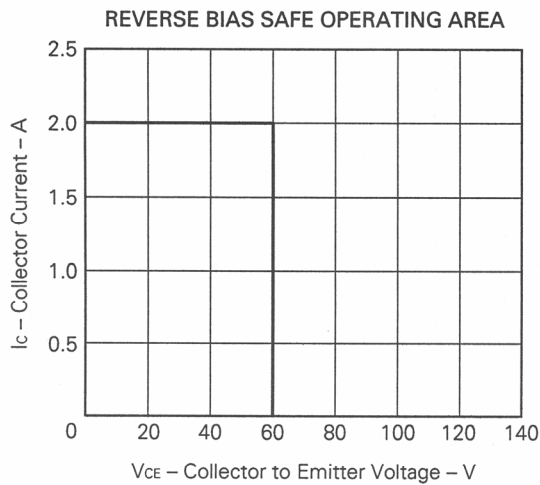
CHARACTERISTIC	SYMBOL	MIN.	TYP.	MAX.	UNIT	TEST CONDITIONS
Collector Cutoff Current	$I_{CBO}$			10	$\mu\text{A}$	$V_{CB} = 60\text{ V}$ , $I_E = 0$
Emitter Cutoff Current	$I_{EBO}$			1.0	mA	$V_{EB} = 5.0\text{ V}$ , $I_C = 0$
DC Current Gain	$h_{FE1}^*$	1 000				$V_{CE} = 2.0\text{ V}$ , $I_C = 0.5\text{ A}$
DC Current Gain	$h_{FE2}^*$	2 000		30 000		$V_{CE} = 2.0\text{ V}$ , $I_C = 1.0\text{ A}$
Collector Saturation Voltage	$V_{CE(sat)}^*$			1.5	V	$I_C = 1.0\text{ A}$ , $I_B = 1.0\text{ mA}$
Base Saturation Voltage	$V_{BE(sat)}^*$			2.0	V	$I_C = 1.0\text{ A}$ , $I_B = 1.0\text{ mA}$
Turn-on Time	$t_{on}$		0.5		$\mu\text{s}$	$I_C = 1.0\text{ A}$ , $I_{B1} = -I_{B2} = 1.0\text{ mA}$ $V_{CC} = 50\text{ V}$ , $R_L = 50\text{ }\Omega$
Storage Time	$t_{stg}$		1.0		$\mu\text{s}$	
Fall Time	$t_f$		1.0		$\mu\text{s}$	

\*Pulsed:  $PW \leq 350\text{ }\mu\text{s}$ , Duty Cycle  $\leq 2\%$

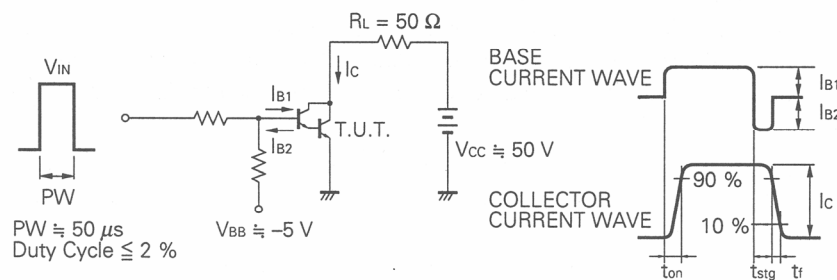
## hFE Classification

MARKING	M	L	K
$h_{FE2}$	2 000 to 5 000	4 000 to 10 000	8 000 to 30 000

TYPICAL CHARACTERISTICS ( $T_a = 25\text{ }^{\circ}\text{C}$ )

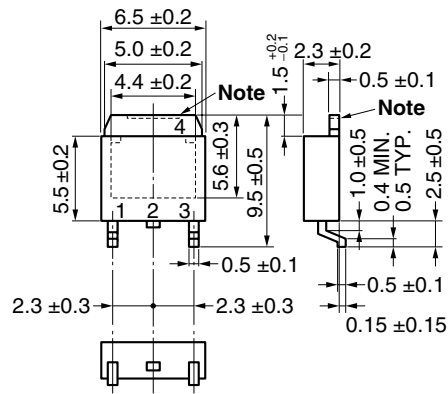


### SWITCHING TIME ( $t_{on}$ , $t_{stg}$ , $t_f$ ) TEST CIRCUIT



PACKAGE DRAWING (Unit: mm)

TO-252 (MP-3Z)



1. Base
2. Collector
3. Emitter
4. Collector Fin

**Note** The depth of notch at the top of the fin is from 0 to 0.2 mm.

<b>Revision History</b>	<b>2SD1164-Z Data Sheet</b>
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Rev.	Date	Description	
		Page	Summary
–	Jul 2006	–	Previous No. : D18286EJ3V0DS00
4.00	Feb 24, 2011	p.1	Modification of equivalent circuit

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