

isc Silicon NPN Power Transistor

2SD1263

DESCRIPTION

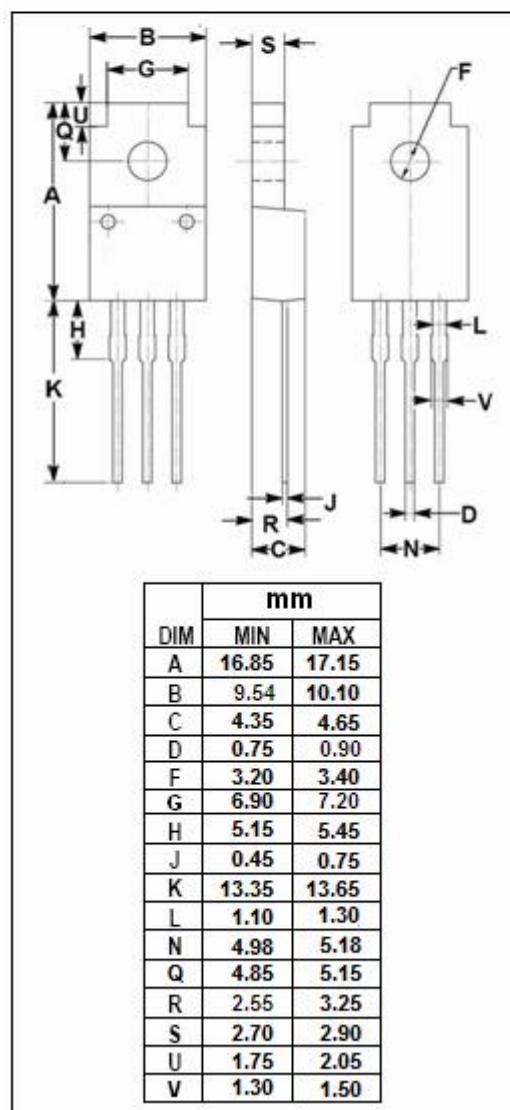
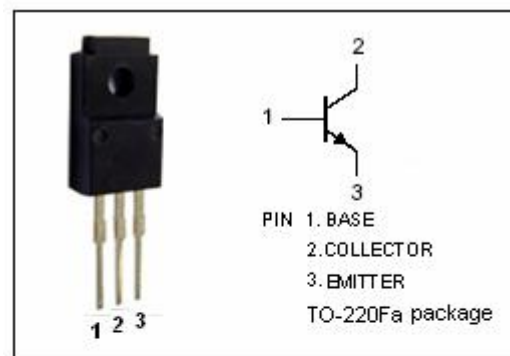
- Collector-Emitter Breakdown Voltage
: $V_{(BR)CEO} = 250V(\text{Min})$
- High Collector Power Dissipation
- 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

APPLICATIONS

- Designed for power amplification applications.

ABSOLUTE MAXIMUM RATINGS($T_a=25^\circ\text{C}$)

SYMBOL	PARAMETER	VALUE	UNIT
V_{CBO}	Collector-Base Voltage	350	V
V_{CEO}	Collector-Emitter Voltage	250	V
V_{EBO}	Emitter-Base Voltage	5	V
I_C	Collector Current-Continuous	0.75	A
I_{CM}	Collector Current-Peak	1.5	A
P_C	Total Power Dissipation @ $T_a=25^\circ\text{C}$	2	W
	Total Power Dissipation @ $T_c=25^\circ\text{C}$	35	
T_J	Junction Temperature	150	$^\circ\text{C}$
T_{stg}	Storage Temperature Range	-55~150	$^\circ\text{C}$



isc Silicon NPN Power Transistor**2SD1263****ELECTRICAL CHARACTERISTICS****T_C=25°C unless otherwise specified**

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{(BR)CEO}	Collector-Emitter Breakdown Voltage	I _C = 30mA; I _B = 0	250			V
V _{(BR)CBO}	Collector-Base Breakdown Voltage	I _C = 100 μ A; I _E = 0	350			V
V _{(BR)EBO}	Emitter-Base Breakdown Voltage	I _E = 100 μ A; I _C = 0	5			V
V _{CE(sat)}	Collector-Emitter Saturation Voltage	I _C = 1A; I _B = 0.2A			1.0	V
V _{BE(on)}	Base-Emitter On Voltage	I _C = 1A; V _{CE} = 10V			1.5	V
I _{CBO}	Collector Cutoff Current	V _{CB} = 350V; I _E = 0			100	μ A
I _{EBO}	Emitter Cutoff Current	V _{EB} = 5V; I _C = 0			100	μ A
h _{FE-1}	DC Current Gain	I _C = 0.3A; V _{CE} = 10V	70		250	
h _{FE-2}	DC Current Gain	I _C = 1A; V _{CE} = 10V	10			
f _T	Current-Gain—Bandwidth Product	I _C = 0.5A; V _{CE} = 5V		30		MHz

◆ **h_{FE-1} Classifications**

Q	P
70-150	120-250

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