Silicon NPN Power Transistors 2SD1263 2SD1263A

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DESCRIPTION

·With TO-220Fa package

·High breakdown voltalge

APPLICATIONS

·For power amplification

PINNING

PIN	DESCRIPTION
1	Base
2	Collector
3	Emitter

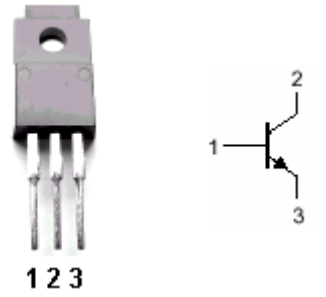

Fig.1 simplified outline (TO-220Fa) and symbol

Absolute maximum ratings(Ta=25□)

SYMBOL	PARAMETER		CONDITIONS	VALUE	UNIT
V_{CBO}	Collector-base voltage	2SD1263	Open emitter	350	V
		2SD1263A		400	
V_{CEO}	Collector-emitter voltage	2SD1263	Open base	250	V
		2SD1263A		300	
V_{EBO}	Emitter-base voltage		Open collector	5	V
I_C	Collector current			0.75	A
I_{CM}	Collector current-peak			1.5	A
P_C	Collector power dissipation		T_a=25□	2	W
			T_C=25□	35	
T_j	Junction temperature			150	□
T_{stg}	Storage temperature			-55~150	□

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CHARACTERISTICS

Tj=25□ unless otherwise specified

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SYMBOL	PARAMETER		CONDITIONS	MIN	TYP.	MAX	UNIT
V_{CEO}	Collector-emitter voltage	2SD1263	I_C=30mA ,I_B=0	250			V
		2SD1263A		300			
V_{CEsat}	Collector-emitter saturation voltage		I_C=1A, I_B=0.2A			1.0	V
V_{BE}	Base-emitter voltage		I_C=1A ; V_{CE}=10V			1.5	V
I_{EBO}	Emitter cut-off current		V_{EB}=5V; I_C=0			1.0	mA
I_{CEO}	Collector cut-off current	2SD1263	V_{CE}=150V; I_B=0			1.0	mA
		2SD1263A	V_{CE}=200V; I_B=0			1.0	mA
I_{CES}	Collector cut-off current	2SD1263	V_{CE}=350V; V_{BE}=0			1.0	mA
		2SD1263A	V_{CE}=400V; V_{BE}=0			1.0	mA
h_{FE-1}	DC current gain		I_C=0.3A ; V_{CE}=10V	70		250	
h_{FE-2}	DC current gain		I_C=1A ; V_{CE}=10V	10			
f_T	Transition frequency		I_C=0.5A; V_{CE}=5V,f=10MHz		30		MHz
Switching times							
t_{on}	Turn-on time		I_C=1A;I_{B1}=-I_{B2}=0.1A V_{CC}=50V		0.5		μs
t_s	Storage time				2		μs
t_f	Fall time				0.5		μs

◆ **h_{FE-1} Classifications**

Q	P
70-150	120-250

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PACKAGE OUTLINE

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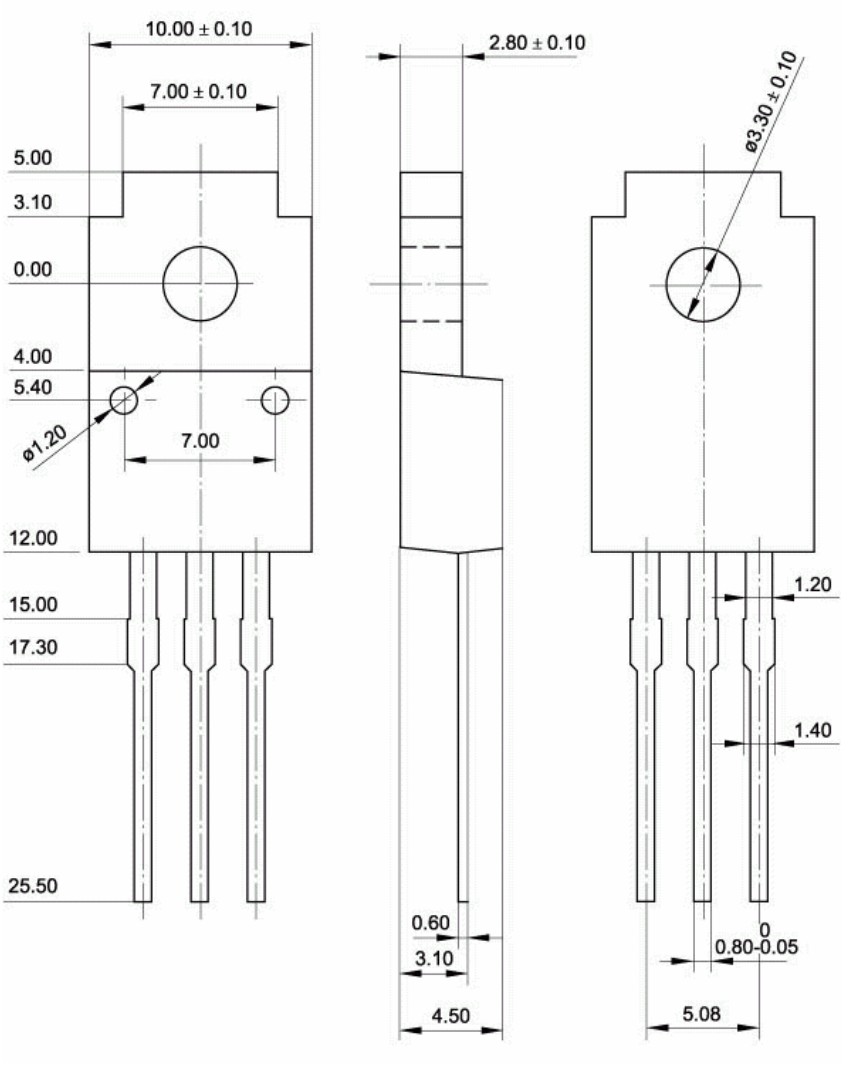

Fig.2 Outline dimensions (unindicated tolerance: ±0.15 mm)

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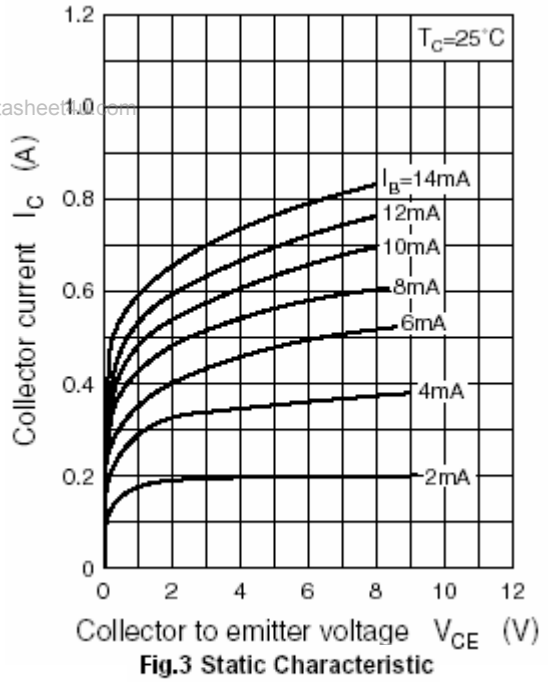

Fig.3 Static Characteristic

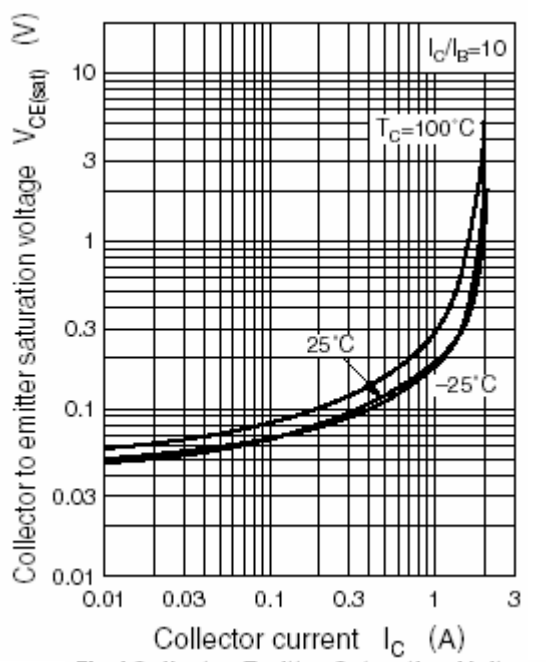

Fig.4 Collector-Emitter Saturation Voltage

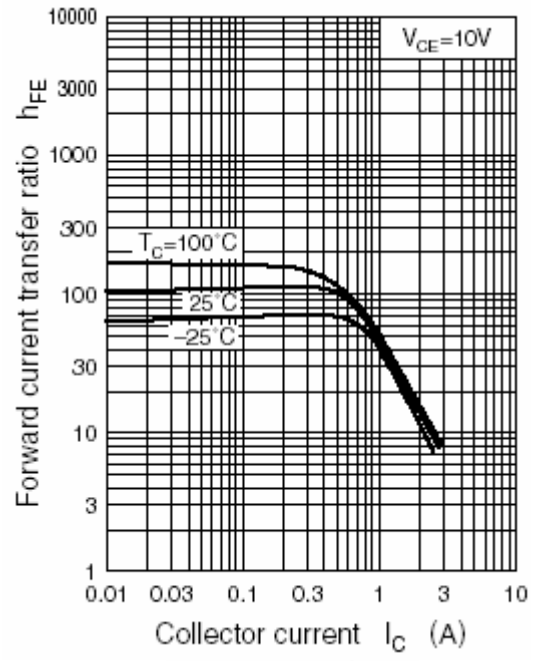

Fig.5 DC current Gain

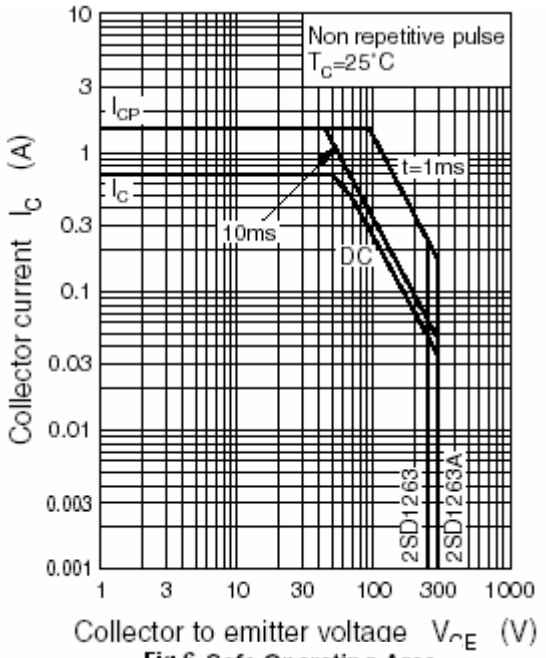

Fig.6 Safe Operating Area

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