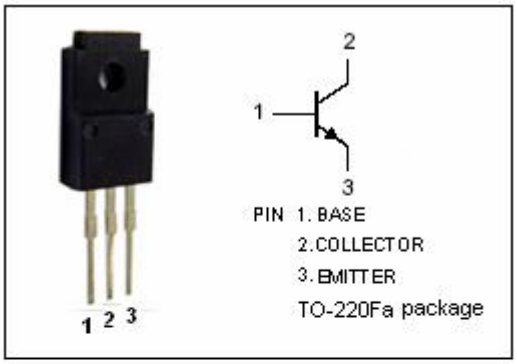
isc Silicon NPN Power Transistor — 2SD1355



DESCRIPTION

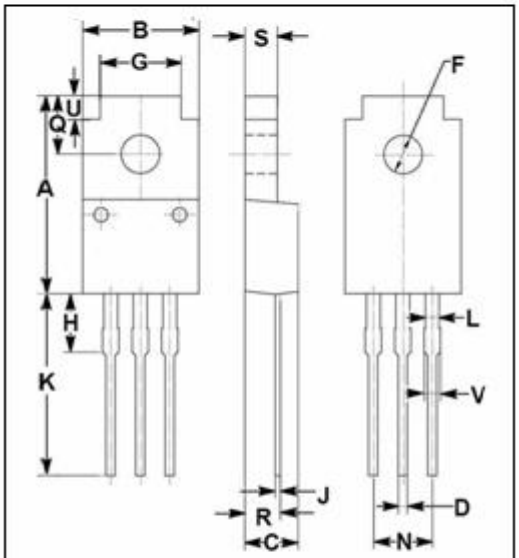
- Low Collector Saturation Voltage
  : $V_{CE(sat)}$= 2.0V(Max)@ I_C= 4A
- Collector-Emitter Breakdown Voltage-
  : $V_{(BR)CEO}$= 100V (Min)
- Complement to Type 2SB995
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

APPLICATIONS

- Power amplifier applications.
- Recommended for 30W high-fidelity audio frequency amplifier output stage.

ABSOLUTE MAXIMUM RATINGS(T_a=25℃)

SYMBOL	PARAMETER	VALUE	UNIT
V_{CBO}	Collector-Base Voltage	100	V
V_{CEO}	Collector-Emitter Voltage	100	V
V_{EBO}	Emitter-Base Voltage	5	V
I_C	Collector Current-Continuous	5	A
I_B	Base Current-Continuous	0.5	A
P_C	Collector Power Dissipation @ T_C=25℃	40	W
T_J	Junction Temperature	150	℃
T_{stg}	Storage Temperature Range	-55~150	℃



DIM	mm MIN	mm MAX
A	16.85	17.15
B	9.54	10.10
C	4.35	4.65
D	0.75	0.90
F	3.20	3.40
G	6.90	7.20
H	3.80	4.20
J	0.45	0.75
K	13.35	13.80
L	1.10	1.30
N	4.98	5.18
Q	4.85	5.15
R	2.55	3.25
S	2.70	2.90
U	1.75	2.05
V	1.30	1.50

isc Silicon NPN Power Transistor 2SD1355

ELECTRICAL CHARACTERISTICS

T_C=25℃ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
$V_{(BR)CEO}$	Collector-Emitter Breakdown Voltage	I_C= 10mA; I_B= 0	100			V
$V_{CE(sat)}$	Collector-Emitter Saturation Voltage	I_C= 4A; I_B= 0.4A			2.0	V
$V_{BE(on)}$	Base-Emitter On Voltage	I_C= 1A; V_{CE}= 5V			1.5	V
I_{CBO}	Collector Cutoff Current	V_{CB}= 100V; I_E= 0			100	μA
I_{EBO}	Emitter Cutoff Current	V_{EB}= 5V; I_C= 0			100	μA
h_{FE-1}	DC Current Gain	I_C= 1A; V_{CE}= 5V	40		240	
h_{FE-2}	DC Current Gain	I_C= 4A; V_{CE}= 5V	20			
f_T	Current-Gain—Bandwidth Product	I_C= 1A; V_{CE}= 5V		12		MHz
C_{OB}	Output Capacitance	I_E= 0; V_{CB}= 10V; f_{test}= 1MHz		100		pF

◆ **h_{FE-1} Classifications**

R	O	Y
40-80	70-140	120-240