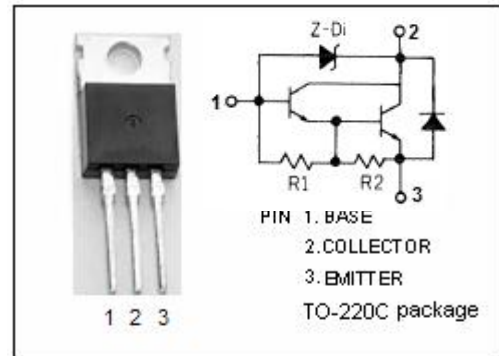


isc Silicon NPN Darlington Power Transistor
2SD1393
DESCRIPTION

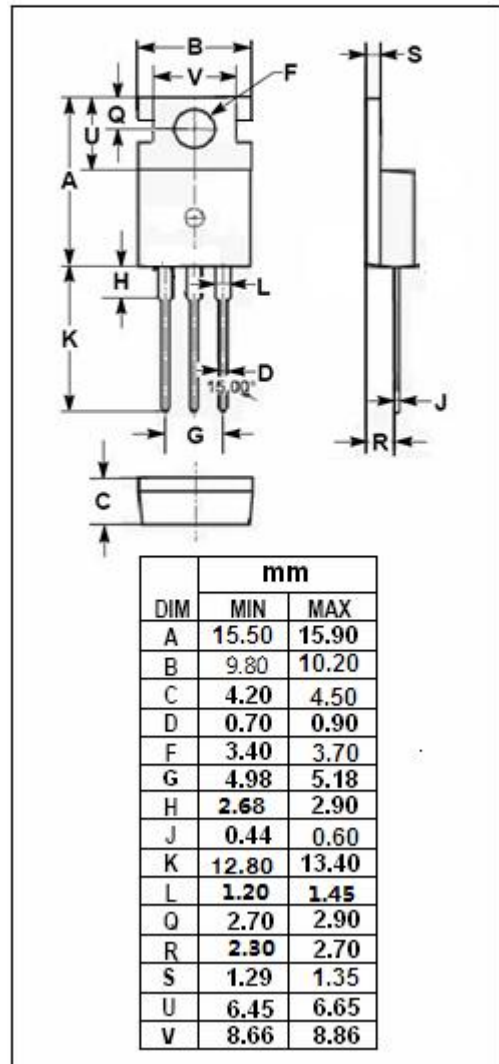
- Collector-Emitter Breakdown Voltage-
: $V_{(BR)CEO} = 50V(\text{Min})$
- High DC Current Gain
: $h_{FE} = 1000(\text{Min}) @ I_C = 0.8A$
- Low Saturation Voltage
- 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation


APPLICATIONS

- Designed for general purpose amplifier and low speed switching applications.

ABSOLUTE MAXIMUM RATINGS($T_a=25^\circ\text{C}$)

SYMBOL	PARAMETER	VALUE	UNIT
V_{CBO}	Collector-Base Voltage	50	V
V_{CEO}	Collector-Emitter Voltage	50	V
V_{EBO}	Emitter-Base Voltage	6	V
I_C	Collector Current-Continuous	1.5	A
I_{CP}	Collector Current-Peak	3	A
P_C	Collector Power Dissipation @ $T_C=25^\circ\text{C}$	30	W
T_J	Junction Temperature	150	$^\circ\text{C}$
T_{stg}	Storage Temperature Range	-55~150	$^\circ\text{C}$



isc Silicon NPN Darlington Power Transistor

2SD1393

ELECTRICAL CHARACTERISTICS

 $T_C=25^\circ\text{C}$ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
$V_{CE(SUS)}$	Collector-Emitter Sustaining Voltage	$I_C=10\text{mA}$, $I_B=0$	50			V
$V_{CE(sat)}$	Collector-Emitter Saturation Voltage	$I_C=1.5\text{A}$, $I_B=3\text{mA}$			2.0	V
$V_{BE(on)}$	Base-Emitter On Voltage	$I_C=1.5\text{A}$; $V_{CE}=3\text{V}$			2.5	V
I_{CBO}	Collector Cutoff Current	$V_{CB}=50\text{V}$, $I_E=0$			0.1	mA
I_{CEO}	Collector Cutoff Current	$V_{CE}=50\text{V}$, $I_B=0$			1.0	mA
I_{EBO}	Emitter Cutoff Current	$V_{EB}=5\text{V}$; $I_C=0$			3.0	mA
h_{FE-1}	DC Current Gain	$I_C=0.8\text{A}$; $V_{CE}=3\text{V}$	1000		15000	
h_{FE-2}	DC Current Gain	$I_C=1.5\text{A}$; $V_{CE}=3\text{V}$	500			

Switching Times

t_{on}	Turn-on Time	$I_C=1.5\text{A}$; $I_{B1}=I_{B2}=3\text{mA}$			1.5	μs
t_s	Storage Time				6.0	μs
t_f	Fall Time				4.5	μs

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