

isc Silicon NPN Darlington Power Transistor

2SD1540

DESCRIPTION

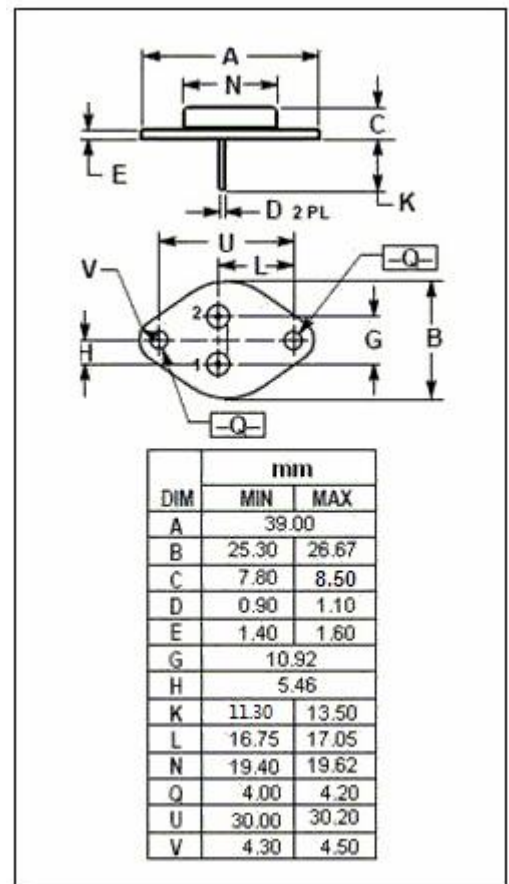
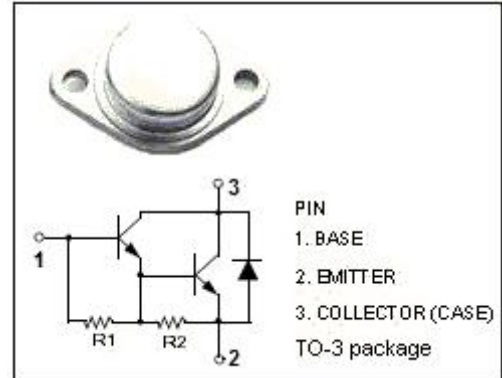
- High DC current gain-
 $h_{FE} = 800$ (Min) @ $I_C = 0.5A$
- Collector-Emitter Breakdown Voltage-
 $V_{(BR)CEO} = 100V$ (Min)
- Fast Switching Speed
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

APPLICATIONS

- Designed for general purpose power amplifier and switching applications.

ABSOLUTE MAXIMUM RATINGS($T_C=25^\circ C$)

SYMBOL	PARAMETER	VALUE	UNIT
V_{CBO}	Collector-Base Voltage	100	V
V_{CEO}	Collector-Emitter Voltage	100	V
V_{EBO}	Emitter-Base Voltage	5	V
I_C	Collector Current -Continuous	6	A
I_B	Base Current	0.2	A
P_C	Collector Power Dissipation@ $T_C=25^\circ C$	60	W
T_J	Junction Temperature	150	$^\circ C$
T_{stg}	Storage Temperature	-65~150	$^\circ C$



isc Silicon NPN Darlington Power Transistor**2SD1540****ELECTRICAL CHARACTERISTICS** $T_C=25^{\circ}\text{C}$ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	MAX	UNIT
$V_{(BR)CEO}$	Collector-Emitter Breakdown Voltage	$I_C=30\text{mA}; I_B=0$	100		V
$V_{(BR)EBO}$	Emitter-Base Breakdown Voltage	$I_E=2\text{mA}; I_C=0$	5		V
$V_{CE(sat)}$	Collector-Emitter Saturation Voltage	$I_C=4\text{A}; I_B=16\text{mA}$		2.0	V
$V_{BE(on)}$	Base-Emitter On voltage	$I_C=4\text{A}; V_{CE}=4\text{V}$		2.5	V
I_{CEO}	Collector Cutoff current	$V_{CE}=100\text{V}; I_B=0$		1.0	mA
I_{CBO}	Collector Cutoff current	$V_{CB}=100\text{V}; I_E=0$		0.1	mA
I_{EBO}	Emitter Cut-off current	$V_{EB}=5\text{V}; I_C=0$		2.0	mA
h_{FE-1}	DC Current Gain	$I_C=0.5\text{A}; V_{CE}=5\text{V}$	800		20000
h_{FE-2}	DC Current Gain	$I_C=4\text{A}; V_{CE}=3\text{V}$	500		

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