

isc Silicon NPN Power Transistor
2SD155
DESCRIPTION

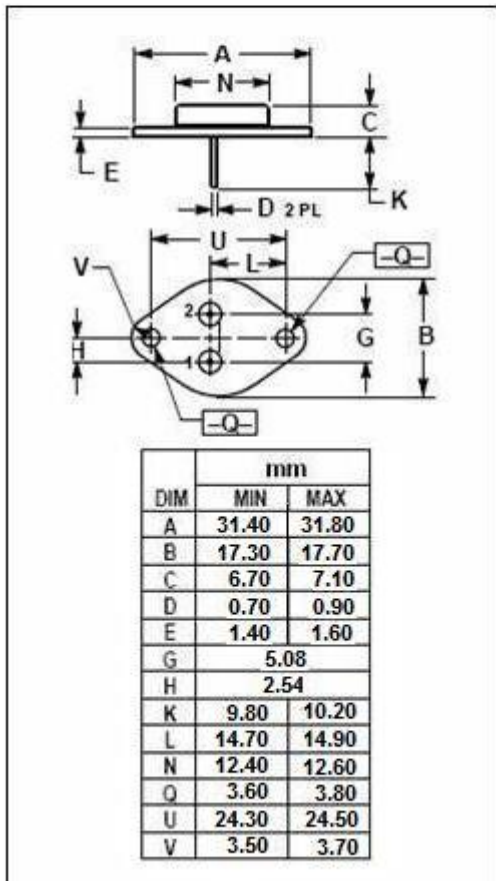
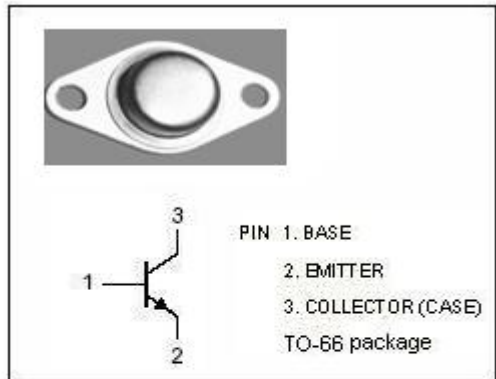
- Low Collector Saturation Voltage
: $V_{CE(sat)} = 1.0V(\text{Max}) @ I_C = 2A$
- Collector-Emitter Breakdown Voltage-
: $V_{(BR)CEO} = 80V(\text{Min})$
- Wide Area of Safe Operation
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

APPLICATIONS

- Designed for low frequency power amplifier applications.

ABSOLUTE MAXIMUM RATINGS($T_a = 25^\circ\text{C}$)

SYMBOL	PARAMETER	VALUE	UNIT
V_{CBO}	Collector-Base Voltage	80	V
V_{CEO}	Collector-Emitter Voltage	80	V
V_{EBO}	Emitter-Base Voltage	5	V
I_C	Collector Current-Continuous	3.0	A
I_{CM}	Collector Current-Peak	5.0	A
I_B	Base Current	1.0	A
P_C	Collector Power Dissipation@ $T_C = 25^\circ\text{C}$	25	W
T_J	Junction Temperature	150	$^\circ\text{C}$
T_{stg}	Storage Temperature	-65~150	$^\circ\text{C}$



isc Silicon NPN Power Transistor**2SD155****ELECTRICAL CHARACTERISTICS**T_C=25°C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{(BR)CEO}	Collector-Emitter Breakdown Voltage	I _C = 5mA; I _B = 0	80			V
V _{(BR)CBO}	Collector-Base Breakdown Voltage	I _C = 100 μ A; I _E = 0	80			V
V _{(BR)EBO}	Emitter-Base Breakdown Voltage	I _E = 100 μ A; I _C = 0	5			V
V _{CE(sat)}	Collector-Emitter Saturation Voltage	I _C = 2A; I _B = 0.2A			1.0	V
V _{BE(sat)}	Base-Emitter Saturation Voltage	I _C = 2A; I _B = 0.2A			1.5	V
I _{CBO}	Collector Cutoff Current	V _{CB} = 80V; I _E = 0			10	μ A
I _{EBO}	Emitter Cutoff Current	V _{EB} = 5V; I _C = 0			10	μ A
h _{FE-1}	DC Current Gain	I _C = 0.5A; V _{CE} = 2V	60		320	
h _{FE-2}	DC Current Gain	I _C = 2A; V _{CE} = 2V	20			
C _{OB}	Output Capacitance	I _E =0; V _{CB} = 10V; f= 1MHz		40		pF

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