

isc Silicon NPN Power Transistor

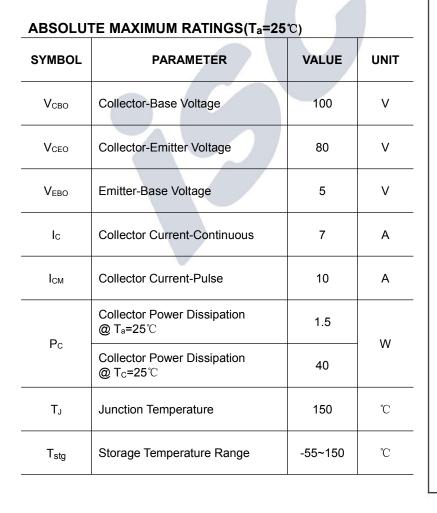
2SD1580

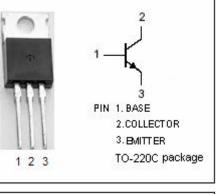
DESCRIPTION

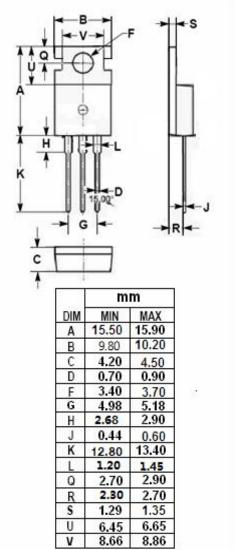
- Low Collector Saturation Voltage
 - : V_{CE(sat)}= 1.0V(Max.)@ I_C= 4A
- High Collector Power Dissipation
- Good Linearity of h_{FE}
- Wide Area of Safe Operation
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

APPLICATIONS

• Designed for low frequency power amplifier applications.







isc website: www.iscsemi.com



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2SD1580

ELECTRICAL CHARACTERISTICS

$T_{\texttt{C}}\text{=}25^{\circ}\!\!\!\mathbb{C}$ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	МАХ	UNIT
V _{(BR)CEO}	Collector-Emitter Breakdown Voltage	I _C = 1mA; I _B = 0	80			V
V _{(BR)CBO}	Collector-Base Breakdown Voltage	I _C = 50 μ A; I _E = 0	100			V
V _{(BR)EBO}	Emitter-Base Breakdown Voltage	I _E = 50 μ A; I _C = 0	5			V
V _{CE(sat)}	Collector-Emitter Saturation Voltage	I _C = 4A; I _B = 0.4A			1.0	V
V _{BE(sat)}	Base-Emitter Saturation Voltage	I _C = 4A; I _B = 0.4A			1.5	V
I _{CBO}	Collector Cutoff Current	V _{CB} = 100V; I _E = 0			10	μA
I _{EBO}	Emitter Cutoff Current	V _{EB} = 4V; I _C = 0			10	μA
h _{FE}	DC Current Gain	I _C = 1A; V _{CE} = 5V	60		320	
f _T	Current-Gain—Bandwidth Product	I _E = -0.5A; V _{CE} = 5V		5		MHz
Сов	Output Capacitance	I _E = 0; V _{CB} = 10V; f _{test} = 1.0MHz		150		pF

h_{FE} Classifications

D	Е	F
60-120	100-200	160-320

NOTICE:

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