

# SILICON POWER TRANSISTOR 2SD1588

# NPN SILICON EPITAXIAL TRANSISTOR FOR LOW-FREQUENCY POWER AMPLIFIERS AND LOW-SPEED SWITCHING

#### **FEATURES**

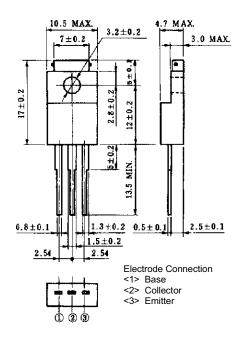
- Mold package that does not require an insulating board or insulation bushing
- Large current capacity in small dimension: Ic(DC) = 7 A
- Low collector saturation voltage: VcE(sat) = 0.5 V MAX. (@5 A)
- Ideal for use in ramp drivers or inductance drivers
- Complementary transistor: 2SB1097

## ABSOLUTE MAXIMUM RATINGS (TA = 25°C)

Parameter	Symbol	Ratings	Unit	
Collector to base voltage	VcBO	100	V	
Collector to emitter voltage	VCEO	60	V	
Emitter to base voltage	V <sub>EBO</sub>	7.0	V	
Collector current (DC)	Ic(DC)	7.0	Α	
Collector current (Pulse)	Ic(pulse)*	15	Α	
Base current (DC)	I <sub>B(DC)</sub>	3.5	Α	
Total power dissipation	Pт (Tc = 25°C)	30	W	
Total power dissipation	Рт (T <sub>A</sub> = 25°С)	2.0	W	
Junction temperature	Tj	150	°C	
Storage temperature	T <sub>stg</sub>	-55 to +150	°C	

<sup>\*</sup> PW  $\leq$  300  $\mu$ s, duty cycle  $\leq$  10%

# PACKAGE DRAWING (UNIT: mm)



## ELECTRICAL CHARACTERISTICS (TA = 25°C)

Parameter	Symbol	Conditions	MIN.	TYP.	MAX.	Unit
Collector cutoff current	Ісво	$V_{CB} = 80 \text{ V}, I_{E} = 0$			10	μΑ
Emitter cutoff current	ІЕВО	$V_{EB} = 5.0 \text{ V}, \text{ Ic} = 0$			10	μΑ
DC current gain	hFE1**	Vce = 1.0 V, Ic = 3 A	40		200	
DC current gain	hFE2**	Vce = 1.0 V, Ic = 5 A	20			
Collector saturation voltage	V <sub>CE(sat)</sub> **	Ic = 5 A, Iв = 0.5 A			0.5	٧
Base saturation voltage	V <sub>BE(sat)</sub> **	Ic = 5 A, I <sub>B</sub> = 0.5 A			1.5	V

<sup>\*\*</sup> Pulse test PW  $\leq$  350  $\mu$ s, duty cycle  $\leq$  2%/per pulsed

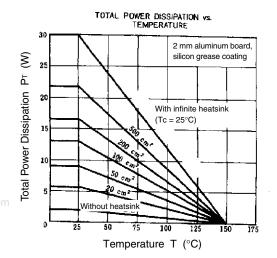
#### **hfe CLASSIFICATION**

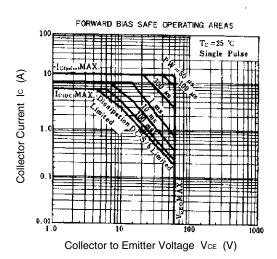
Marking	М	L	K
h <sub>FE1</sub>	40 to 80	60 to 120	100 to 200

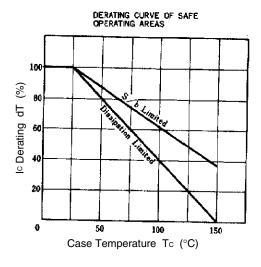
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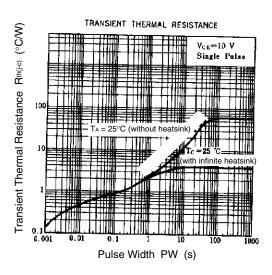


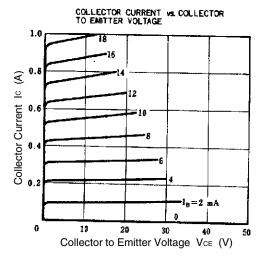
# TYPICAL CHARACTERISTICS (TA = 25°C)

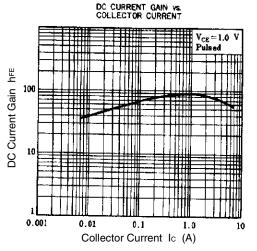


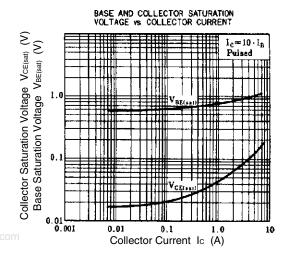












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