

isc Silicon NPN Power Transistor

2SD1619

DESCRIPTION

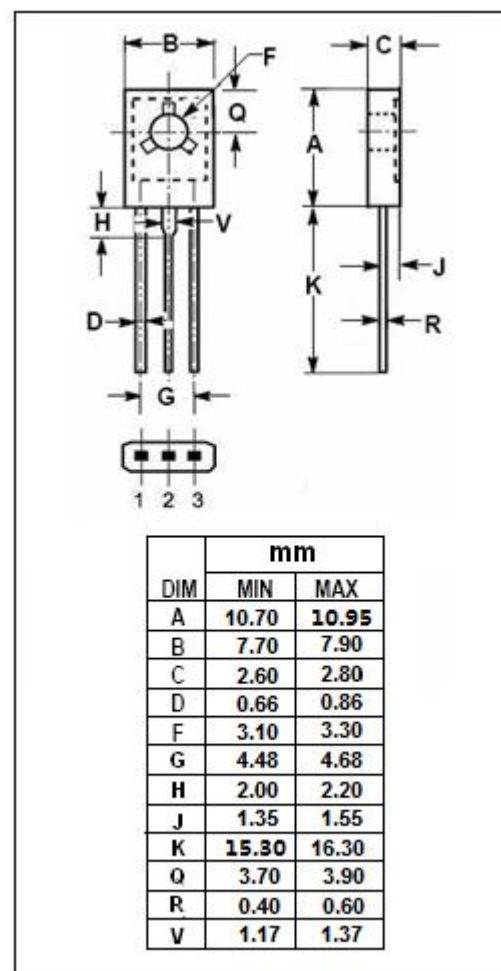
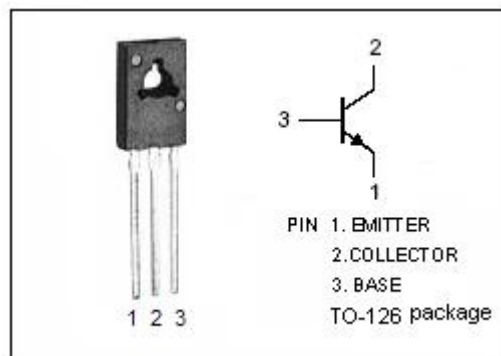
- Collector-Emitter Breakdown Voltage-
: $V_{(BR)CEO} = 25V$ (Min)
- Complement to Type 2SB1119
- 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

APPLICATIONS

- Designed for L_F Amp Electronic Governor applications.

ABSOLUTE MAXIMUM RATINGS($T_a=25^\circ C$)

SYMBOL	PARAMETER	VALUE	UNIT
V_{CBO}	Collector-Base Voltage	25	V
V_{CEO}	Collector-Emitter Voltage	25	V
V_{EBO}	Emitter-Base Voltage	5	V
I_C	Collector Current-Continuous	1	A
I_{CM}	Collector Current-Peak	2	A
P_C	Collector Power Dissipation @ $T_c=25^\circ C$	0.5	W
T_J	Junction Temperature	150	$^\circ C$
T_{stg}	Storage Temperature Range	-55~150	$^\circ C$



isc Silicon NPN Power Transistor**2SD1619****ELECTRICAL CHARACTERISTICS****T_C=25°C unless otherwise specified**

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{(BR)CEO}	Collector-Emitter Breakdown Voltage	I _C = 1mA; I _B = 0	25			V
V _{(BR)CBO}	Collector-Base Breakdown Voltage	I _C =-10uA, I _E =0	25			V
V _{(BR)EBO}	Emitter-Base Breakdown Voltage	I _E = 10uA; I _C = 0	5			V
V _{CE(sat)}	Collector-Emitter Saturation Voltage	I _C = 500mA; I _B = 50mA			0.3	V
V _{BE(sat)}	Base-Emitter Saturation Voltage	I _C = 500mA; I _B = 50mA			1.2	V
I _{CBO}	Collector Cutoff Current	V _{CB} = 20V; I _E = 0			0.1	μ A
I _{EBO}	Emitter Cutoff Current	V _{EB} = 4V; I _C = 0			0.1	μ A
h _{FE-1}	DC Current Gain	I _C = 50mA; V _{CE} = 2V	100		560	
h _{FE-2}	DC Current Gain	I _C = 1A; V _{CE} = 2V	40			
f _T	Current-Gain—Bandwidth Product	I _C = 50mA; V _{CE} = 10V		180		MHz
C _{OB}	Output Capacitance	I _E = 0; V _{CB} = 10V; f= 1.0MHz		15		pF

◆ h_{FE-1} Classifications

R	S	T	U
100-200	140-280	200-400	280-560

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