

isc Silicon NPN Power Transistor

2SD1624

DESCRIPTION

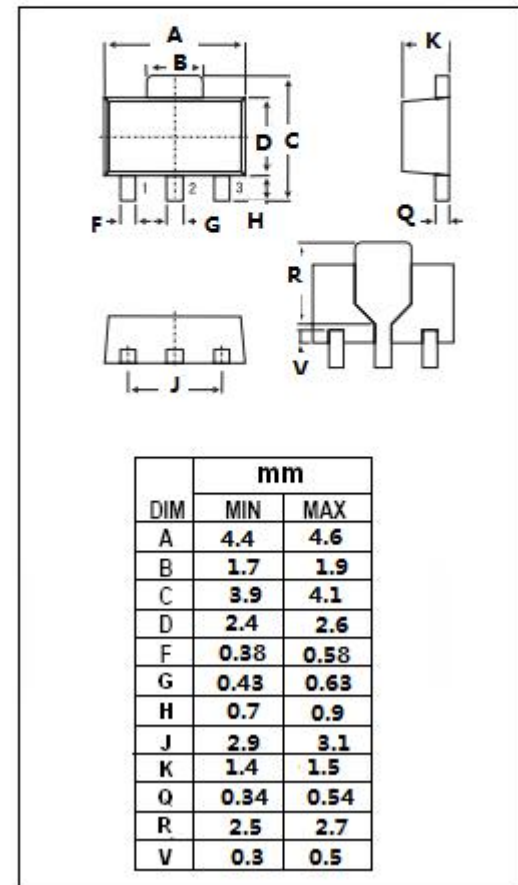
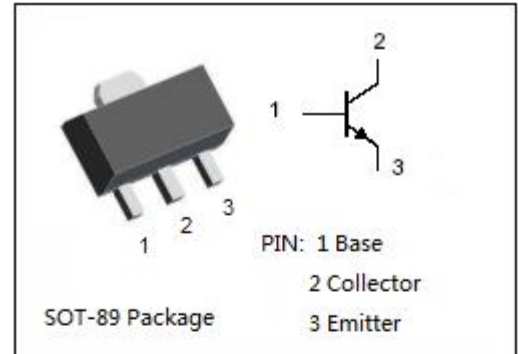
- Collector-Emitter Breakdown Voltage-
: $V_{(BR)CEO} = 50V$ (Min)
- Fast switching speed
- 100% tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

APPLICATIONS

- Designed for L_F Amp Electronic Governor applications.

ABSOLUTE MAXIMUM RATINGS($T_a=25^\circ C$)

SYMBOL	PARAMETER	VALUE	UNIT
V_{CBO}	Collector-Base Voltage	60	V
V_{CEO}	Collector-Emitter Voltage	50	V
V_{EBO}	Emitter-Base Voltage	6	V
I_C	Collector Current-Continuous	3	A
I_{CM}	Collector Current-Peak	6	A
P_C	Collector Power Dissipation @ $T_c=25^\circ C$	0.5	W
T_J	Junction Temperature	150	$^\circ C$
T_{stg}	Storage Temperature Range	-55~150	$^\circ C$



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ELECTRICAL CHARACTERISTICS

T_C=25°C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{(BR)CEO}	Collector-Emitter Breakdown Voltage	I _C = 1mA; I _B = 0	50			V
V _{(BR)CBO}	Collector-Base Breakdown Voltage	I _C =-10uA, I _E =0	60			V
V _{(BR)EBO}	Emitter-Base Breakdown Voltage	I _E = 10uA; I _C = 0	6			V
V _{CE(sat)}	Collector-Emitter Saturation Voltage	I _C = 2A; I _B = 100mA			0.5	V
V _{BE(sat)}	Base-Emitter Saturation Voltage	I _C = 2A; I _B = 100mA			1.2	V
I _{CBO}	Collector Cutoff Current	V _{CB} = 40V; I _E = 0			1.0	μA
I _{EBO}	Emitter Cutoff Current	V _{EB} = 4V; I _C = 0			1.0	μA
h _{FE}	DC Current Gain	I _C = 100mA; V _{CE} = 2V	100		560	
f _T	Current-Gain—Bandwidth Product	I _C = 50mA; V _{CE} = 10V		150		MHz
C _{OB}	Output Capacitance	I _E = 0; V _{CB} = 10V; f= 1.0MHz		25		pF

◆ h_{FE} Classifications

R	S	T	U
100-200	140-280	200-400	280-560

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