

isc Silicon NPN Power Transistor

2SD1763

DESCRIPTION

- · High Collector-Emitter Breakdown Voltage-
 - : V_{(BR)CEO}= 120V(Min.)
- · Good Linearity of hFE
- Complement to Type 2SB1186
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

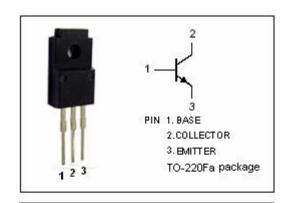
APPLICATIONS

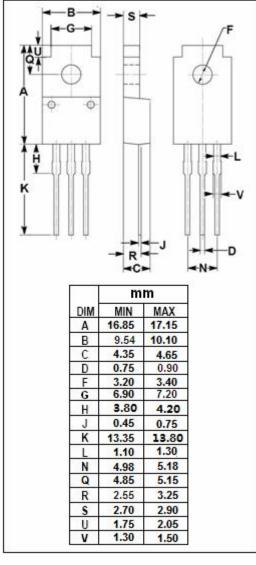
- · Power amplifier applications.
- · Driver stage amplifier applications.



ABSOLUTE MAXIMUM RATINGS(Ta=25℃)

SYMBOL	PARAMETER	VALUE	UNIT
V _{CBO}	Collector-Base Voltage	120	V
V _{CEO}	Collector-Emitter Voltage	120	V
V _{EBO}	Emitter-Base Voltage	5	V
Ic	Collector Current-Continuous	2	А
Ісм	Collector Current-Peak	3	А
Pc	Collector Power Dissipation @ T _C =25℃	20	W
TJ	Junction Temperature	150	$^{\circ}$ C
T _{stg}	Storage Temperature Range	-55~150	$^{\circ}$ C







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ELECTRICAL CHARACTERISTICS

T_c=25℃ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{(BR)CEO}	Collector-Emitter Breakdown Voltage	I _C = 1mA; I _B = 0	120			V
V _{(BR)CBO}	Collector-Base Breakdown Voltage	I _C = 50 μ A; I _E = 0	120			V
V _{(BR)EBO}	Emitter-Base Breakdown Voltage	I _E = 50 μ A; I _C = 0	5			V
V _{CE} (sat)	Collector-Emitter Saturation Voltage	I _C = 1A; I _B = 0.1A			0.4	V
I _{CBO}	Collector Cutoff Current	V _{CB} = 100V; I _E = 0			1	μА
I _{EBO}	Emitter Cutoff Current	V _{EB} = 4V; I _C = 0			1	μА
h _{FE}	DC Current Gain	I _C = 0.1A; V _{CE} = 5V	100		320	
Сов	Output Capacitance	I _E = 0; V _{CB} = 10V; f _{test} =1MHz		20		pF
fτ	Current-Gain—Bandwidth Product	I _E = -0.1A; V _{CE} = 5V; f _{test} = 30MHz		80		MHz

NOTICE:

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