



isc Silicon NPN Darlington Power Transistor

DESCRIPTION

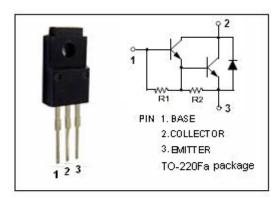
- · Collector-Emitter Breakdown Voltage-
 - : V_{(BR)CEO}= 100V(Min)
- · Collector-Emitter Saturation Voltage-
 - : V_{CE(sat)}= 1.5V(Max.) @I_C= 1A
- · High DC Current Gain
 - : h_{FE}= 1000(Min.) @ I_C= 1A, V_{CE}= 2V
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

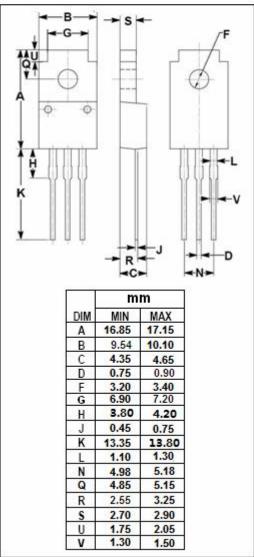
APPLICATIONS

• Designed for low frequency power amplifier applications.

ABSOLUTE MAXIMUM RATINGS(Ta=25℃)

SYMBOL	PARAMETER	VALUE	UNIT
V _{CBO}	Collector-Base Voltage	100	V
V _{CEO}	Collector-Emitter Voltage	V	
V _{ЕВО}	Emitter-Base Voltage	age 6	
Ic	Collector Current-Continuous 2		A
I _{CM}	Collector Current-Pulse	ector Current-Pulse 3	
P _C	Collector Power Dissipation @ T _a =25°C	2	W
	Collector Power Dissipation @ T _C =25 °C	20	V V
TJ	Junction Temperature	150	${\mathbb C}$
T _{stg}	Storage Temperature Range	-55~150	℃







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2SD1765

ELECTRICAL CHARACTERISTICS

Tc=25℃ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{(BR)CEO}	Collector-Emitter Breakdown Voltage	I _C = 5mA; I _B = 0	100			V
V _(BR) CBO	Collector-Base Breakdown Voltage	I _C = 50 μ A; I _E = 0	100			V
V _{CE(sat)}	Collector-Emitter Saturation Voltage	I _C = 1A; I _B = 1mA			1.5	V
I _{CBO}	Collector Cutoff Current	V _{CB} = 100V; I _E = 0			10	μА
І _{ЕВО}	Emitter Cutoff Current	V _{EB} = 5V; I _C = 0			3.0	mA
h _{FE}	DC Current Gain	I _C = 1A; V _{CE} = 2V	1000		10000	
Сов	Collector Output Capacitance	I _E = 0; V _{CB} = 10V; f= 1MHz		25		pF



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