

Power Transistor (80V, 1A)

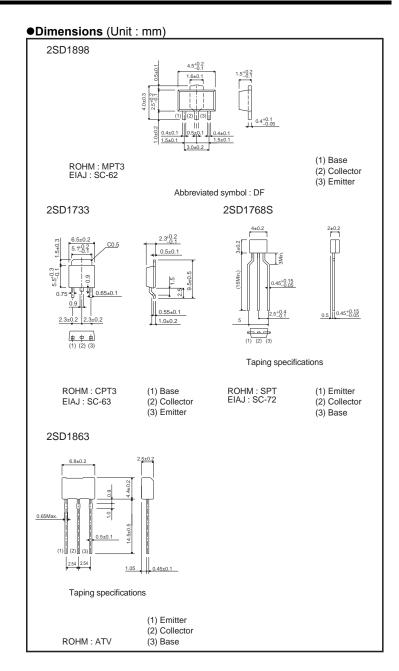
2SD1898 / 2SD1733 / 2SD1768S / 2SD1863

Features

- 1) High VCEO, VCEO=80V
- 2) High Ic, Ic=1A (DC)
- 3) Good hFE linearity
- 4) Low Vce (sat)
- 5) Complements the 2SB1260 / 2SB1241 / 2SB1181

●Structure

Epitaxial planer type NPN silicon transistor



●Absolute maximum ratings (Ta=25°C)

Parameter		Symbol	Limits	Unit	
Collector-base voltage		Vсво	120	V	
Collector-emitter voltage		Vceo	80	V	
Emitter-base voltage		VEBO	5	V	
Collector current		la.	1	A (DC)	
		lc lc	2	A (Pulse) *1	
Collector power dissipation	2SD1898		0.5	W	
			2	W *3	
	2SD1733	Pc	1	W	
		PC	10	W (Tc=25°C)	
	2SD1768S		0.3	W	
	2SD1863		1	W *2	
Junction temperature		Tj	150	°C	
Storage temperature		Tstg	-55 to +150	°C	

●Electrical characteristics (Ta=25°C)

Parameter		Symbol	Min.	Тур.	Max.	Unit	Conditions	
Collector-base breakdown voltage		ВУсво	120	_	_	V	Ic=50μA	
Collector-emitter breakdown voltage		BV _{CEO}	80	_	_	V	Ic=1mA	
Emitter-base breakdown voltage		BV _{EBO}	5	_	_	V	Iε=50μA	
Collector cutoff current		Ісво	_	_	1	μΑ	VcB=100V	
Emitter cutoff current		ІЕВО	_	_	1	μΑ	V _{EB} =4V	
DC current transfer ratio	2SD1863	* hfe	120	_	390	_	Vce=3V, Ic=0.5A	
	2SD1733, 2SD1898		120	_	390	_		
	2SD1768S		120	_	390	_		
Collector-emitter saturation voltage		VCE(sat)	_	0.15	0.4	V	Ic/I _B =500mA/20mA	
Transition frequency		f⊤	_	100	_	MHz	VcE=10V, IE=-50mA, f=100MHz	
Output capacitance		Cob	_	20	_	pF	Vcb=10V, Ie=0A, f=1MHz	

^{*} Measured using pulse current

●Packaging specifications and hfe

		Package	Taping			
		Code	T100	TL	TP	TV2
Туре	hfe	Basic ordering unit (pieces)	1000	2500	5000	2500
2SD1898	QR		0	-	_	_
2SD1733	QR		_	0	_	_
2SD1768S	QR		_	-	0	-
2SD1863	QR		_	_	_	0

hFE values are classified as follows:

Item	Q	R
hfe	120 to 270	180 to 390

^{*1} Pw=20ms, duty=1 / 2
*2 Printed circuit board 1.7mm thick, collector copper plating 1cm² or larger.
*3 When mounted on a 40×40×0.7mm ceramic board.

•Electrical characteristic curves

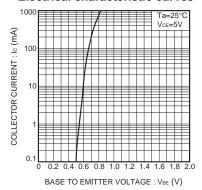


Fig.1 Grounded emitter propagation characteristics

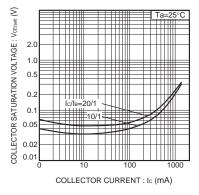


Fig.4 Collector-emitter saturation voltage vs. collector current

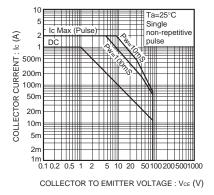


Fig.7 Safe operating area (2SD1863)

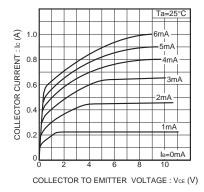


Fig.2 Grounded emitter output characteristics

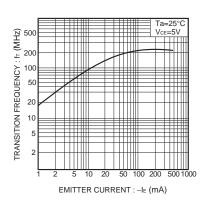


Fig.5 Gain bandwidth product vs. emitter current

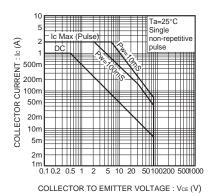


Fig.8 Safe operating area (2SD1898)

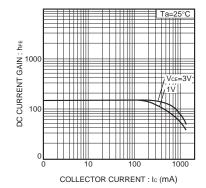


Fig.3 DC current gain vs. collector current

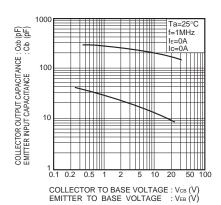


Fig.6 Collector output capacitance vs. collector-base voltage Emitter input capacitance vs. emitter-base voltage

Notes

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