DESCRIPTION

- ·With TO-3 package
- ·High breakdown voltage

APPLICATIONS

- ·voltage regulator
- ·Inverters
- ·Switching mode power supply

PINNING(see Fig.2)

PIN	DESCRIPTION
1	Base
2	Emitter
3	Collector

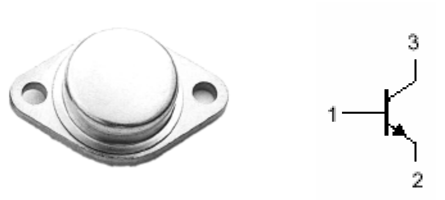

Fig.1 simplified outline (TO-3) and symbol

Absolute maximum ratings(Ta=□)

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
V_{CBO}	Collector-base voltage	Open emitter	300	V
V_{CEO}	Collector-emitter voltage	Open base	300	V
V_{EBO}	Emitter-base voltage	Open collector	6	V
I_C	Collector current		1	A
P_C	Collector power dissipation	T_C=75□	25	W
T_j	Junction temperature		165	□
T_{stg}	Storage temperature		-55~200	□

CHARACTERISTICS

Tj=25□ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
$V_{CEO(SUS)}$	Collector-emitter sustaining voltage	I_C=100mA ;I_B=0	300			V
$V_{(BR)EBO}$	Emitter-base breakdown voltage	I_E=1mA ;I_C=0	6			V
V_{CEsat}	Collector-emitter saturation voltage	I_C=1.0A; I_B=0.1A			1.0	V
V_{BEsat}	Base-emitter saturation voltage	I_C=1.0A; I_B=0.1A			1.5	V
I_{CBO}	Collector cut-off current	V_{CB}=300V; I_E=0			0.1	mA
I_{EBO}	Emitter cut-off current	V_{EB}=6V; I_C=0			0.1	mA
h_{FE}	DC current gain	I_C=0.1A ; V_{CE}=5V	30		300	
f_T	Transition frequency	I_C=0.5A ; V_{CE}=10V		25		MHz

PACKAGE OUTLINE

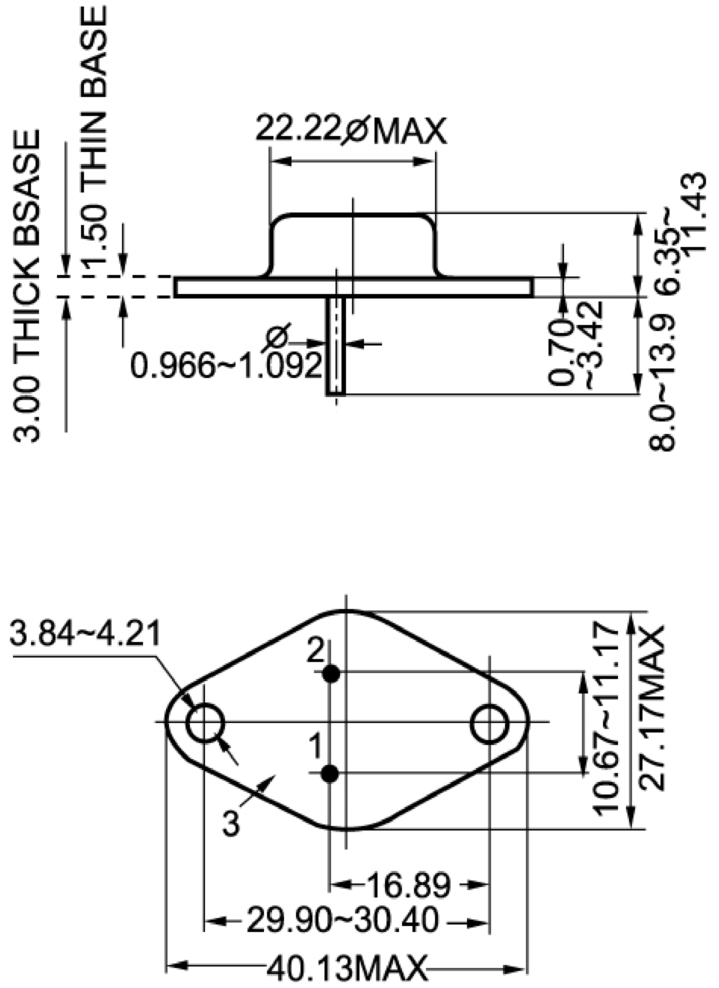

Fig.2 outline dimensions (unindicated tolerance:±0.1mm)