

isc Silicon NPN Power Transistor

2SD203

DESCRIPTION

- · Excellent Safe Operating Area
- · Collector-Emitter Sustaining Voltage-
 - : V_{CEO(SUS)}= 100V(Min.)
- · Low Collector Saturation Voltage-
- · High Switching Speed
- · Minimum Lot-to-Lot variations for robust device performance and reliable operation



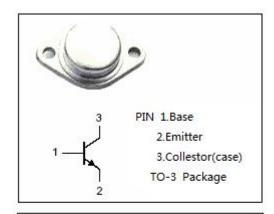
APPLICATIONS

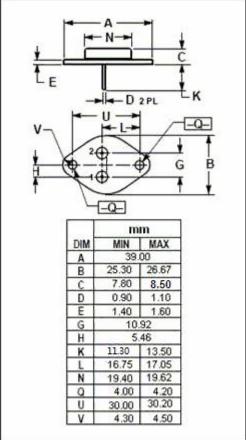
Designed for power amplifier and switching applications



ABSOLUTE MAXIMUM RATINGS(T_a=25℃)

SYMBOL	PARAMETER	MAX	UNIT
V _{CBO}	Collector-Base Voltage	130	V
V _{CEO}	Collector-Emitter Voltage	100	V
V _{EBO}	Emitter-Base Voltage	6	V
Ic	Collector Current-Continuous	6	Α
I _{CP}	Collector Current-Peak	10	Α
Pc	Collector Power Dissipation @T _C =25℃	50	W
T _j	Junction Temperature 150		$^{\circ}$
T _{stg}	Storage Temperature Range	-65~150	$^{\circ}$







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ELECTRICAL CHARACTERISTICS

T_C=25℃ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	MAX	UNIT
V _{CEO(SUS)}	Collector-Emitter Sustaining Voltage	I _C = 10mA ; I _B = 0	100		V
V _{CE(sat)-1}	Collector-Emitter Saturation Voltage	I _C = 3A; I _B = 0.3A		1.0	V
V _{CE(sat)-2}	Collector-Emitter Saturation Voltage	I _C = 6A; I _B = 0.6A		2.0	V
V _{BE(sat)}	Base-Emitter Saturation Voltage	I _C = 3A; I _B = 0.3A		1.5	V
I _{CEO}	Collector Cutoff Current	V _{CE} = 100V; I _B = 0		1.0	mA
Ісво	Collector Cutoff Current	V _{CB} = 130V; I _E = 0		0.1	mA
I _{EBO}	Emitter Cutoff Current	V _{EB} = 5.0V; I _C = 0		0.1	mA
h _{FE-1}	DC Current Gain	I _C = 3A; V _{CE} = 4V	20	100	
h _{FE-2}	DC Current Gain	I _C = 6A; V _{CE} = 4V	5		
f _T	Current Gain-Bandwidth Product	I _C = 0.5A ; V _{CE} = 10V;f= 0.5MHz	3.0		MHz

NOTICE:

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