

isc Silicon NPN Power Transistor
2SD226
DESCRIPTION

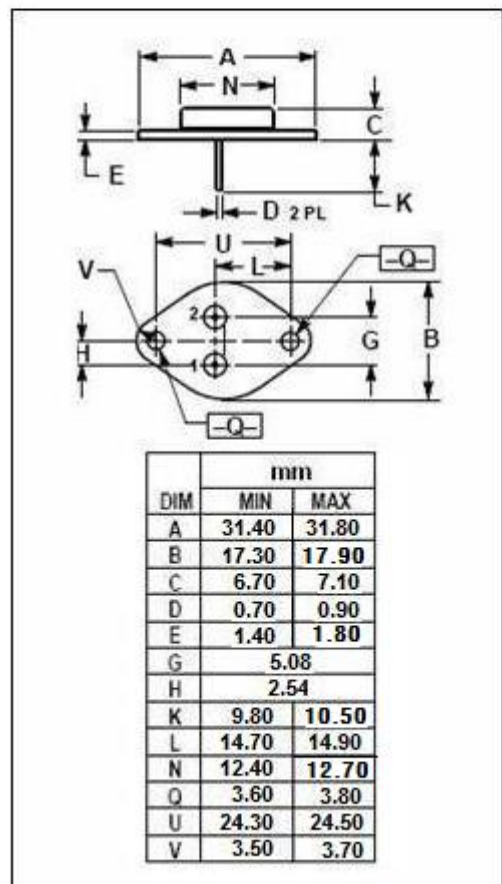
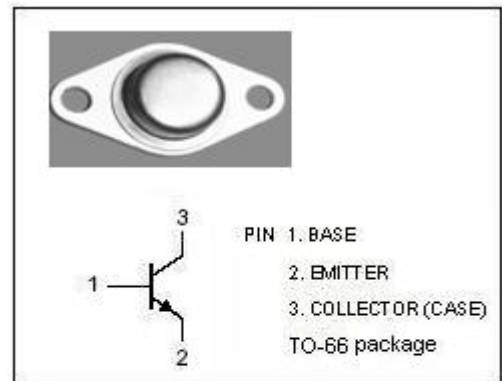
- Excellent Safe Operating Area
- Low Collector-Emitter Saturation Voltage
- 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation.

APPLICATIONS

- Designed for general-purpose power amplifier and switching applications

ABSOLUTE MAXIMUM RATINGS($T_a=25^\circ\text{C}$)

SYMBOL	PARAMETER	VALUE	UNIT
V_{CBO}	Collector-Base Voltage	50	V
V_{CEO}	Collector-Emitter Voltage	40	V
V_{EBO}	Emitter-Base Voltage	8	V
I_C	Collector Current-Continuous	3	A
I_{CM}	Collector Current-Peak	5	A
P_C	Collector Power Dissipation@ $T_C=25^\circ\text{C}$	25	W
T_J	Junction Temperature	150	$^\circ\text{C}$
T_{stg}	Storage Temperature	-65~150	$^\circ\text{C}$



isc Silicon NPN Power Transistor**2SD226****ELECTRICAL CHARACTERISTICS** $T_C=25^{\circ}\text{C}$ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	MAX	UNIT
$V_{CEO(SUS)}^*$	Collector-Emitter Sustaining Voltage	$I_C=30\text{mA}; I_B=0$	40		V
$V_{(BR)EBO}$	Emitter-Base Breakdown Voltage	$I_E=1\text{mA}; I_C=0$	8		V
I_{EBO}	Emitter Cutoff Current	$V_{EB}=7\text{V}; I_C=0$		0.1	mA
I_{CBO}	Collector Base Cutoff Current	$V_{CB}=50\text{V}; I_E=0$		0.1	mA
$V_{CE(sat)}$	Collector-Emitter Saturation Voltage	$I_C=1.5\text{A}; I_B=0.15\text{A}$		0.7	V
$V_{BE(ON)}$	Base-Emitter On Voltage	$I_C=1.5\text{A}; V_{CE}=3\text{V}$		1.4	V
h_{FE-1}^*	DC Current Gain	$I_C=1\text{A}; V_{CE}=3\text{V}$	50	200	
h_{FE-2}^*	DC Current Gain	$I_C=3\text{A}; V_{CE}=3\text{V}$	20		

*:Pulse test:Pulse width=300us,duty cycle≤2%

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