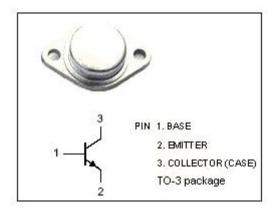


isc Silicon NPN Power Transistor

2SD517

DESCRIPTION

- · Collector-Emitter Sustaining Voltage-
 - : $V_{CEO(SUS)} = 700V(Min)$
- · High Switching Speed
- · Wide Area of Safe Operation
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

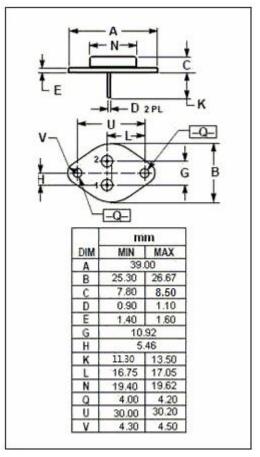


APPLICATIONS

• Designed for use in large screen color deflection circuits .

ABSOLUTE MAXIMUM RATINGS(Ta=25℃)

SYMBOL	PARAMETER	VALUE	UNIT
V _{CEX}	Collector-Emitter Voltage	1500	V
V _{CEO}	Collector-Emitter Voltage	700	V
V_{EBO}	Emitter-Base Voltage	5	V
Ic	Collector Current-Continuous	3	А
Ісм	Collector Current-Peak	5	А
Pc	Collector Power Dissipation @T _C =90°C	16	W
TJ	Junction Temperature	150	$^{\circ}$
T _{stg}	Storage Temperature -65~150		$^{\circ}$





isc Silicon NPN Power Transistor

2SD517

ELECTRICAL CHARACTERISTICS

T_C=25℃ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT		
V _{CEO(SUS)}	Collector-Emitter Sustaining Voltage	I _C = 30mA; I _B = 0	700			V		
V _{CE(sat)}	Collector-Emitter Saturation Voltage	I _C = 3.0A; I _B = 1A			1.0	٧		
V _{BE(sat)}	Base-Emitter Saturation Voltage	I _C = 3.0A; I _B = 1A			1.5	٧		
І _{СВО}	Collector Cutoff Current	V _{CB} = 1500V; I _E = 0			0.5	mA		
I _{EBO}	Emitter Cutoff Current	V _{EB} = 5.0V; I _C = 0			0.1	mA		
h _{FE}	DC Current Gain	I _C = 1A; V _{CE} = 5V	8					
f⊤	Current-Gain—Bandwidth Product	I _C = 0.5A; V _{CE} = 10V; f _{test} = 1MHz		5		MHz		
Switching Times								
ts	Storage Time	I _C = 3A; I _{B1} = I _{B2} = 1.0A			8.0	μ \$		
t _f	Fall Time	10- 07 t, 181- 182- 1.07			0.9	μ \$		

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