

isc Silicon NPN Power Transistor

2SD570

DESCRIPTION

- Collector-Emitter Breakdown Voltage-
: $V_{(BR)CEO} = 70V(\text{Min.})$
- Low Collector Saturation Voltage
: $V_{CE(sat)} = 0.6V(\text{Max.})@I_C = 2A$
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

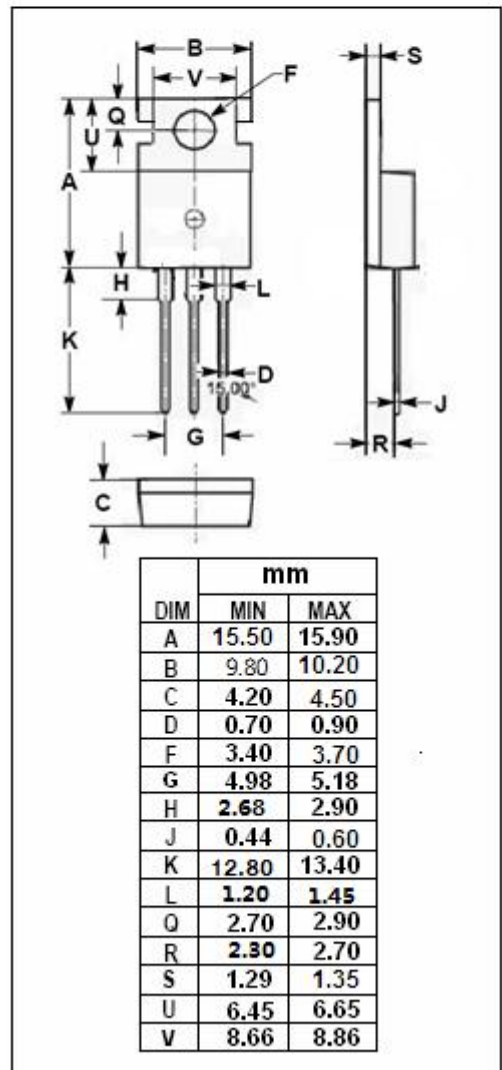
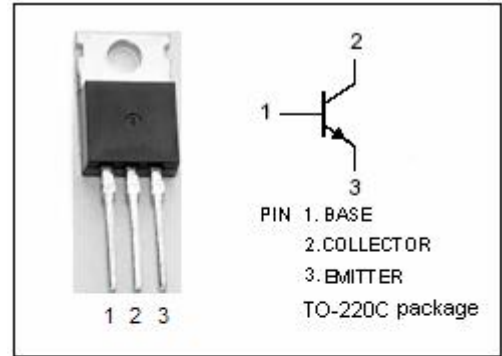


APPLICATIONS

- Designed for audio and general purpose applications.

ABSOLUTE MAXIMUM RATINGS($T_a=25^\circ\text{C}$)

SYMBOL	PARAMETER	VALUE	UNIT
V_{CBO}	Collector-Base Voltage	70	V
V_{CEO}	Collector-Emitter Voltage	70	V
V_{EBO}	Emitter-Base Voltage	6	V
I_C	Collector Current-Continuous	4	A
I_B	Base Current-Peak	1	A
P_C	Total Power Dissipation @ $T_C=25^\circ\text{C}$	30	W
T_J	Junction Temperature	150	$^\circ\text{C}$
T_{stg}	Storage Temperature Range	-55~150	$^\circ\text{C}$



isc Silicon NPN Power Transistor**2SD570****ELECTRICAL CHARACTERISTICS**T_c=25°C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{(BR)CEO}	Collector-Emitter Breakdown Voltage	I _C = 25mA; I _B = 0	70			V
V _{CE(sat)}	Collector-Emitter Saturation Voltage	I _C = 2A; I _B = 0.2A			0.6	V
V _{BE(sat)}	Base-Emitter Saturation Voltage	I _C = 2A; I _B = 0.2A			1.0	V
I _{CBO}	Collector Cutoff Current	V _{CB} = 70V; I _E = 0			100	μ A
I _{EBO}	Emitter Cutoff Current	V _{EB} = 6V; I _C = 0			100	μ A
h _{FE}	DC Current Gain	I _C = 0.5A; V _{CE} = 5V	100			
f _T	Current-Gain—Bandwidth Product	I _E = 0.1A; V _{CE} = 5V		10		MHz
C _{OB}	Output Capacitance	I _E = 0; V _{CB} = 10V; f _{test} = 1MHz		60		pF

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