

isc Silicon NPN Power Transistor

2SD577

DESCRIPTION

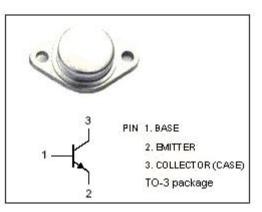
- Collector-Emitter Sustaining Voltage-
- : V_{CEO(SUS)} = 700V(Min)
- High Switching Speed
- Wide Area of Safe Operation
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

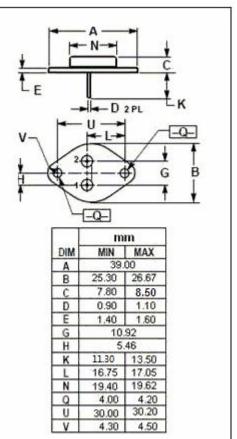
APPLICATIONS

• Designed for use in large screen color deflection circuits .

ABSOLUTE MAXIMUM RATINGS(Ta=25℃)

SYMBOL	PARAMETER	VALUE	UNIT
V _{CEX}	Collector-Emitter Voltage	1500	V
V _{CEO}	Collector-Emitter Voltage	700	V
V_{EBO}	Emitter-Base Voltage	5	v
lc	Collector Current-Continuous	3	А
I _{CM}	Collector Current-Peak	5	A
Pc	Collector Power Dissipation @T _c =90°C	16	W
TJ	Junction Temperature	150	°C
T _{stg}	Storage Temperature	-65~150	°C







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ELECTRICAL CHARACTERISTICS

$T_c=25^{\circ}C$ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	МАХ	UNIT
Vceo(sus)	Collector-Emitter Sustaining Voltage	I _C = 30mA; I _B = 0	700			v
V _{CE(sat)}	Collector-Emitter Saturation Voltage	I _C = 3.0A; I _B = 1A			1.0	V
V _{BE(sat)}	Base-Emitter Saturation Voltage	I _C = 3.0A; I _B = 1A			1.5	v
I _{CBO}	Collector Cutoff Current	V _{CB} = 1500V; I _E = 0			0.1	mA
I _{EBO}	Emitter Cutoff Current	V _{EB} = 5.0V; I _C = 0			0.1	mA
hfe -1	DC Current Gain	Ic= 1A; Vc== 5V	10		30	
h _{FE -1}	DC Current Gain	I _C = 3A; V _{CE} = 10V	7			
f⊤	Current-Gain—Bandwidth Product	I _C = 0.5A; V _{CE} = 10V; f _{test} = 1MHz		5		MHz
Switching T	ïmes					<u> </u>
ts	Storage Time	- I _C = 3A; I _{B1} = I _{B2} = 1.0A			8.0	μ S
tf	Fall Time	10- 5A, 181- 182- 1.UA			1.0	μ S

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