

isc Silicon NPN Power Transistor
2SD764
DESCRIPTION

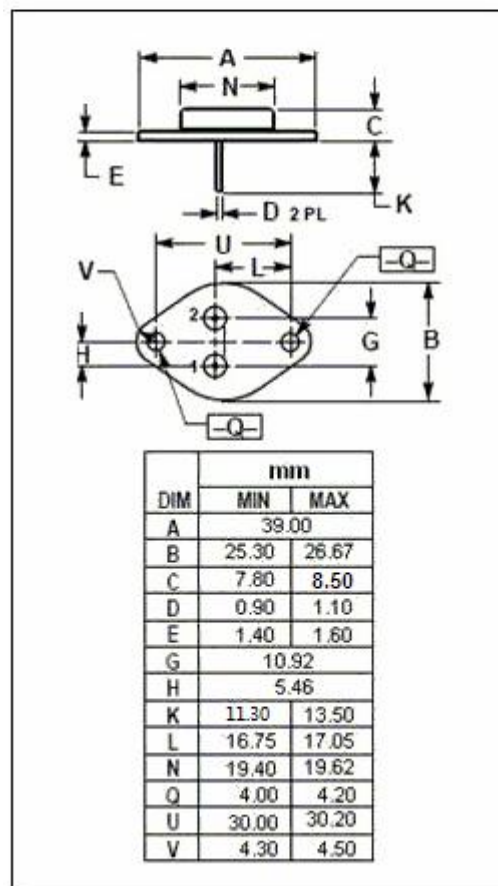
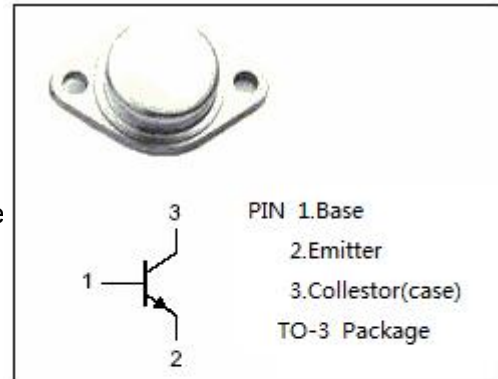
- With TO-3 Package
- High Voltage Capability
- Low collector saturation voltage
- Minimum Lot-to-Lot variations for robust device performance and reliable operation.

APPLICATIONS

- Designed for high voltage power switching TV horizontal deflection output applications.

ABSOLUTE MAXIMUM RATINGS($T_a=25^\circ\text{C}$)

SYMBOL	PARAMETER	VALUE	UNIT
V_{CBO}	Collector-Base Voltage	1500	V
V_{CEO}	Collector-Emitter Voltage	600	V
V_{EBO}	Emitter-Base Voltage	6	V
I_C	Collector Current-Continuous	1.5	A
I_{CM}	Collector Current-Peak	2	A
P_C	Collector Power Dissipation @ $T_C=25^\circ\text{C}$	50	W
T_J	Junction Temperature	150	$^\circ\text{C}$
T_{stg}	Storage Temperature Range	-45-150	$^\circ\text{C}$



isc Silicon NPN Power Transistor**2SD764****ELECTRICAL CHARACTERISTICS**T_C=25°C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	MAX	UNIT
V _{(BR)EBO}	Emitter-Base Breakdown Voltage	I _E = 10mA; I _C = 0	6		V
V _{(BR)CEO}	Collector-Emitter Breakdown Voltage	I _C = 10mA; I _B = 0	600		V
V _{CE(sat)}	Collector-Emitter Saturation Voltage	I _C = 1A; I _B = 0.2A		5.0	V
V _{BE(sat)}	Base-Emitter Saturation Voltage	I _C = 1A; I _B = 0.2A		1.5	V
I _{CBO}	Collector Cutoff Current	V _{CB} =1500V; I _B = 0		0.5	mA
I _{EBO}	Emitter Cutoff Current	V _{EB} = 6V; I _C =0		100	μA
h _{FE}	DC Current Gain	I _C = 1A; V _{CE} = 5V	8	36	
t _f	Fall Time	I _C = 0.8A; I _{B1} = 0.16A; I _{B2} = 0.2A		1.5	μs

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