

isc Silicon NPN Power Transistor

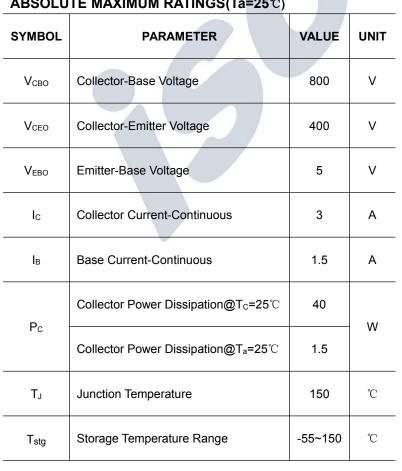
2SD841

DESCRIPTION

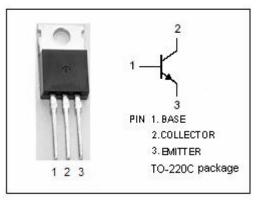
- High Collector-Base Breakdown Voltage : V_{(BR)CBO}= 800V(Min.)
- High Switching Speed
- · Low Collector Saturation Voltage-
- : V_{CE(sat)}= 1.0V(Max.)@ I_C= 0.5A
- · Minimum Lot-to-Lot variations for robust device performance and reliable operation

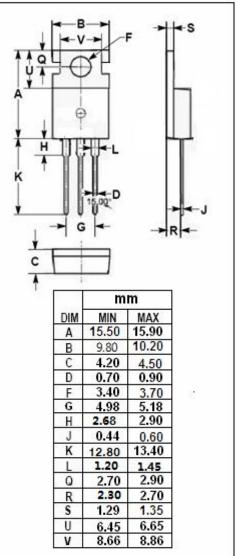
APPLICATIONS

· Designed for voltage switching applications.









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1



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ELECTRICAL CHARACTERISTICS

$T_c=25^{\circ}C$ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	МАХ	UNIT
V _{(BR)CEO}	Collector-Emitter Breakdown Voltage	I _C = 10mA; I _B = 0	400			V
V _{CE(sat)}	Collector-Emitter Saturation Voltage	I _C = 0.5A; I _B = 50mA			1.0	V
V _{BE(sat)}	Base-Emitter Saturation Voltage	I _C = 0.5A; I _B = 50mA			1.5	V
Ісво	Collector Cutoff Current	V _{CB} = 800V; I _E = 0			1.0	mA
I _{EBO}	Emitter Cutoff Current	V _{EB} = 5V; I _C =0			1.0	mA
h _{FE-1}	DC Current Gain	I _C = 10mA; V _{CE} = 5V	8			
hfe-2	DC Gurrent Gain	Ic= 0.5A; V _{CE} = 5V	10			
f⊤	Current-Gain—Bandwidth Product	I _E = 0.1A; V _{CE} = 10V		4		MHz
Сов	Output Capacitance	I _E = 0; V _{CB} = 10V; f _{test} = 1.0MHz		75		pF
t _f	Fall Time	I _C = 0.5A; I _{B1} = I _{B2} = 50mA; R _L = 400 Ω ; V _{CC} ≈ 200V			1.0	μS

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2