

# **isc Silicon NPN Power Transistor**

2SD862

#### **DESCRIPTION**

- High Collector Current-I<sub>C</sub>= 2A
- · Collector-Emitter Breakdown Voltage-
  - : V<sub>(BR)CEO</sub>= 20V(Min)
- · Good Linearity of hFE
- Low Saturation Voltage
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

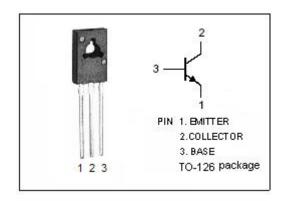


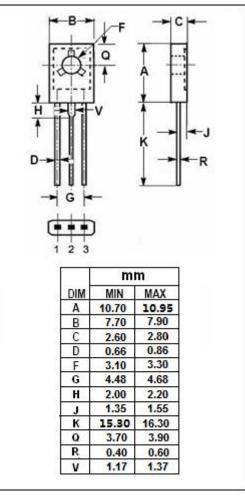
### **APPLICATIONS**

 Designed for high frequency, Low Vce(sat) middle power transistors in a plastic envelope, primarily for use in audio and general purpose applications.



SYMBOL	PARAMETER	VALUE	UNIT
V <sub>CBO</sub>	Collector-Base Voltage	20	V
V <sub>CEO</sub>	Collector-Emitter Voltage	20	V
V <sub>EBO</sub>	Emitter-Base Voltage	6	V
Ic	Collector Current-Continuous	2	Α
I <sub>B</sub>	Base Current-Continuous	0.5	Α
Pc	Collector Power Dissipation @ $T_c$ =25 $^{\circ}$ C	10	W
TJ	Junction Temperature	150	°C
T <sub>stg</sub>	Storage Temperature Range	-55~150	°C







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### **ELECTRICAL CHARACTERISTICS**

T<sub>C</sub>=25℃ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V <sub>(BR)CBO</sub>	Collector-Base Breakdown Voltage	I <sub>C</sub> = 1mA ; I <sub>E</sub> = 0	20			V
V <sub>(BR)CEO</sub>	Collector-Emitter Breakdown Voltage	I <sub>C</sub> = 1mA; R <sub>BE</sub> = ∞	20			V
V <sub>(BR)EBO</sub>	Emitter-Base Breakdown VItage	I <sub>E</sub> = 1mA ; I <sub>C</sub> =0	5			V
V <sub>CE(sat)</sub>	Collector-Emitter Saturation Voltage	I <sub>C</sub> = 1.5A; I <sub>B</sub> = 0.15A			0.5	V
I <sub>EBO</sub>	Emitter Cutoff Current	V <sub>EB</sub> = 5V; I <sub>C</sub> = 0			200	μ А
Ісво	Collector Cutoff Current	V <sub>CB</sub> = 20V; I <sub>E</sub> = 0			100	μА
h <sub>FE</sub>	DC Current Gain	I <sub>C</sub> = 100mA ; V <sub>CE</sub> = 2V	60		400	
f⊤	Current-Gain—Bandwidth Product	I <sub>C</sub> = 0.5A; V <sub>CE</sub> = 12V		80		MHz
Сов	Output Capacitance	I <sub>E</sub> = 0; V <sub>CB</sub> = 10V, f <sub>test</sub> = 1MHz		75		pF

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