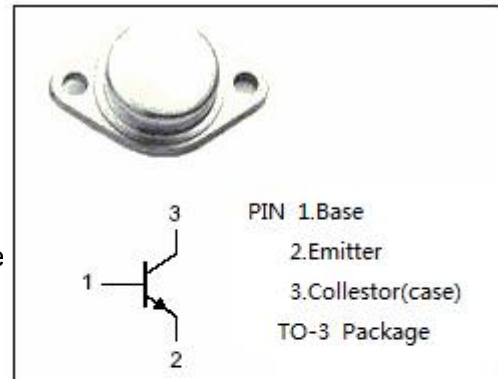


isc Silicon NPN Power Transistor
2SD867
DESCRIPTION

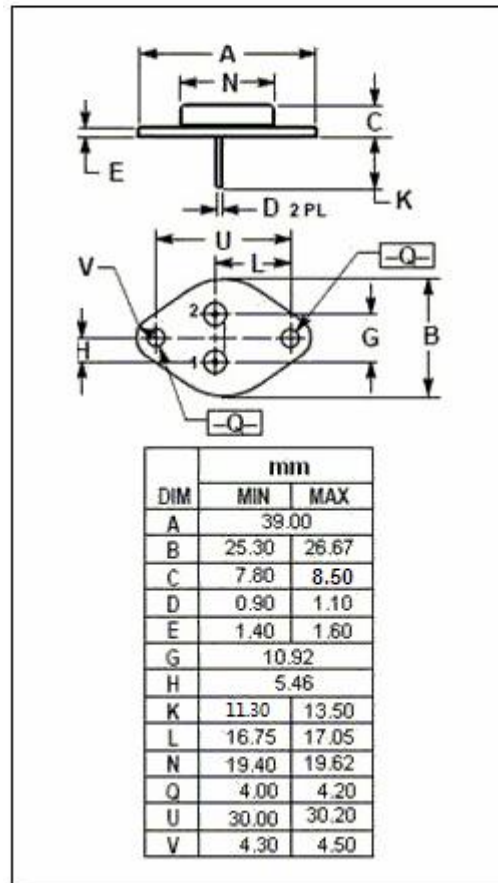
- Collector-Emitter Sustaining Voltage-
: $V_{CE(SUS)} = 110V(\text{Min})$.
- Excellent Safe Operating Area
- Low collector saturation voltage
: $V_{CE(sat)} = 3.0V(\text{Max}) @ I_C = 10A$
- Minimum Lot-to-Lot variations for robust device performance and reliable operation.


APPLICATIONS

- High voltage high current power transistors

ABSOLUTE MAXIMUM RATINGS($T_a=25^\circ\text{C}$)

SYMBOL	PARAMETER	VALUE	UNIT
V_{CBO}	Collector-Base Voltage	130	V
V_{CEO}	Collector-Emitter Voltage	110	V
V_{EBO}	Emitter-Base Voltage	7	V
I_C	Collector Current-Continuous	10	A
I_{CP}	Collector Current-Peak	15	A
I_B	Base Current-Continuous	7	A
P_C	Collector Power Dissipation@ $T_C=25^\circ\text{C}$	100	W
T_J	Junction Temperature	175	$^\circ\text{C}$
T_{stg}	Storage Temperature	-65~175	$^\circ\text{C}$



isc Silicon NPN Power Transistor**2SD867****ELECTRICAL CHARACTERISTICS****T_C=25°C unless otherwise specified**

V _{CE0(SUS)}	Collector-Emitter Sustaining Voltage	I _C = 50mA; I _B = 0	110		V
V _{CE(sat)-1}	Collector-Emitter Saturation Voltage	I _C = 5A; I _B = 0.5A		1.0	V
V _{CE(sat)-2}	Collector-Emitter Saturation Voltage	I _C = 10A; I _B = 3.3A		3.0	V
V _{BE(on)}	Base-Emitter On Voltage	I _C = 5A; V _{CE} = 4V		2.0	V
I _{CEO}	Collector Cutoff Current	V _{CE} = 110V; I _B = 0		1	mA
I _{EBO}	Emitter Cutoff Current	V _{EB} = 7.0V; I _C = 0		0.1	mA
h _{FE-1}	DC Current Gain	I _C = 1A; V _{CE} = 5V	50	200	
h _{FE-2}	DC Current Gain	I _C = 5A; V _{CE} = 5V	20		
h _{FE-3}	DC Current Gain	I _C = 10A; V _{CE} = 5V	5		
f _T	Current Gain-Bandwidth Product	I _C = 1A; V _{CE} = 5V;	3		kHz

NOTICE:

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