





TO-252-2L

TRANSISTOR (NPN)

#### **FEATURES**

Power Dissipation

# MAXIMUM RATINGS (Ta=25°C unless otherwise noted)

1. BASE

2. COLLECTOR

3 .EMITTER

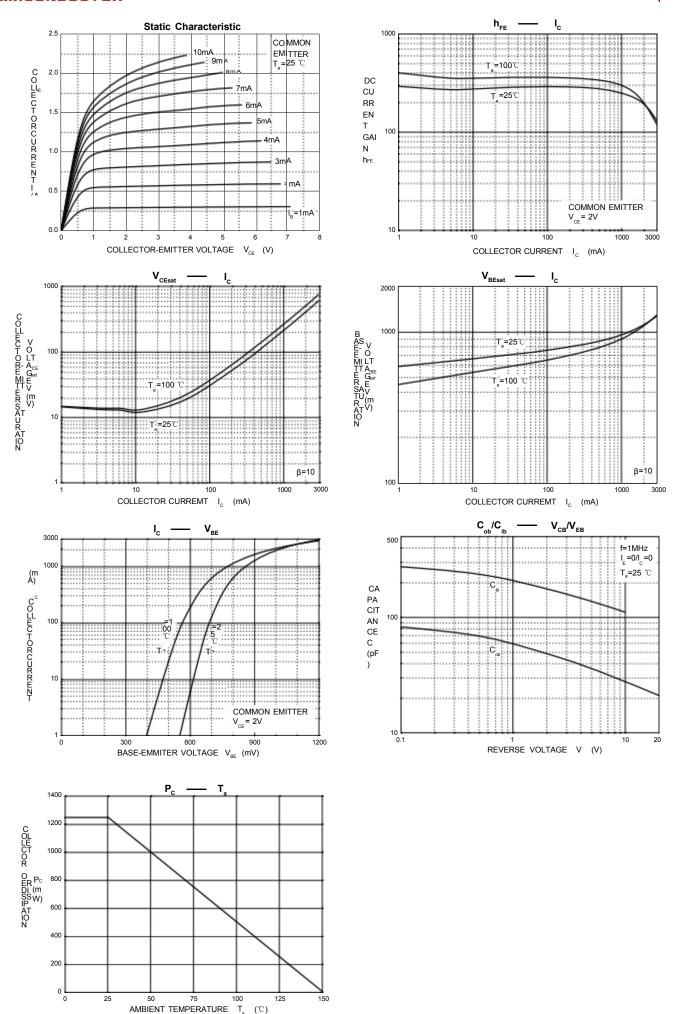
Symbol	Parameter	Value	Unit
Vcво	Collector-Base Voltage	40	V
VCEO	Collector-Emitter Voltage	30	V
V <sub>EBO</sub>	Emitter-Base Voltage	6	V
lc	Collector Current -Continuous	3	Α
Pc	Collector Power Dissipation	1.25	W
T <sub>J</sub> ,T <sub>stg</sub>	Operation Junction and Storage Temperature Range	-55-150 ℃	

## ELECTRICAL CHARACTERISTICS ( $T_a=25\,^{\circ}\mathrm{C}$ unless otherwise specified )

Parameter	Symbol	Test conditions	Min	Тур	Max	Unit
Collector-base breakdown voltage	V(BR) <sub>CBO</sub>	I <sub>C</sub> = 100μA, I <sub>E</sub> =0	40			V
Collector-emitter breakdown voltage	V(BR) <sub>CEO</sub>	Ic = 10mA, I <sub>B</sub> =0	40			V
Emitter-base breakdown voltage	V(BR) <sub>EBO</sub>	I== 100μA, Ic=0	6			V
Collector cut-off current	Ісво	V <sub>CB</sub> = 40 V, I <sub>E</sub> =0			1	μA
Collector cut-off current	ICEO	V <sub>CE</sub> = 30 V, I <sub>B</sub> =0			10	μA
Emitter cut-off current	I <sub>EBO</sub>	V <sub>EB</sub> = 6 V, I <sub>C</sub> =0			1	μA
DC current gain	hfE	V <sub>CE</sub> = 2 V, I <sub>C</sub> = 1A	60		400	
Collector-emitter saturation voltage	V <sub>CE</sub> (sat)	Ic= 2A, I <sub>B</sub> = 0.2 A			0.5	V
Base-emitter saturation voltage	V <sub>BE (sat)</sub>	Ic= 2A, IB= 0.2 A			1.5	V
Transition frequency	f⊤	V <sub>CE</sub> = 5V, I <sub>C</sub> =0.1A f =10MHz		90		MHz

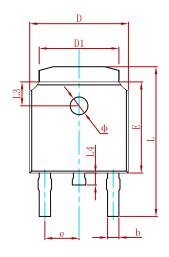


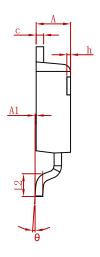


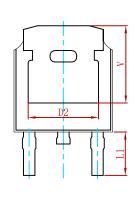




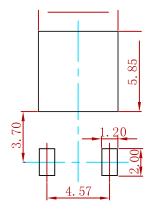
## **PACKAGE MECHANICAL DATA**







0	Dimensions In Millimeters Min. Max.		Dimensions In Inches		
Symbol			Min.	Max.	
Α	2.200	2.400	0.087	0.094	
A1	0.000	0.127	0.000	0.005	
b	0.635	0.770	0.025	0.030	
С	0.460	0.580	0.018	0.023	
D	6.500	6.700	0.256	0.264	
D1	5.100	5.460	0.201	0.215	
D2	4.830	REF.	0.190	REF.	
E	6.000	6.200	0.236	0.244	
е	2.186	2.386	0.086	0.094	
L	9.712	10.312	0.382	0.406	
L1	2.900	REF.	0.114	REF.	
L2	1.400	1.700	0.055	0.067	
L3	1.600 REF.		0.063	REF.	
L4	0.600	1.000	0.024	0.039	
Ф	1.100	1.300	0.043	0.051	
θ	0°	8°	0°	8°	
h	0.000	0.300	0.000	0.012	



#### Note:

- 1.Controlling dimension:in millimeters.
- 2.General tolerance:± 0.05mm.
- 3. The pad layout is for reference purposes only.

## **REEL SPECIFICATION**

P/N	PKG	QTY
2SD882	TO-252	2500



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