

General Description

These P-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

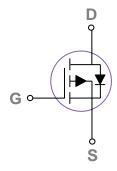
BVDSS	RDSON	ID
-30V	40m $Ω$	-5A

Features

- -30V, -5A, $RDS(ON) = 40m\Omega$ @VGS = -10V
- Improved dv/dt capability
- Fast switching
- Green Device Available

SOT89 Pin Configuration





Applications

- MB / VGA / Vcore
- Load Switch
- Hand-Held Instrument

Absolute Maximum Ratings Tc=25°C unless otherwise noted

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	-30	V
V _G s	Gate-Source Voltage	±20	V
la	Drain Current – Continuous (T _A =25°C)	- 5	А
ID	Drain Current – Continuous (T _A =70°C)	-3.2	А
I _{DM}	Drain Current – Pulsed ¹	- 16	А
P _D	Power Dissipation (T _A =25°C)	1.6	W
r D	Power Dissipation – Derate above 25°C	0.013	W/°C
Tstg	Storage Temperature Range	-55 to 150	°C
TJ	Operating Junction Temperature Range	-55 to 125	°C

Thermal Characteristics

Symbol	Symbol Parameter		Max.	Unit
R _θ ЈА	Thermal Resistance Junction to Ambient		80	°C/W



Electrical Characteristics (T_J=25 °C, unless otherwise noted)

Off Characteristics

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V , I _D = - 250uA	- 30			V
	Drain Source Leakage Current	V _{DS} = - 30V , V _{GS} =0V , T _J =25°C			-1	uA
I _{DSS}	Drain-Source Leakage Current	V _{DS} = - 24V , V _{GS} =0V , T _J =125°C			-10	uA
I _{GSS}	Gate-Source Leakage Current	V _{GS} =±20V , V _{DS} =0V			±100	nA

On Characteristics

Dagger	Static Drain-Source On-Resistance	V _{GS} =-10V , I _D =-3A		40	55	mΩ
NDS(ON)	R _{DS(ON)} Static Drain-Source On-Resistance	V _{GS} = - 4.5V , I _D = - 2.5A		55	90	mΩ
V _{GS(th)}	Gate Threshold Voltage	$V_{GS}=V_{DS}$, $I_{D}=-250uA$	- 1.0	- 1.6	- 2.5	V
gfs	Forward Transconductance	V _{DS} =-10V , I _D =-3A		4		S

Dynamic and switching Characteristics

Q_g	Total Gate Charge ^{2,3}		 5.1	
Q_{gs}	Gate-Source Charge ^{2,3}	V _{DS} =-15V , V _{GS} =-4.5V , I _D =-2A	 2	 nC
Q_{gd}	Gate-Drain Charge ^{2,3}		 2.2	
$T_{d(on)}$	Turn-On Delay Time ^{2,3}		 3.4	
Tr	Rise Time ^{2, 3}	V_{DD} =-15 V , V_{GS} =-10 V , R_{G} =6 Ω	 10.8	 20
$T_{d(off)}$	Turn-Off Delay Time ^{2, 3}	I _D = - 2A	 26.9	 ns
Tf	Fall Time ^{2,3}		 6.9	
Ciss	Input Capacitance		 560	
Coss	Output Capacitance	V _{DS} =-15V , V _{GS} =0V , F=1MHz	 55	 pF
Crss	Reverse Transfer Capacitance		 40	

Drain-Source Diode Characteristics and Maximum Ratings

Symbol	Parameter	Conditions		Тур.	Max.	Unit
Is	Continuous Source Current	V- V- OV Force Current			- 5	Α
Іѕм	Pulsed Source Current	V _G =V _D =0V , Force Current			- 10	Α
V _{SD}	Diode Forward Voltage	V _{GS} =0V , I _S =-1A , T _J =25°C			- 1.2	V

Note:

- 1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
- 2. The data tested by pulsed , pulse width ≤ 300 us , duty cycle $\leq 2\%$.
- 3. Essentially independent of operating temperature.



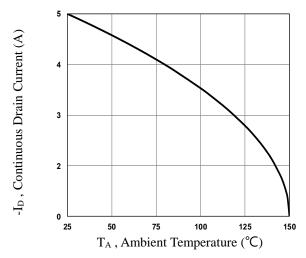


Fig.1 Continuous Drain Current vs. TA

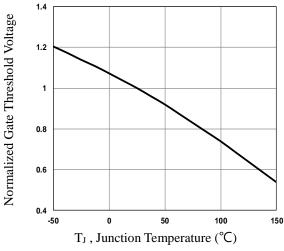


Fig.3 Normalized V_{th} vs. T_J

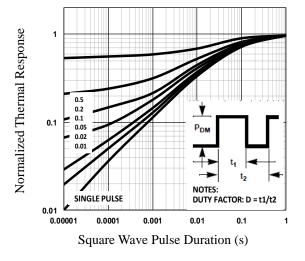


Fig.5 Normalized Transient Impedance

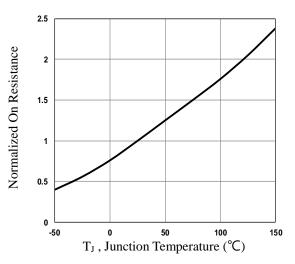


Fig.2 Normalized RDSON vs. T_J

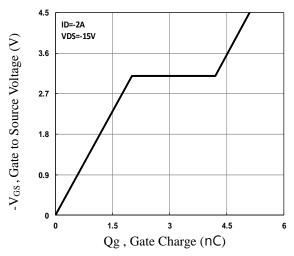


Fig.4 Gate Charge Waveform

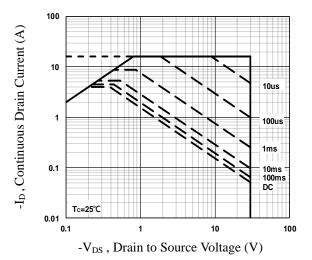
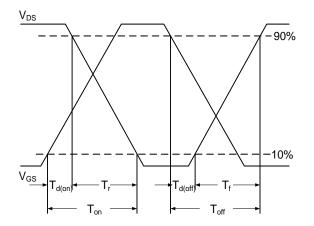


Fig.6 Maximum Safe Operation Area





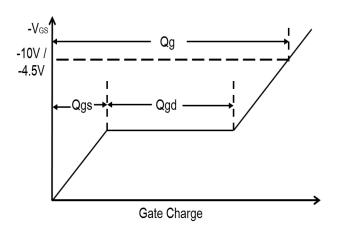
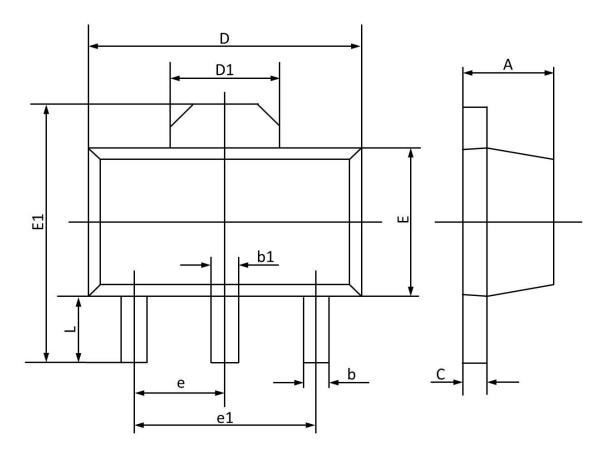


Fig.8 Gate Charge Waveform



SOT89 PACKAGE INFORMATION



Symbol	Dimensions I	n Millimeters	Dimension	s In Inches	
Symbol	Min	Max	Min	Max	
A	1.400	1.600	0.055	0.063	
b	0.320	0.520	0.013	0.020	
b1	0.400	0.580	0.016	0.023	
С	0.350	0.440	0.014	0.017	
D	4.400	4.600	0.173	0.181	
D1	1.550 REF		0.061	REF	
${f E}$	2.300	2.600	0.091	0.102	
E1	3.940	4.250	0.155	0.167	
e	1.500 TYP.		0.060 TYP.		
e1	3.000	TYP	0.118 TYP		
L	0.900	1.200	0.035	0.047	



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