**Features**

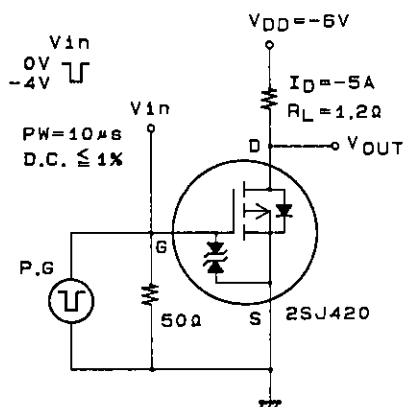
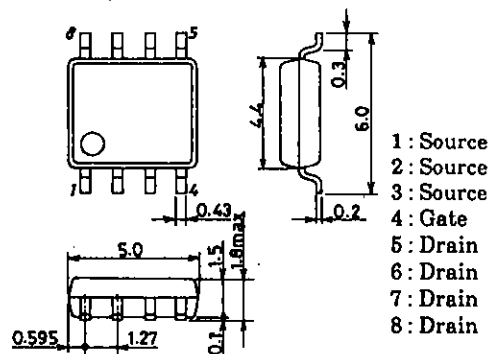
- Low ON resistance.
- Ultrahigh-speed switching.
- 2.5V drive.

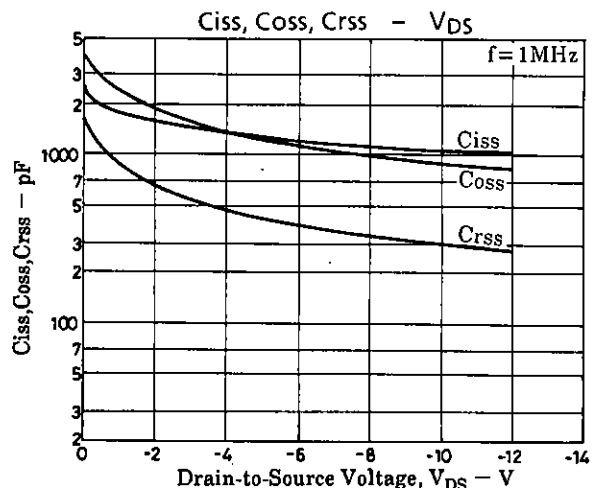
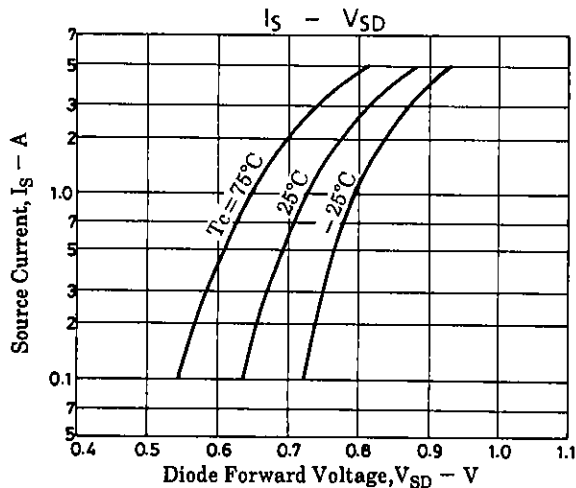
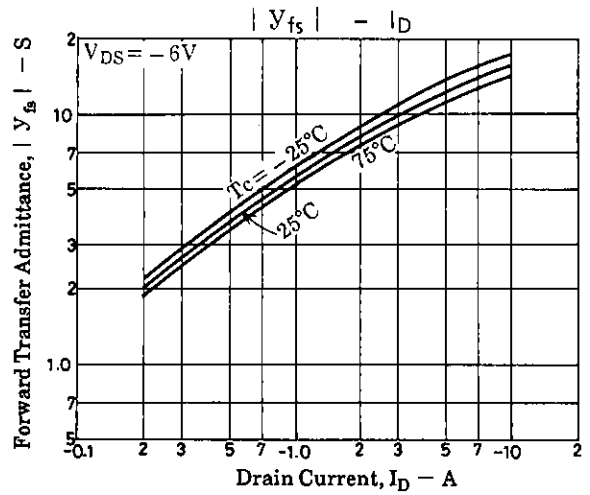
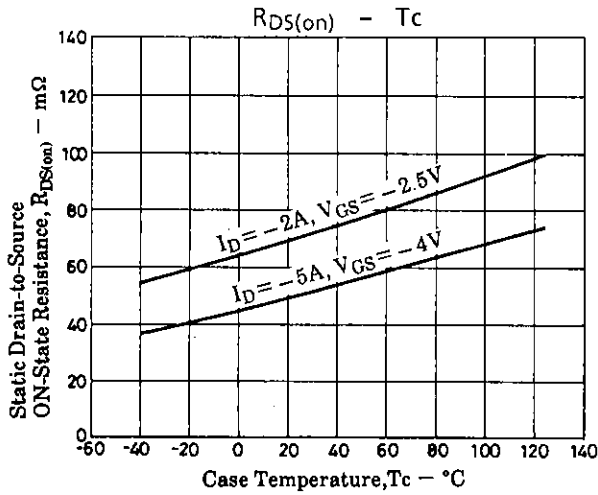
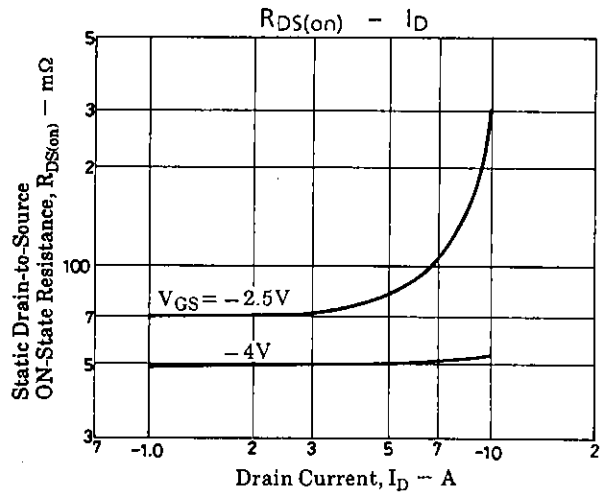
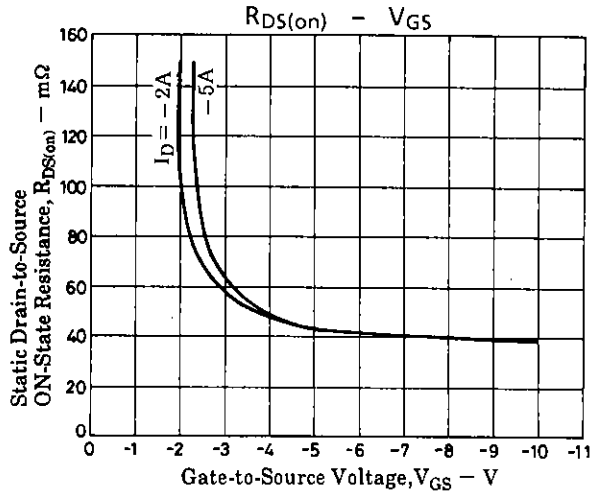
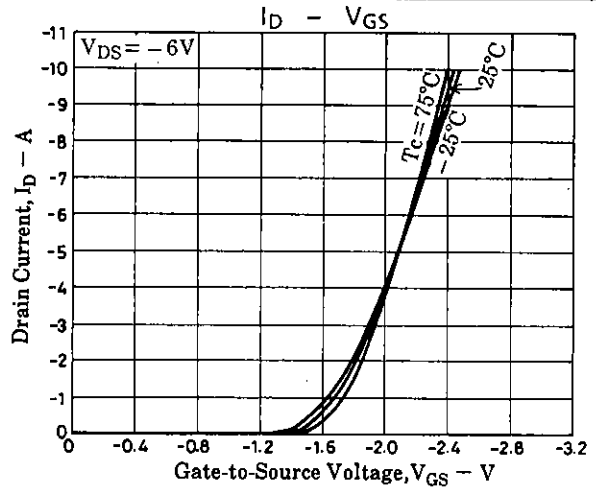
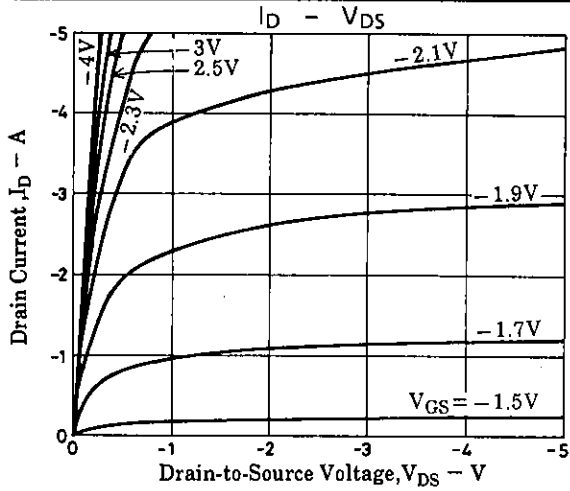
**Absolute Maximum Ratings at Ta=25°C**

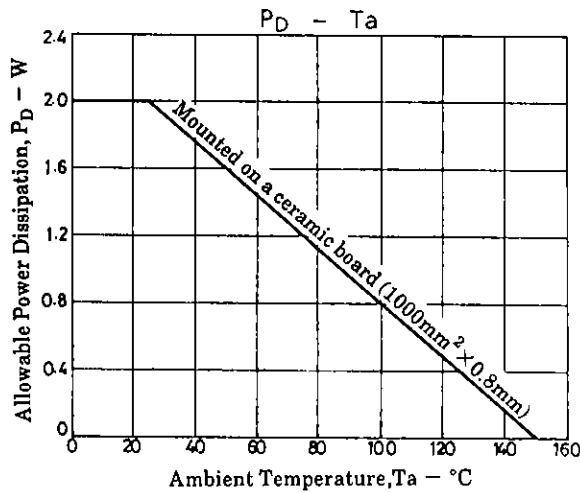
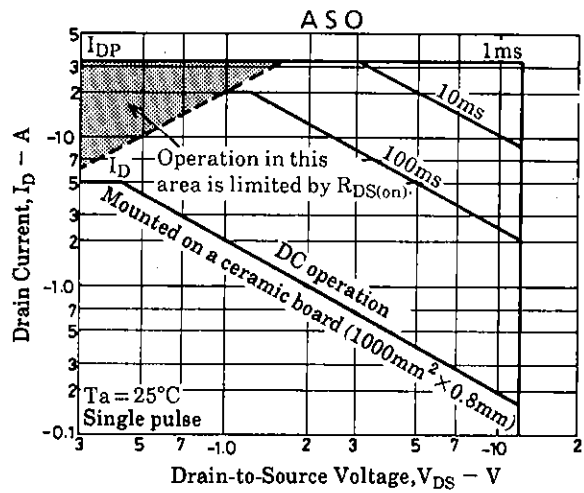
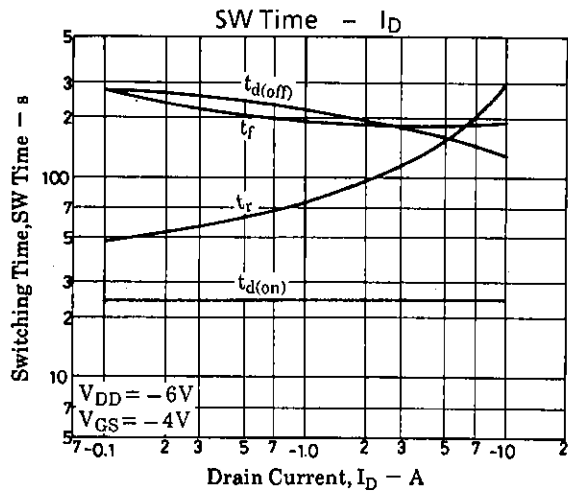
			unit
Drain-to-Source Voltage	$V_{DSS}$	-12	V
Gate-to-Source Voltage	$V_{GSS}$	$\pm 10$	V
Drain Current (DC)	$I_D$	-5	A
Drain Current (Pulse)	$I_{DP}$	$PW \leq 10\mu s, \text{ duty cycle} \leq 1\%$	A
Allowable Power Dissipation	$P_D$	Mounted on a ceramic board (1000mm <sup>2</sup> × 0.8mm)	W
Channel Temperature	$T_{ch}$	150	°C
Storage Temperature	$T_{stg}$	-55 to +150	°C

**Electrical Characteristics at Ta=25°C**

			min	typ	max	unit
D-S Breakdown Voltage	$V_{(BR)DSS}$	$I_D = -1mA, V_{GS} = 0$	-12			V
Zero-Gate Voltage	$I_{DSS}$	$V_{DS} = -10V, V_{GS} = 0$			-100	$\mu A$
Drain Current						
Gate-to-Source Leakage Current	$I_{GSS}$	$V_{GS} = \pm 8V, V_{DS} = 0$			$\pm 10$	$\mu A$
Cutoff Voltage	$V_{GS(off)}$	$V_{DS} = -6V, I_D = -1mA$	-0.4		-1.4	V
Forward Transfer Admittance	$ Y_{fs} $	$V_{DS} = -6V, I_D = -5A$	8	12		S
Static Drain-to-Source ON-State Resistance	$R_{DS(on)1}$	$I_D = -5A, V_{GS} = -4V$		50	63	m $\Omega$
	$R_{DS(on)2}$	$I_D = -2A, V_{GS} = -2.5V$		70	108	m $\Omega$
Input Capacitance	$C_{iss}$	$V_{DS} = -6V, f = 1MHz$		1200		pF
Output Capacitance	$C_{oss}$	$V_{DS} = -6V, f = 1MHz$		1100		pF
Reverse Transfer Capacitance	$C_{rss}$	$V_{DS} = -6V, f = 1MHz$		400		pF
Turn-ON Delay Time	$t_{d(on)}$	See specified Test Circuit.		25		ns
Rise Time	$t_r$	"		150		ns
Turn-OFF Delay Time	$t_{d(off)}$	"		150		ns
Fall Time	$t_f$	"		180		ns
Diode Forward Voltage	$V_{SD}$	$I_S = -5A, V_{GS} = 0$	-1.0	-1.2		V

**Switching Time Test Circuit****Package Dimensions 2116**  
(unit: mm)





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