

Ordering number : ENN7501

P-Channl Silicon MOSFET

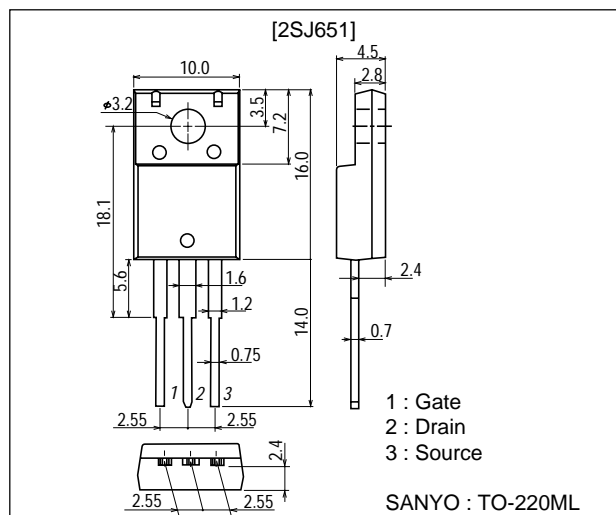
2SJ651**DC / DC Converter Applications****Features**

- Low ON-resistance.
- Ultrahigh-speed switching.
- 4V drive.

Package Dimensions

unit : mm

2063A

**Specifications****Absolute Maximum Ratings** at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	V _{DSS}		-60	V
Gate-to-Source Voltage	V _{GSS}		±20	V
Drain Current (DC)	I _D		-20	A
Drain Current (Pulse)	I _{DP}	PW≤10μs, duty cycle≤1%	-80	A
Allowable Power Dissipation	P _D		2.0	W
		T _c =25°C	25	W
Channel Temperature	T _{ch}		150	°C
Storage Temperature	T _{stg}		-55 to +150	°C

Electrical Characteristics at Ta=25°C

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Drain-to-Source Breakdown Voltage	V _{(BR)DSS}	I _D =-1mA, V _{GS} =0	-60			V
Zero-Gate Voltage Drain Current	I _{DSS}	V _{DS} =-60V, V _{GS} =0			-1	μA
Gate-to-Source Leakage Current	I _{GSS}	V _{GS} =±16V, V _{DS} =0			±10	μA
Cutoff Voltage	V _{GS(off)}	V _{DS} =-10V, I _D =-1mA	-1.2		-2.6	V
Forward Transfer Admittance	y _{fs}	V _{DS} =-10V, I _D =-10A	11	17		S

Marking : J651

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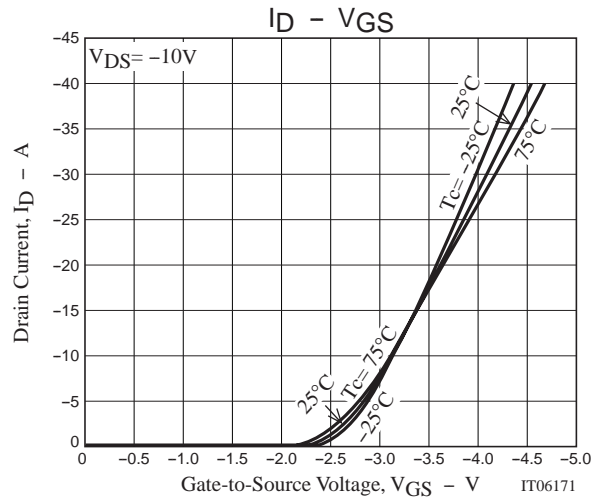
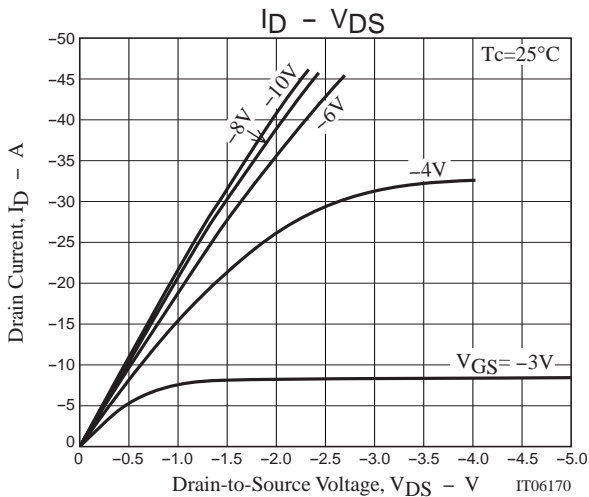
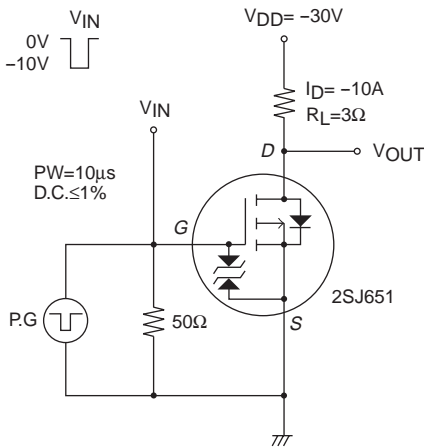
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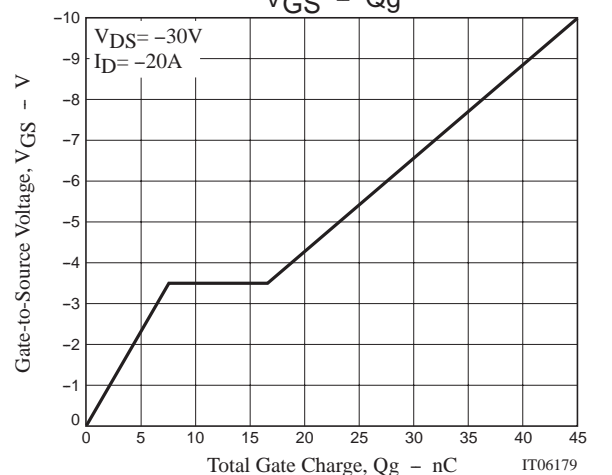
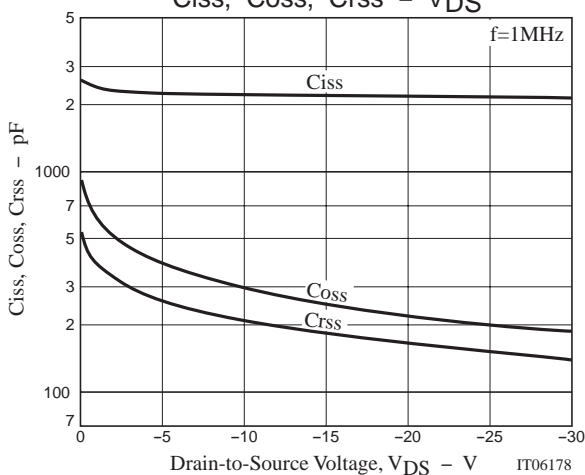
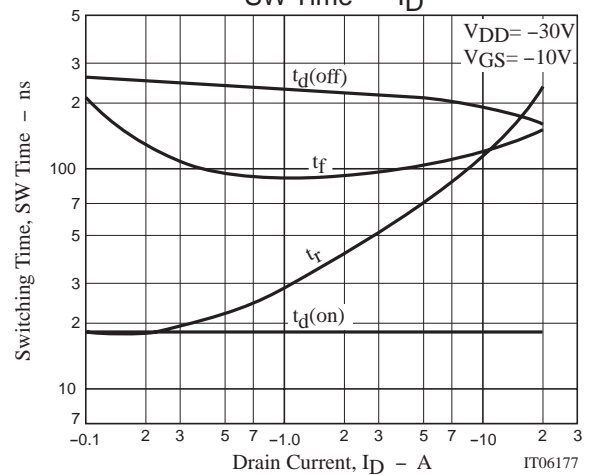
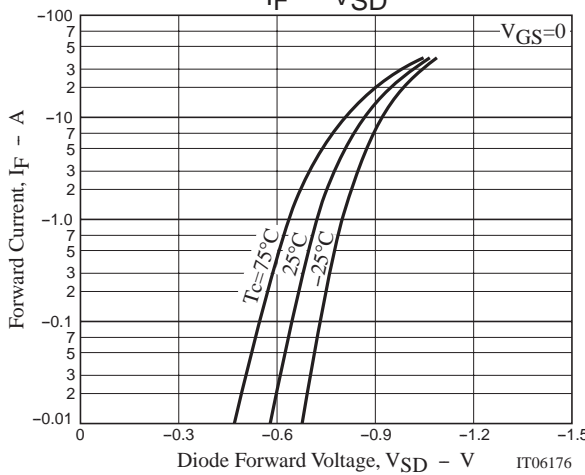
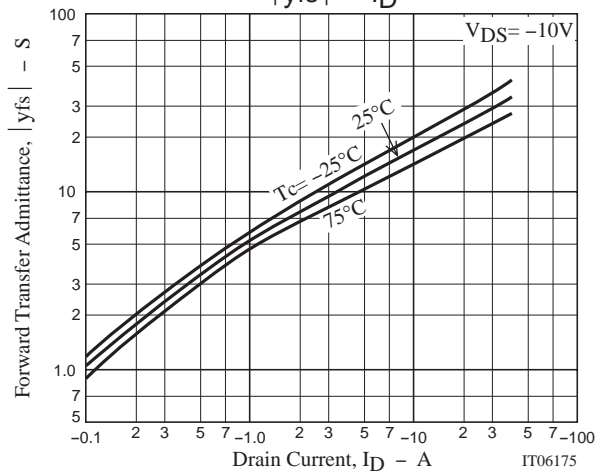
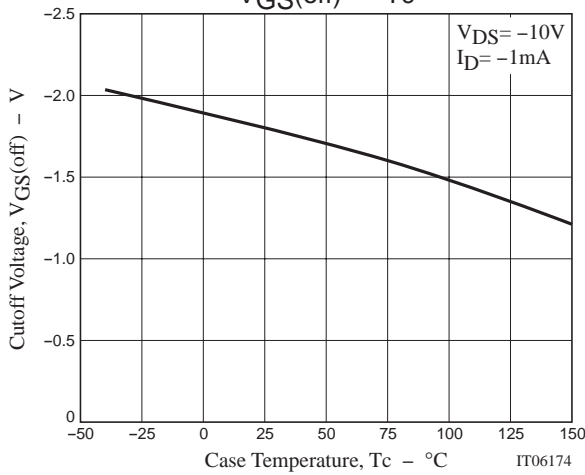
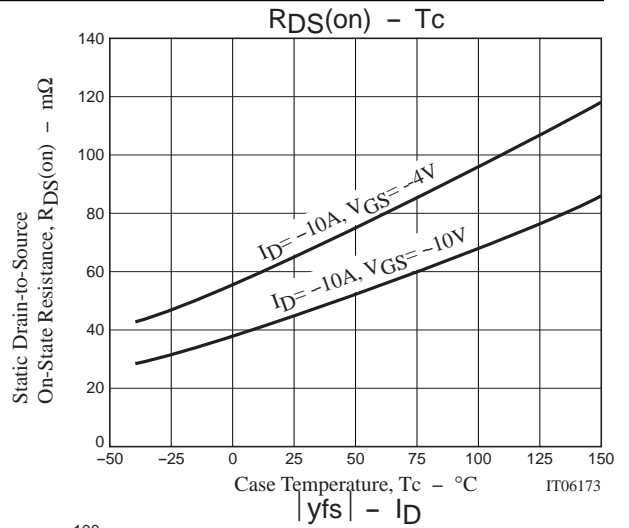
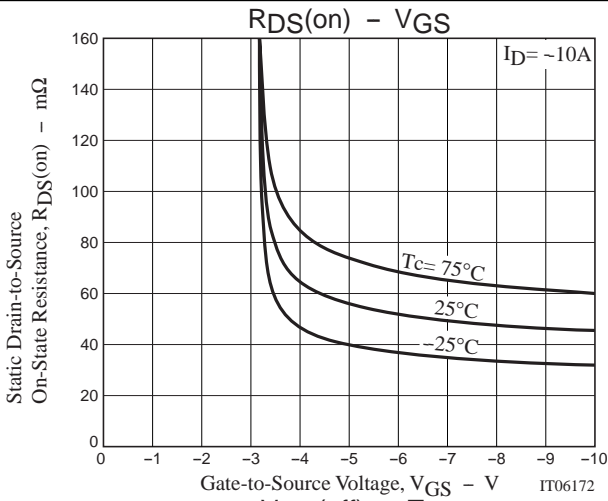
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Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Static Drain-to-Source On-State Resistance	$R_{DS(on)1}$	$I_D = -10A, V_{GS} = -10V$		45	60	$m\Omega$
	$R_{DS(on)2}$	$I_D = -10A, V_{GS} = -4V$		65	92	$m\Omega$
Input Capacitance	C_{iss}	$V_{DS} = -20V, f = 1MHz$		2200		μF
Output Capacitance	C_{oss}	$V_{DS} = -20V, f = 1MHz$		220		μF
Reverse Transfer Capacitance	C_{rss}	$V_{DS} = -20V, f = 1MHz$		165		μF
Turn-ON Delay Time	$t_d(on)$	See specified Test Circuit.		18		ns
Rise Time	t_r	See specified Test Circuit.		115		ns
Turn-OFF Delay Time	$t_d(off)$	See specified Test Circuit.		190		ns
Fall Time	t_f	See specified Test Circuit.		120		ns
Total Gate Charge	Q_g	$V_{DS} = -30V, V_{GS} = -10V, I_D = -20A$		45		nC
Gate-to-Source Charge	Q_{gs}	$V_{DS} = -30V, V_{GS} = -10V, I_D = -20A$		7.4		nC
Gate-to-Drain "Miller" Charge	Q_{gd}	$V_{DS} = -30V, V_{GS} = -10V, I_D = -20A$		9		nC
Diode Forward Voltage	V_{SD}	$I_S = -20A, V_{GS} = 0$		-0.95	-1.2	V

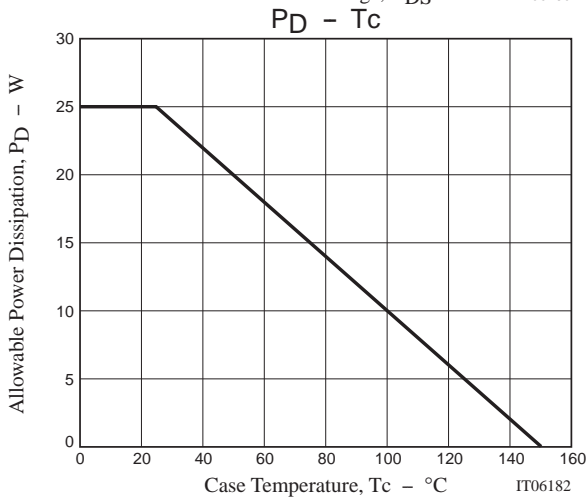
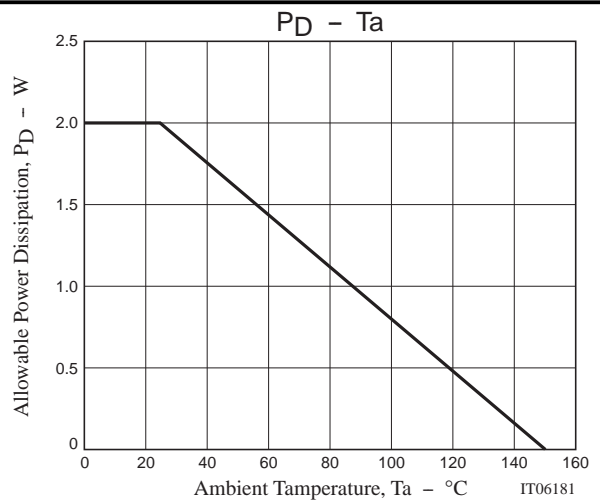
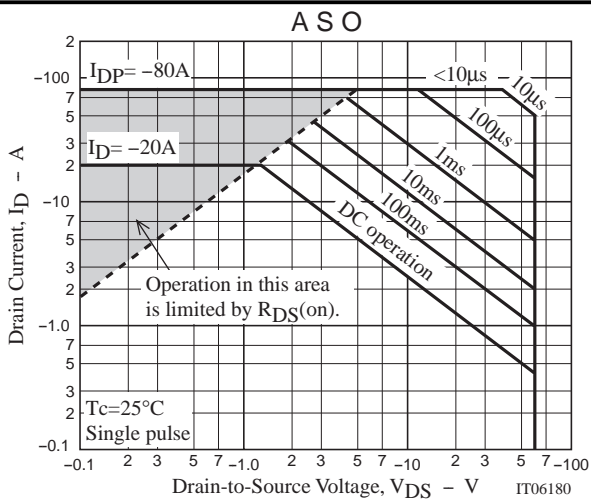
Switching Time Test Circuit



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