Ordering number:EN3563

N-Channel Silicon MOSFET

2SK1425

Ultrahigh-Speed Switching Applications

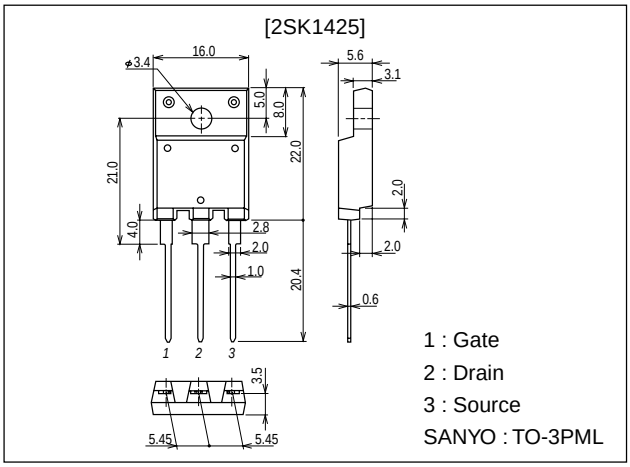
Features

- Low ON-state resistance.
- Ultrahigh-speed switching.
- Converters.
- Micaless package facilitating easy mounting.

Package Dimensions

unit:mm

2076B



Specifications

Absolute Maximum Ratings at Ta = 25˚C

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	V_{DSS}		60	V
Gate-to-Source Voltage	V_{GSS}		±20	V
Drain Current (DC)	I_D		60	A
Drain Current (Pulse)	I_{DP}	PW≤10μs, duty cycle≤1%	240	A
Allowable Power Dissipation	P_D	Tc=25°C	80	W
			3.0	W
Channel Temperature	Tch		150	˚C
Storage Temperature	Tstg		–55 to +150	˚C

Electrical Characteristics at Ta = 25˚C

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Drain-to-Source Breakdown Voltage	$V_{(BR)DSS}$	I_D=1mA, V_{GS}=0	60			V
Zero-Gate Voltage Drain Current	I_{DSS}	V_{DS}=60V, V_{GS}=0			100	μA
Gate-to-Source Leakage Current	I_{GSS}	V_{GS}=±20V, V_{DS}=0			±100	nA
Cutoff Voltage	$V_{GS(off)}$	V_{DS}=10V, I_D=1mA	1.5		2.5	V
Forward Transfer Admittance	| yfs |	V_{DS}=10V, I_D=50A	30	50		S
Static Drain-to-Source ON-State Resistance	$R_{DS(on)}$	I_D=50A, V_{GS}=10V		0.012	0.016	Ω

(Note) Be careful in handling the 2SK1425 because it has no protection diode between gate and source.

Continued on next page.

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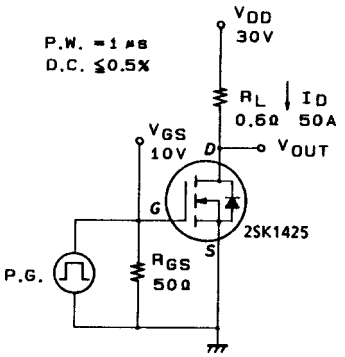
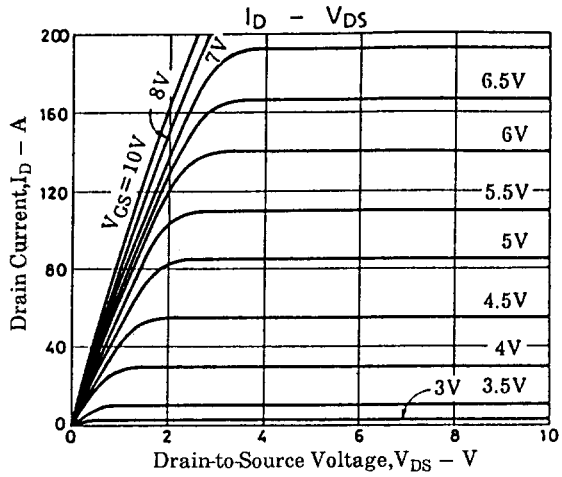
TOKYO OFFICE Tokyo Bldg., 1-10, 1 Chome, Ueno, Taito-ku, TOKYO, 110-8534 JAPAN

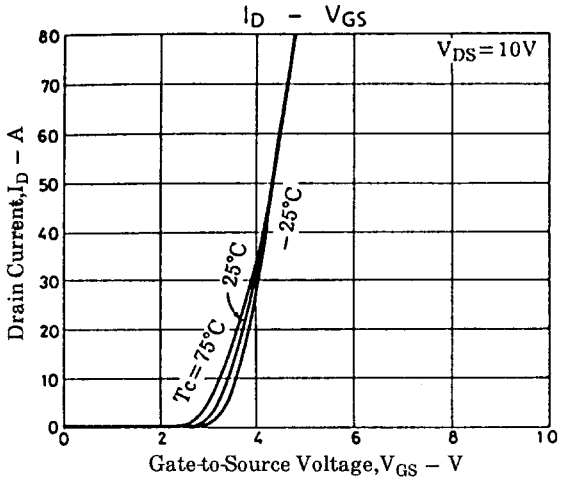
Continued from preceding page.

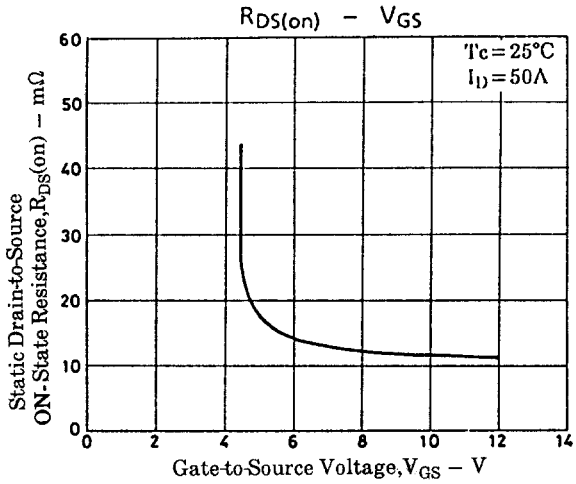
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Input Capacitance	Ciss	V_{DS}=20V, f=1MHz		4800		pF
Output Capacitance	Coss	V_{DS}=20V, f=1MHz		2200		pF
Reverse Transfer Capacitance	Crss	V_{DS}=20V, f=1MHz		600		pF
Turn-ON Delay Time	$t_{d(on)}$	I_D=50A, V_{GS}=10V, V_{DD}=30V, R_{GS}=50Ω		55		ns
Rise Time	t_r	I_D=50A, V_{GS}=10V, V_{DD}=30V, R_{GS}=50Ω		345		ns
Turn-OFF Delay Time	$t_{d(off)}$	I_D=50A, V_{GS}=10V, V_{DD}=30V, R_{GS}=50Ω		450		ns
Fall Time	t_f	I_D=50A, V_{GS}=10V, V_{DD}=30V, R_{GS}=50Ω		400		ns
Diode Forward Voltage	V_{SD}	I_S=60A, V_{GS}=0			1.8	V

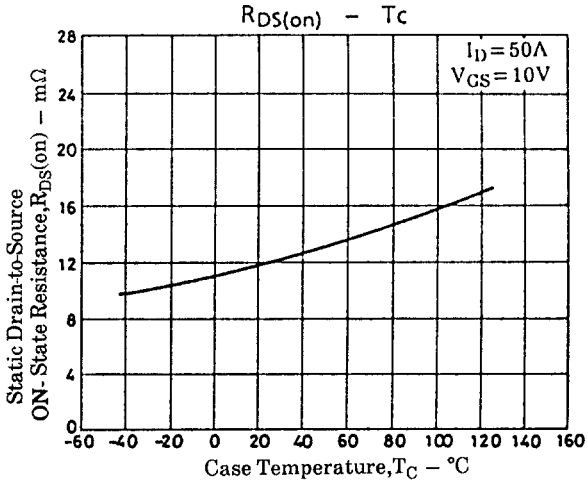
Switching Time Test Circuit

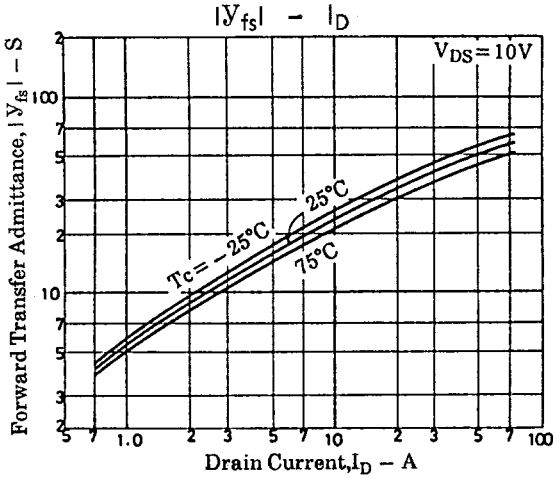


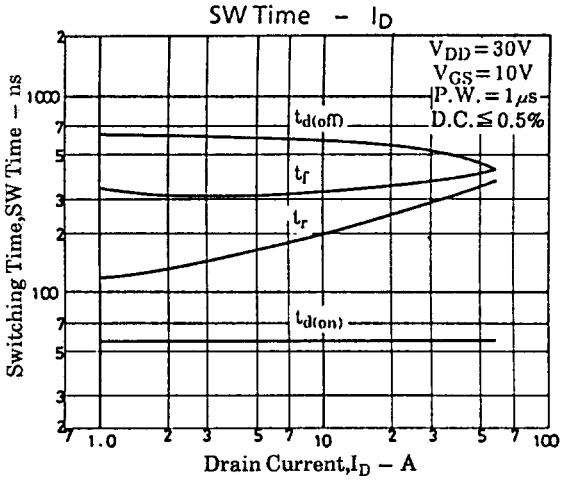


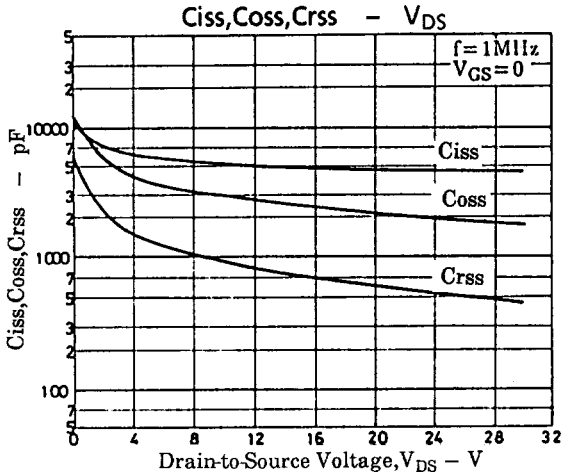


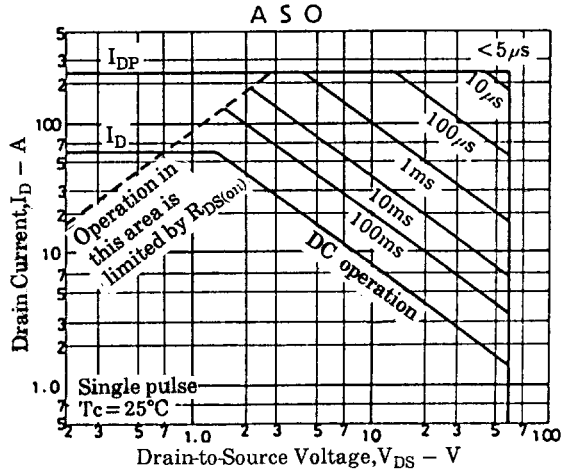


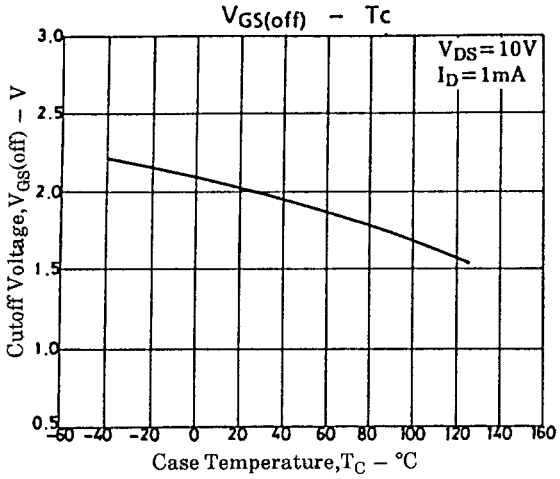


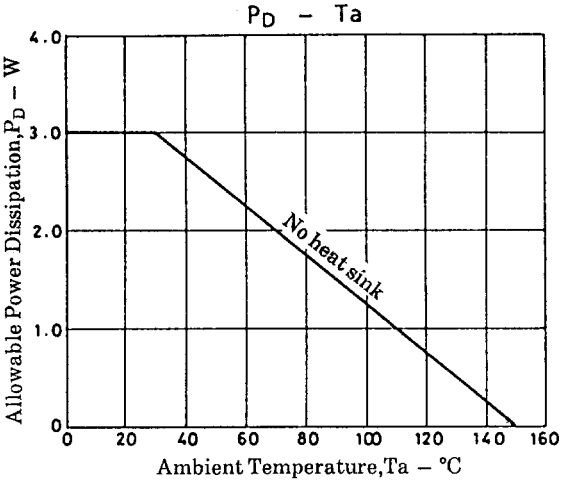

|yfs| – ID
VDS=10V
Tc=–25°C
25°C
75°C
Forward Transfer Admittance, |yfs| – S
Drain Current, ID – A

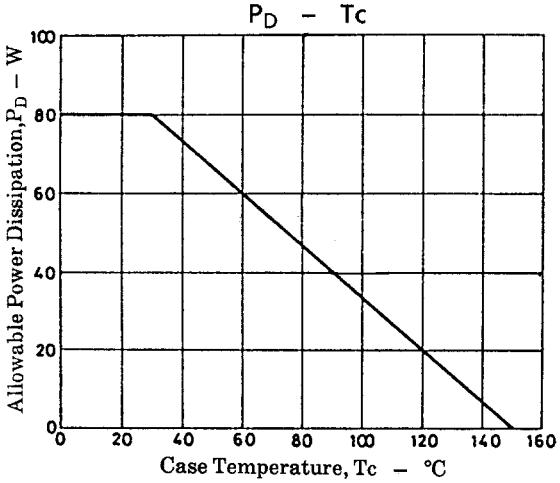

SW Time – ID
VDD=30V
VGS=10V
P.W.=1μs
D.C.≦0.5%
td(off)
tf
tr
td(on)
Switching Time, SW Time – ns
Drain Current, ID – A


Ciss, Coss, Crss – VDS
f=1MHz
VGS=0
Ciss
Coss
Crss
Ciss, Coss, Crss – pF
Drain-to-Source Voltage, VDS – V


A S O
IDP
ID
<5μs
10μs
100μs
1ms
10ms
100ms
DC operation
Operation in this area is limited by RDS(on)
Single pulse
Tc=25°C
Drain Current, ID – A
Drain-to-Source Voltage, VDS – V


VGS(off) – Tc
VDS=10V
ID=1mA
Cutoff Voltage, VGS(off) – V
Case Temperature, TC – °C


PD – Ta
No heat sink
Allowable Power Dissipation, PD – W
Ambient Temperature, Ta – °C


PD – Tc
Allowable Power Dissipation, PD – W
Case Temperature, Tc – °C