2SK1629-E1-E

500V - 30A - MOS FET
High Speed Power Switching

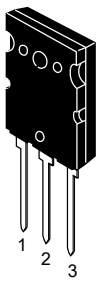
R07DS1197EJ0300
Rev.3.00
Feb.4.2022

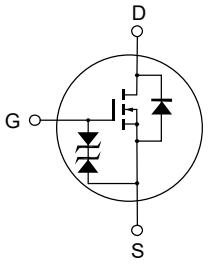
Features

- Low on-resistance
  $R_{DS(on)}$ = 0.22 Ω typ. (at I_D = 15 A, V_{GS} = 10 V, Ta = 25°C)
- High speed switching
- Low drive current
- Suitable for switching regulator and DC-DC converter
- Quality grade: Standard

Outline

RENESAS Package code: PRSS0003ZN-A, PRSS0003ZC-A
(Package name:TO-264A, TO-264)





1. Gate
2. Drain
3. Source

Absolute Maximum Ratings

(Ta = 25 °C)

Item	Symbol	Ratings	Unit
Drain to source voltage	V_{DSS}	500	V
Gate to source voltage	V_{GSS}	±30	V
Drain current	I_D	30	A
Drain peak current	$I_{D(pulse)}$ Notes1	120	A
Body-drain diode reverse drain current	I_{DR}	30	A
Channel dissipation	Pch Notes2	200	W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	–55 to +150	°C

Note: Continuous heavy condition (e.g. high temperature/voltage/current or high variation of temperature) may affect a reliability even if it is within the absolute maximum ratings. Please consider derating condition for appropriate reliability in reference Renesas Semiconductor Reliability Handbook (Recommendation for Handling and Usage of Semiconductor Devices) and individual reliability data.

Notes: 1. PW ≤ 10 μs, duty cycle ≤ 1 %
2. Value at Tc = 25 °C

Electrical Characteristics

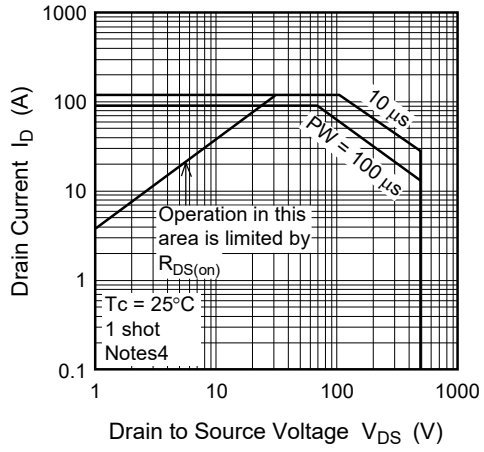
(Ta = 25 °C)

Item	Symbol	Min	Typ	Max	Unit	Test conditions
Drain to source breakdown voltage	$V_{(BR)DSS}$	500	—	—	V	I_D = 10 mA, V_{GS} = 0
Gate to source breakdown voltage	$V_{(BR)GSS}$	±30	—	—	V	I_G = ±100 μA, V_{DS} = 0
Gate to source leak current	I_{GSS}	—	—	±10	μA	V_{GS} = ±25 V, V_{DS} = 0
Zero gate voltage drain current	I_{DSS}	—	—	250	μA	V_{DS} = 400 V, V_{GS} = 0
Gate to source cutoff voltage	$V_{GS(off)}$	2.0	—	3.0	V	I_D = 1 mA, V_{DS} = 10 V
Static drain to source on state resistance	$R_{DS(on)}$	—	0.22	0.27	Ω	I_D = 15 A, V_{GS} = 10 V Notes3
Forward transfer admittance	$\|y_{fs}\|$	12	20	—	S	I_D = 15 A, V_{DS} = 10 V Notes3
Input capacitance	Ciss	—	2800	—	pF	V_{DS} = 10 V V_{GS} = 0 f = 1 MHz
Output capacitance	Coss	—	780	—	pF	
Reverse transfer capacitance	Crss	—	90	—	pF	
Turn-on delay time	$t_{d(on)}$	—	32	—	ns	I_D = 15 A V_{GS} = 10 V R_L = 2 Ω
Rise time	t_r	—	140	—	ns	
Turn-off delay time	$t_{d(off)}$	—	200	—	ns	
Fall time	t_f	—	100	—	ns	
Body-drain diode forward voltage	V_{DF}	—	1.1	—	V	I_F = 30 A, V_{GS} = 0
Body-drain diode reverse recovery time	t_{rr}	—	600	—	ns	I_F = 30 A, V_{GS} = 0 di_F/dt = 100 A/μs

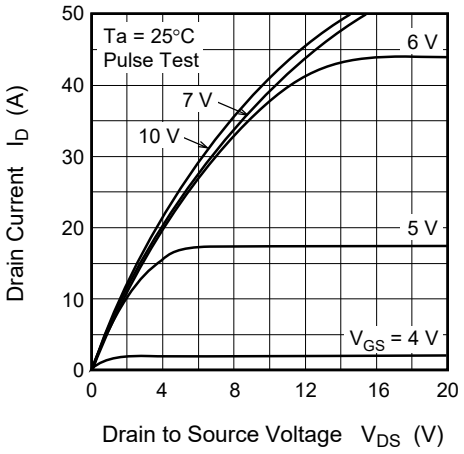
Notes: 3. Pulse test

Main Characteristics

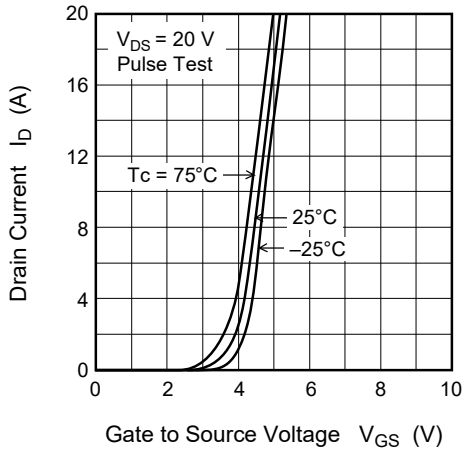
Maximum Safe Operation Area



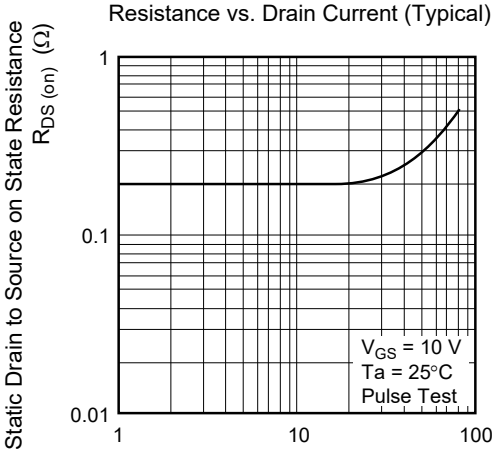
Typical Output Characteristics



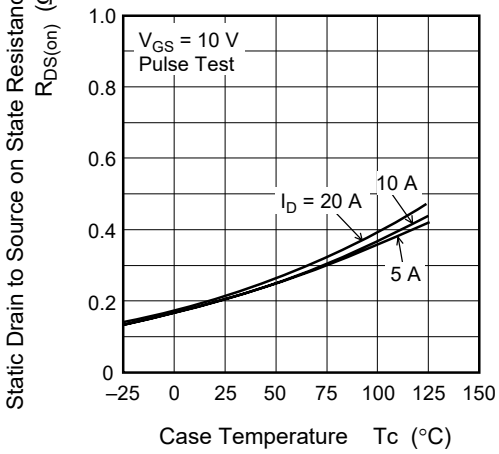
Typical Transfer Characteristics



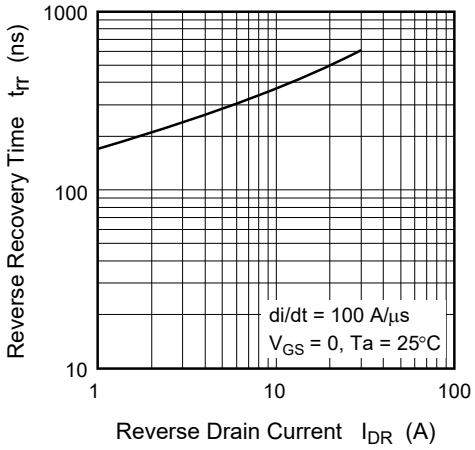
Static Drain to Source on State Resistance vs. Drain Current (Typical)



Static Drain to Source on State Resistance vs. Temperature (Typical)

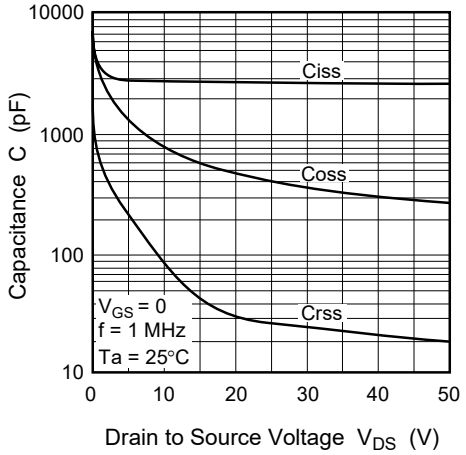


Body-Drain Diode Reverse Recovery Time (Typical)

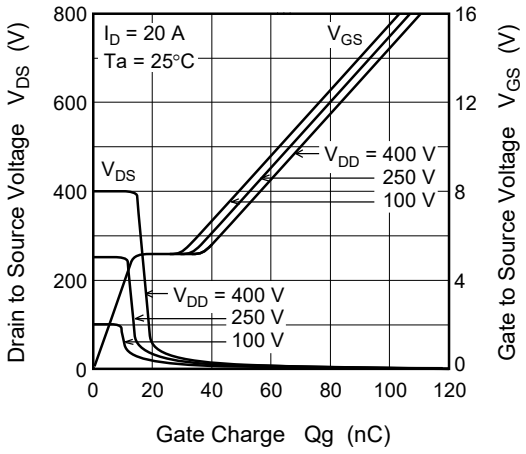


Notes: 4. Designed target value on Renesas measurement condition. (Not tested)
Renesas recommends that operating conditions are designed according to a document “Power MOS FET・IGBT Attention of Handling Semiconductor Devices”.

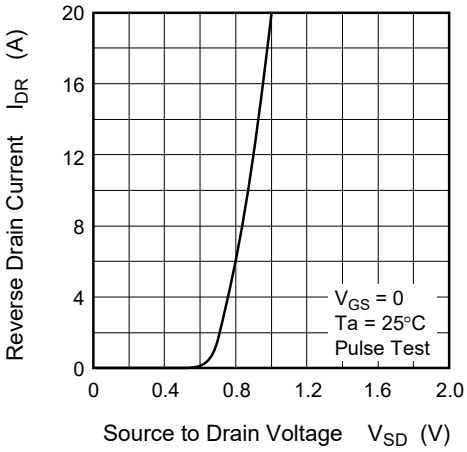
Typical Capacitance vs.
Drain to Source Voltage



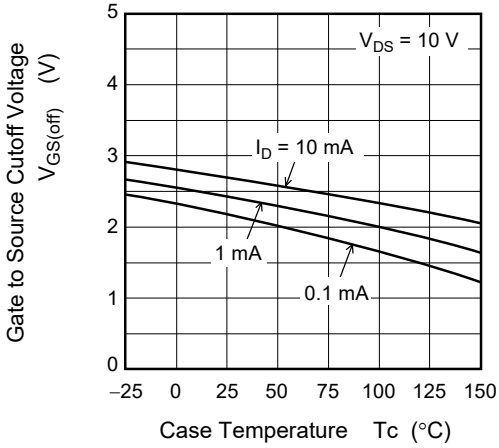
Dynamic Input Characteristics (Typical)



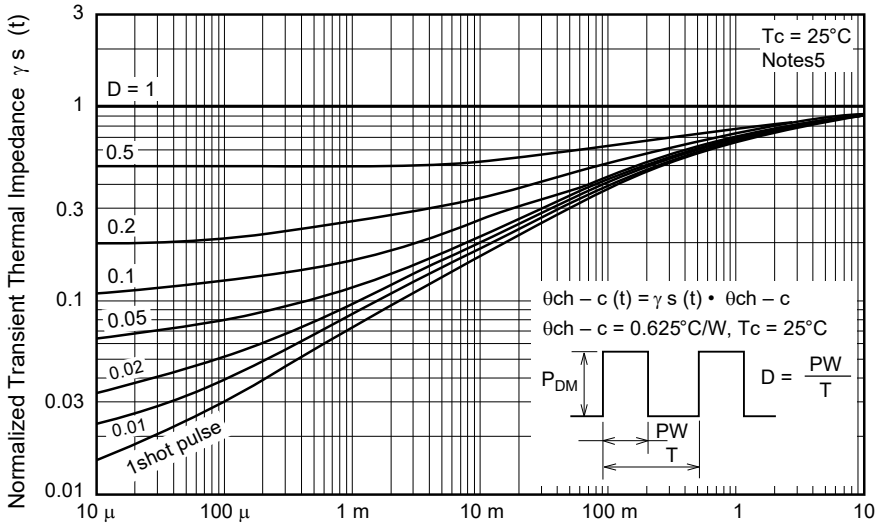
Reverse Drain Current vs.
Source to Drain Voltage (Typical)



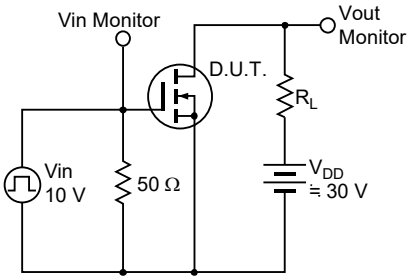
Gate to Source Cutoff Voltage
vs. Case Temperature (Typical)



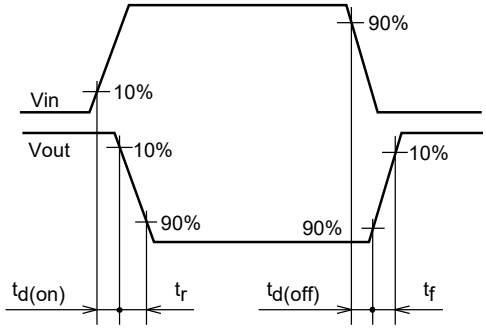
Normalized Transient Thermal Impedance vs. Pulse Width



Switching Time Test Circuit



Waveform



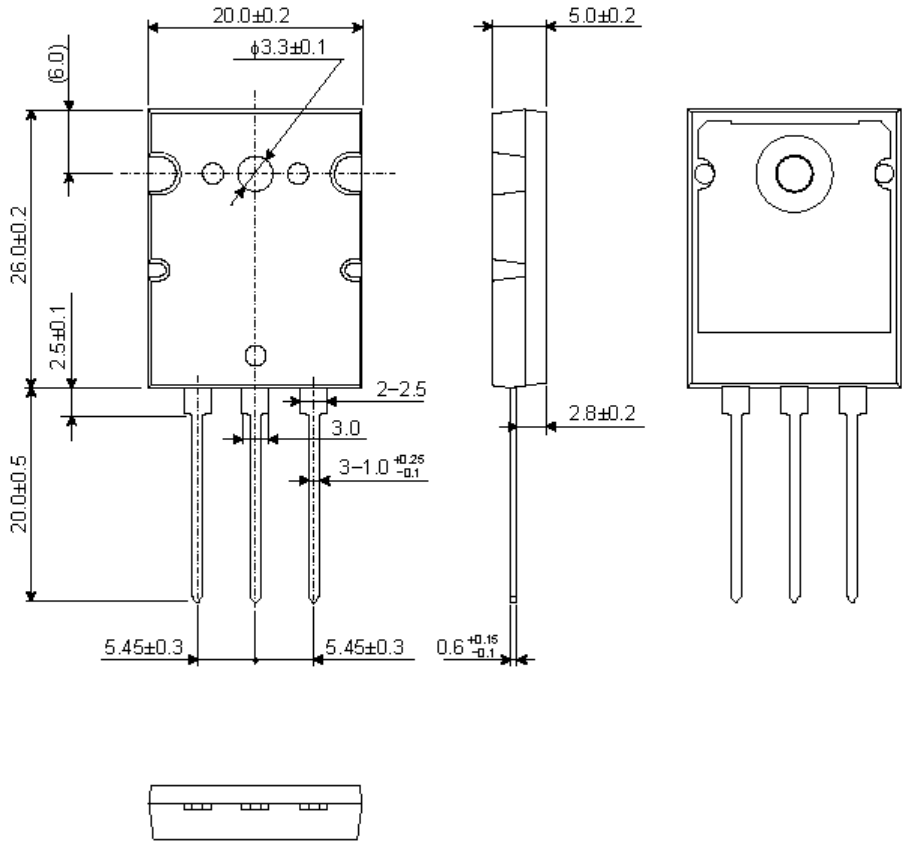
Notes: 5. Designed target value on Renesas measurement condition. (Not tested)

Package Dimensions

ASSEMBLED IN CHINA

Package Name	JEITA Package Code	RENESAS Code	Previous Code	MASS (Typ) [g]
TO-264A	—	PRSS0003ZN-A	TO-264A	9.7

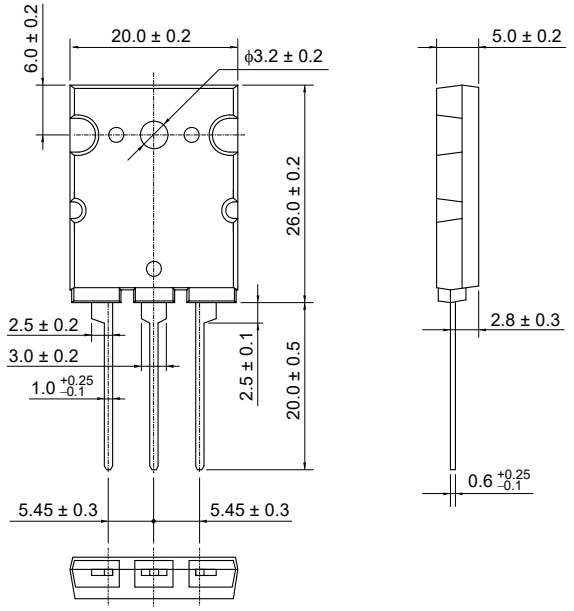
Unit: mm



ASSEMBLED IN KOREA

Package Name	JEITA Package Code	RENESAS Code	Previous Code	MASS[Typ.]
TO-264	—	PRSS0003ZC-A	TO-264S	9.48g

Unit: mm



Ordering Information

Orderable Part No.	Quantity	Shipping Container
2SK1629-E1-E#T2	25 pcs	Tube

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(Rev. 5.0-1 October 2020)

Corporate Headquarters

TOYOSU FORESIA, 3-2-24 Toyosu,
Koto-ku, Tokyo 135-0061, Japan

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