

SANYO Semiconductors DATA SHEET

2SK2170 N-Channel Junction Silicon FET Impedance Converter Applications

Applications

· Low-frequency amplifier, analog switch, constant current source.

Features

• Ultrasmall-sized package permitting 2SK2170 applied sets to be made small and slim.

Specifications

Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	VDSX		30	V
Gate-to-Drain Voltage	V _{GDS}		-30	V
Gate Current	IG		10	mA
Drain Current	ID		20	mA
Allowable Power Dissipation	PD		100	mW
Junction Temperature	Tj		150	°C
Storage Temperature	Tstg		-55 to +150	°C

Electrical Characteristics at Ta=25°C

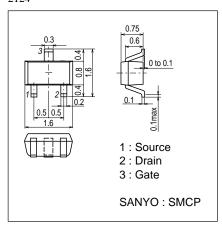
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Offic
Gate-to-Drain Breakdown Voltage	V(BR)GDS	IG=-10μA, VDS=0	-30			V
Gate-to-Source Leakage Current	IGSS	V _{GS} =-20V, V _{DS} =0			-1.0	nA
Cutoff Voltage	VGS(off)	V _{DS} =10V, I _D =1μA	-0.2	-0.7	-2.5	V
Zero-Gate Voltage Drain Current	IDSS	V _{DS} =10V, V _{GS} =0	1.2		4.0	mA
Forward Transfer Admittance	yfs	V _{DS} =10V, V _{GS} =0, f=1kHz	2.5	5.0		mS
Input Capacitance	Ciss	V _{DS} =10V, V _{GS} =0, f=1kHz		5.0		pF
Reverse Transfer Capacitance	Crss	V _{DS} =10V, V _{GS} =0, f=1kHz		0.9		pF
Static Drain-to-Source ON-State Resistance	R _{DS} (on)	V _{DS} =10mV, V _{GS} =0		250		Ω

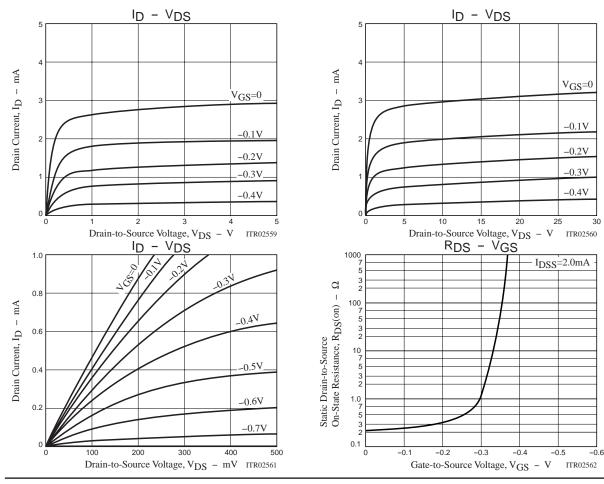
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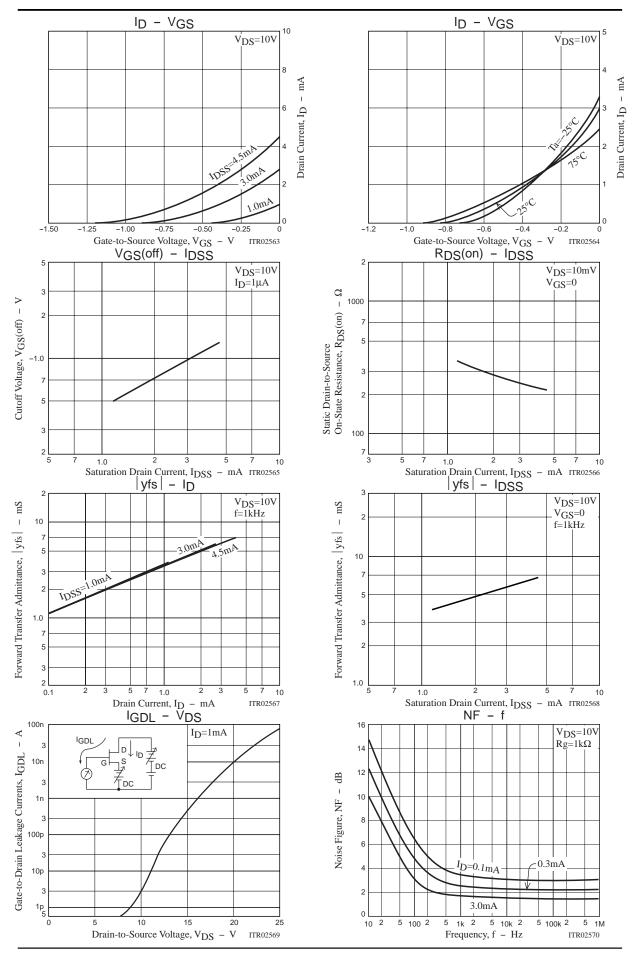
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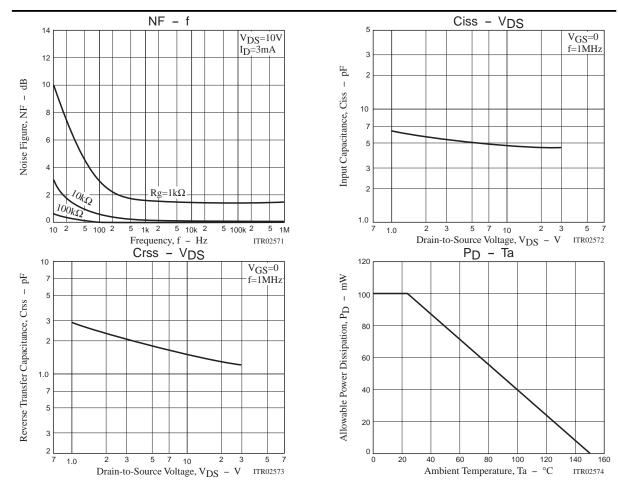
Package Dimensions

unit : mm 2124









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