TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (L^2 - π -MOSV)

2SK2350

Switching Regulator, DC-DC Converter and Motor Drive Applications

• 4-V gate drive

• Low drain-source ON-resistance : $R_{DS(ON)} = 0.26 \Omega$ (typ.)

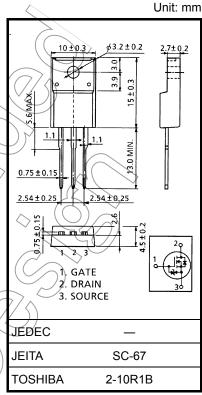
• High forward transfer admittance : |Y_{fs}| = 8 S (typ.)

Low leakage current : I_{DSS} = 100 μA (max) (V_{DS} = 200 V)

Enhancement mode : V_{th} = 1.5 to 3.5 V (V_{DS} = 10 V, I_D = 1 mA)

Absolute Maximum Ratings (Ta = 25°C)

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Characteri	stics	Symbol	Rating	Unit
Drain-source voltage		V _{DSS}	200	À
Drain-gate voltage (R	_{GS} = 20 kΩ)	V _{DGR}	200	> v
Gate-source voltage	_	V _{GSS}	±20	٧
Drain current	DC (Note 1	l _D	8.5	A
	Pulse (Note 1	I _{DP}	34	A
Drain power dissipatio	n (Tc = 25°C)	PD	30	<\w
Single pulse avalanche	e energy (Note 2)	EAS	110	mJ
Avalanche current		(IAR (8.5	A
Repetitive avalanche	energy (Note 3	EAR	3	/m/
Channel temperature		√\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\	150	$\widetilde{\mathbb{C}}$
Storage temperature r	ange	7 _{stg}	-55 to 150	°C



Weight: 1.9 g (typ.)

Note: Using continuously under heavy-loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Thermal Characteristics

Characteristics	\$ymbol	Max	Unit
Thermal resistance, channel to case	R _{th (ch-c)}	4.16	°C / W
Thermal resistance, channel to ambient	R _{th (ch-a)}	62.5	°C / W

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: V_{DD} = 50 V, T_{ch} = 25°C (initial), L = 2.47 mH, R_G = 25 Ω , I_{AR} = 8.5 A

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device.

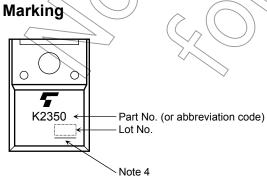
Please handle with caution.

Electrical Characteristics (Ta = 25°C)

Charac	eteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cu	rrent	I _{GSS}	V _{GS} = ±16 V, V _{DS} = 0 V	_	_	±10	μΑ
Drain cut-off cur	rrent	I _{DSS}	V _{DS} = 200 V, V _{GS} = 0 V	_	_	100	μΑ
Drain-source br	eakdown voltage	V (BR) DSS	I _D = 10 mA, V _{GS} = 0 V	200	_	_	V
Gate threshold v	roltage	V _{th}	V _{DS} = 10 V, I _D = 1 mA	1.5	_	3.5	V
Drain-source Ol	N-resistance	R _{DS} (ON)	V _{GS} = 10 V, I _D = 5 A	1	0.26	0.4	Ω
Forward transfer	admittance	Y _{fs}	V _{DS} = 10 V, I _D = 5 A	74	8	_	S
Input capacitano	e	C _{iss}		$\bigcirc)$	700	_	
Reverse transfer	r capacitance	C _{rss}	V _{DS} = 10 V, V _{GS} = 0 V, f = 1 MHz		80	_	pF
Output capacitance		Coss		_	270	_	
Switching time	Rise time	t _r	$V_{GS} = V_{OUT}$ $V_{GS} = V_{OUT}$ $R_{L} = V_{OUT}$ V_{OUT} V_{OUT} V_{OUT} V_{OUT} V_{OUT} V_{OUT}		15	//	- ns
	Turn-on time	t _{on}		. (25	> _	
	Fall time	t _f			15	_	
	Turn-off time	t _{off}	$V_{DD} = 100V$ $Duty \leq 1\%, t_{W} = 10\mu s$	2	70	_	
Total gate charge (Gate-source plus gate-drain)		Qg) _	17		
Gate-source charge		Q _{gs}	$V_{DD} \approx 160 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 10 \text{ A}$	_	10		nC
Gate-drain ("miller") charge		Q _{gd}		_	7	_	

Source-Drain Ratings and Characteristics (Ta = 25°C)

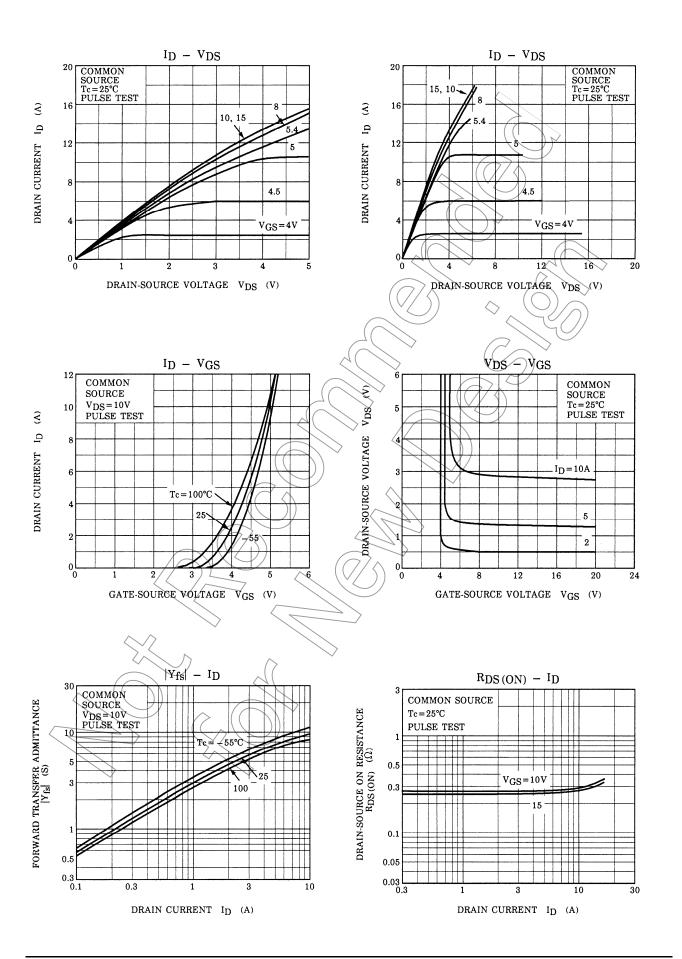
Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	IDR		_	_	8.5	Α
Pulse drain reverse current (Note 1)	IDRP	_	_	_	34	Α
Forward voltage (diode)	V _{DSF}	$I_{DR} = 10 \text{ A}, V_{GS} = 0 \text{ V}$	_	_	-2.0	V
Reverse recovery time	t _{rr}	I _{DR} = 10 A, V _{GS} = 0 V	_	150	_	ns
Reverse recovered charge	Q _{rr}	dl _{DR} / dt = 100 Å / μs	_	0.8	_	μC



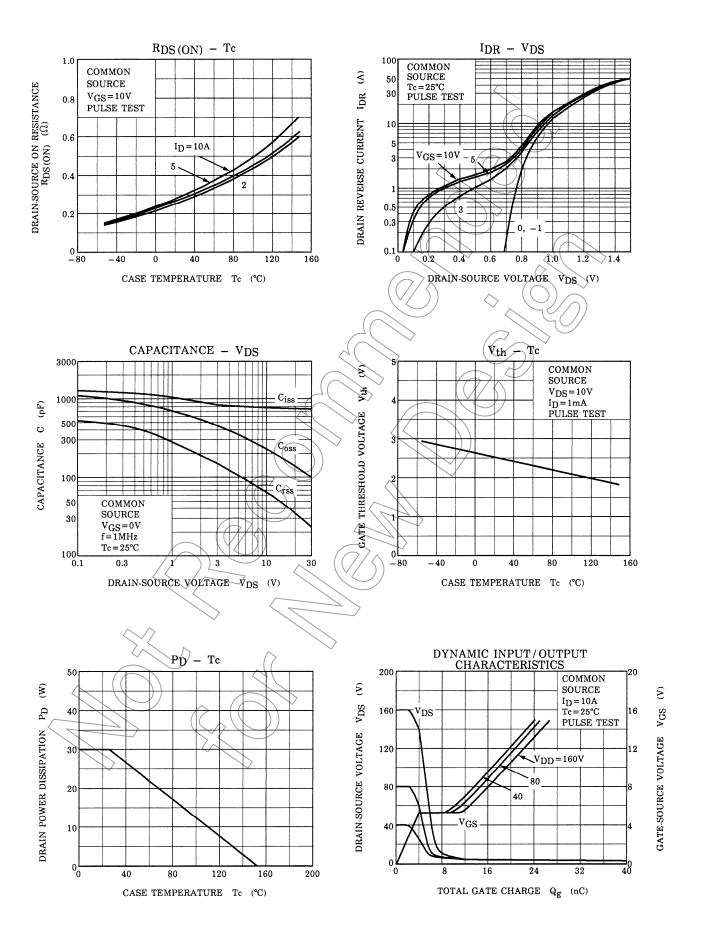
Note 4: A line under a Lot No. identifies the indication of product Labels.

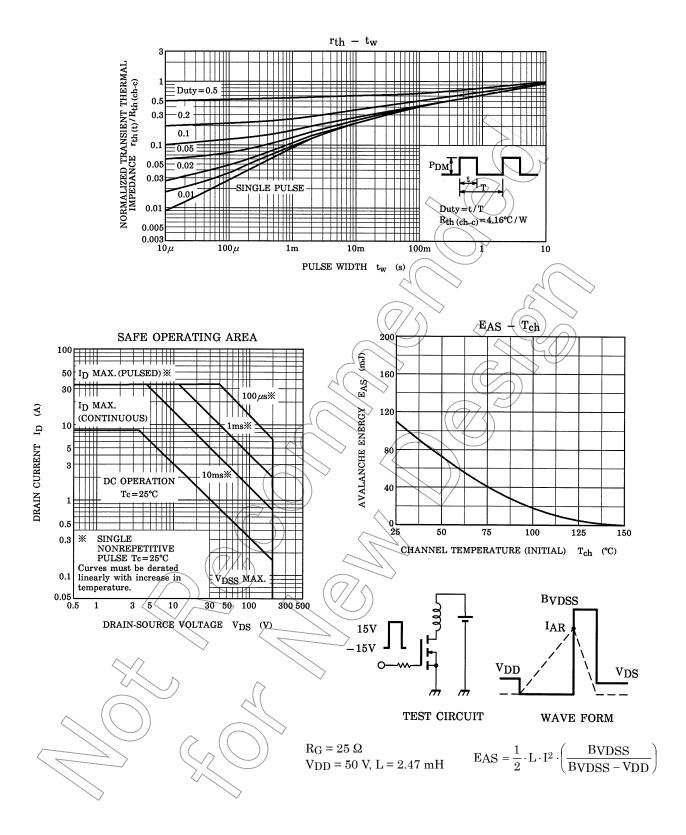
Not underlined: [[Pb]]/INCLUDES > MCV Underlined: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

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