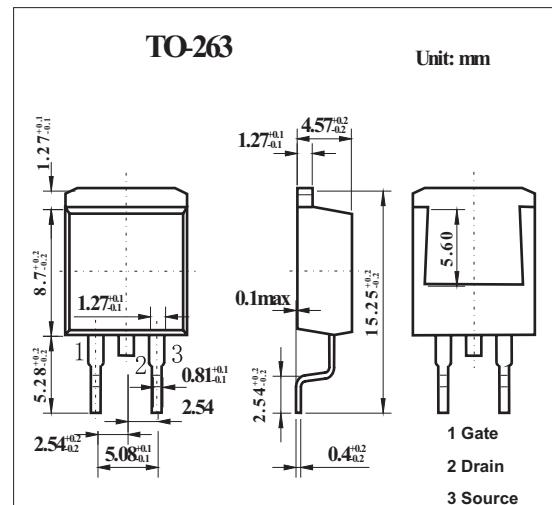


## MOS Field Effect Transistors

## 2SK2484



## ■ Features

- Low On-state Resistance:  $R_{DS(on)} = 2.8 \Omega$  max. ( $V_{GS} = 10V, I_D = 3.0A$ )
- Low Ciss:  $C_{iss} = 1200pF$  TYP
- High Avalanche Capability Ratings

■ Absolute Maximum Ratings  $T_a = 25^\circ\text{C}$ 

Parameter	Symbol	Rating	Unit
Drain to Source Voltage	$V_{DSS}$	900	V
Gate to Source Voltage	$V_{GSS}$	$\pm 30$	V
Drain Current(DC)	$I_D(DS)$	$\pm 5$	A
Drain Current(pulse) *1	$I_D(\text{pulse})$	$\pm 10$	A
Total Power Dissipation $T_a = 25^\circ\text{C}$	$P_T$	1.5	W
Total Power Dissipation $T_c = 25^\circ\text{C}$		75	
Channel Temperature	$T_{ch}$	150	$^\circ\text{C}$
Storage temperature	$T_{stg}$	-55 to +150	$^\circ\text{C}$
Single Avalanche Current *2	$I_{AS}$	5	A
Single Avalanche Energy *2	$E_{AS}$	75	mJ

\*1. PW  $\leqslant 10\mu\text{s}$ , Dduty cycle  $\leqslant 1\%$ .

\*2. Starting  $T_{ch}=25^\circ\text{C}$ ,  $R_G=25\Omega$ ,  $V_{GS}=20V \rightarrow 0$

**2SK2484**

■ Electrical Characteristics Ta = 25°C

Parameter	Symbol	Testconditons	Min	Typ	Max	Unit
Drain to Source On-state Resistance	R <sub>D5(on)</sub>	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 3.0 A		2.2	2.8	Ω
Gate Cut-off Voltage	V <sub>GS(off)</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 1 mA	2.5		3.5	V
Forward Transfer Admittance	Y <sub>fs</sub>	V <sub>DS</sub> = 20 V, I <sub>D</sub> = 3.0 A	2.0			S
Drain Cut-off Current	I <sub>DSS</sub>	V <sub>DS</sub> = V <sub>DSS</sub> , V <sub>GS</sub> = 0			100	μA
Gate Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> = 30 V, V <sub>DS</sub> = 0			±100	nA
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> = 10 V		1200		pF
Output Capacitance	C <sub>oss</sub>	V <sub>GS</sub> = 0		170		pF
Feedback Capacitance	C <sub>rss</sub>	f = 1 MHz		30		pF
Turn-on Delay Time	t <sub>d(on)</sub>	I <sub>D</sub> = 3.0 A		20		ns
Rise Time	t <sub>r</sub>	V <sub>GS</sub> = 10 V		10		ns
Turn-off Delay Time	t <sub>d(off)</sub>	V <sub>DD</sub> = 150 V		70		ns
Fall Time	t <sub>f</sub>	R <sub>G</sub> = 10 Ω		15		ns
Total Gate Charge	Q <sub>g</sub>	I <sub>D</sub> = 5.0 A		40		nC
Gate-Source Charge	Q <sub>gs</sub>	V <sub>DD</sub> = 450 V		7		
Gate-Drain Charge	Q <sub>gd</sub>	V <sub>GS</sub> = 10 V		17		
Diode Forward Voltage	V <sub>F(S-D)</sub>	I <sub>F</sub> = 5.0 A, V <sub>GS</sub> = 0		1.0		V
Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> = 5.0 A, V <sub>GS</sub> = 0		670		ns
Reverse Recovery Charge	Q <sub>rr</sub>	di/dt = 50 A/ μs		3.5		μC