

2SK2737

Silicon N Channel MOS FET
High Speed Power Switching

HITACHI

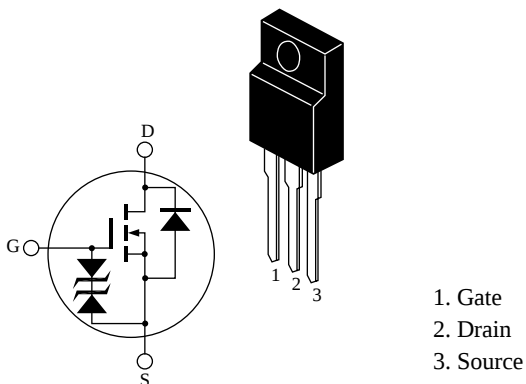
ADE-208-533B(Z)
3rd. Edition
Jun 1998

Features

- Low on-resistance
 $R_{DS(on)} = 10 \text{ m}\Omega$ typ.
- 4V gate drive devices.
- High speed switching

Outline

TO-220CFM



Absolute Maximum Ratings (Ta = 25°C)

Item	Symbol	Ratings	Unit
Drain to source voltage	V_{DSS}	30	V
Gate to source voltage	V_{GSS}	±20	V
Drain current	I_D	45	A
Drain peak current	$I_{D(pulse)}$ ^{Note1}	180	A
Body-drain diode reverse drain current	I_{DR}	45	A
Channel dissipation	Pch ^{Note2}	30	W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	–55 to +150	°C

Note: 1. $PW \leq 10\mu s$, duty cycle $\leq 1\%$

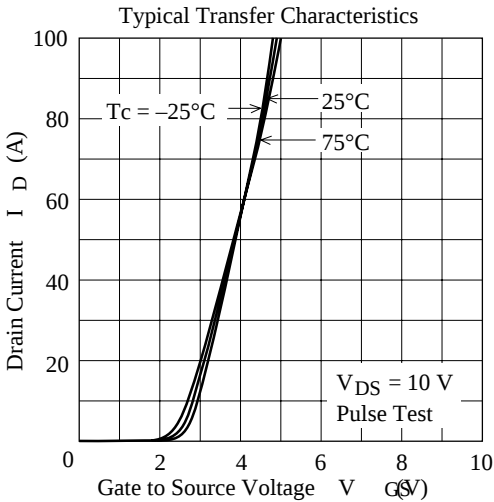
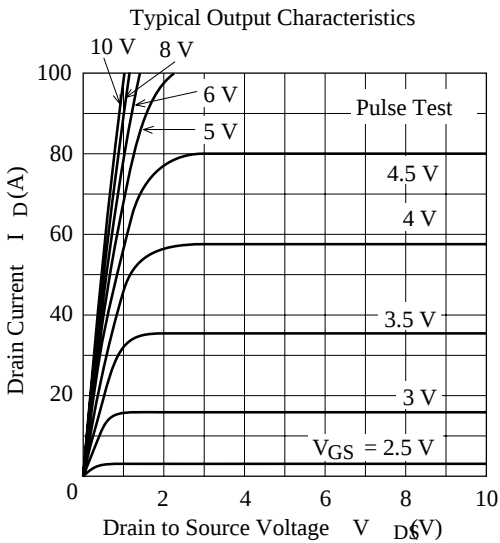
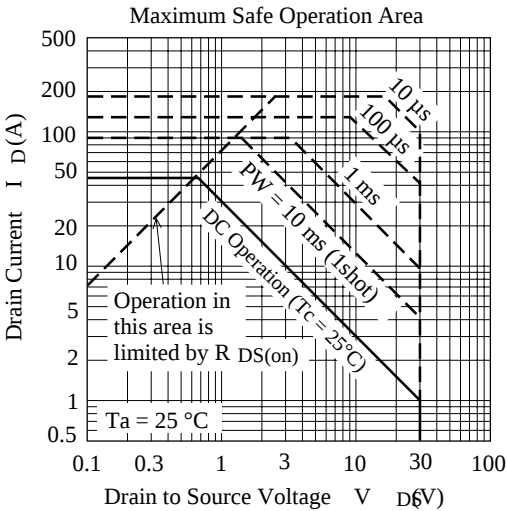
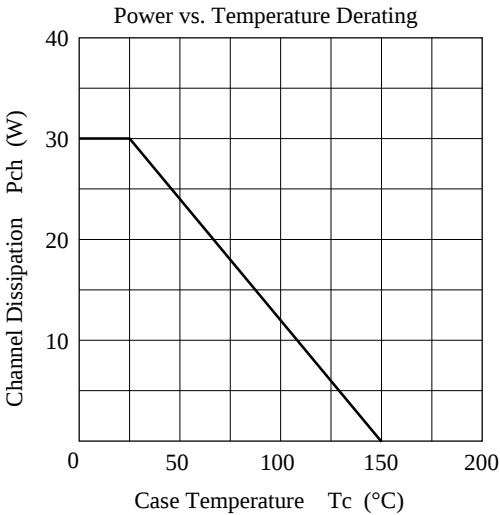
2. Value at Tc = 25°C

Electrical Characteristics (Ta = 25°C)

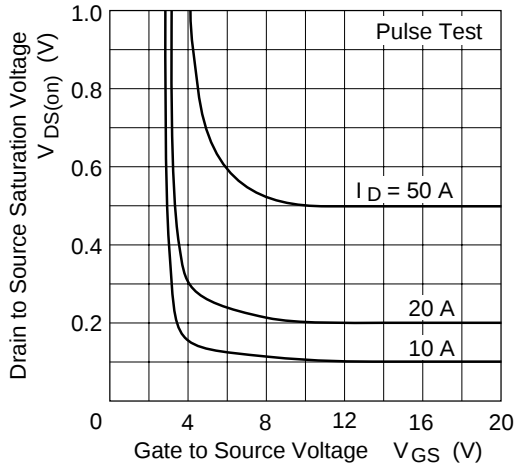
Item	Symbol	Min	Typ	Max	Unit	Test Conditions
Drain to source breakdown voltage	$V_{(BR)DSS}$	30	—	—	V	$I_D = 10mA$, $V_{GS} = 0$
Gate to source breakdown voltage	$V_{(BR)GSS}$	±20	—	—	V	$I_G = \pm 100\mu A$, $V_{DS} = 0$
Gate to source leak current	I_{GSS}	—	—	±10	μA	$V_{GS} = \pm 16V$, $V_{DS} = 0$
Zero gate voltage drain current	I_{DSS}	—	—	10	μA	$V_{DS} = 30V$, $V_{GS} = 0$
Gate to source cutoff voltage	$V_{GS(off)}$	1.0	—	2.0	V	$I_D = 1mA$, $V_{DS} = 10V$ ^{Note3}
Static drain to source on state resistance	$R_{DS(on)}$	—	10	14	mΩ	$I_D = 20A$, $V_{GS} = 10V$ ^{Note3}
Static drain to source on state resistance	$R_{DS(on)}$	—	15	25	mΩ	$I_D = 20A$, $V_{GS} = 4V$ ^{Note3}
Forward transfer admittance	$ y_{fs} $	20	30	—	S	$I_D = 20A$, $V_{DS} = 10V$ ^{Note3}
Input capacitance	Ciss	—	1570	—	pF	$V_{DS} = 10V$
Output capacitance	Coss	—	1100	—	pF	$V_{GS} = 0$
Reverse transfer capacitance	Crss	—	410	—	pF	f = 1MHz
Turn-on delay time	$t_{d(on)}$	—	32	—	ns	$V_{GS} = 10V$, $I_D = 20A$
Rise time	t_r	—	300	—	ns	$R_L = 0.5\Omega$
Turn-off delay time	$t_{d(off)}$	—	180	—	ns	
Fall time	t_f	—	200	—	ns	
Body-drain diode forward voltage	V_{DF}	—	1.0	—	V	$I_F = 45A$, $V_{GS} = 0$
Body-drain diode reverse recovery time	t_{rr}	—	75	—	ns	$I_F = 45A$, $V_{GS} = 0$ diF/ dt = 50A/μs

Note: 3. Pulse test

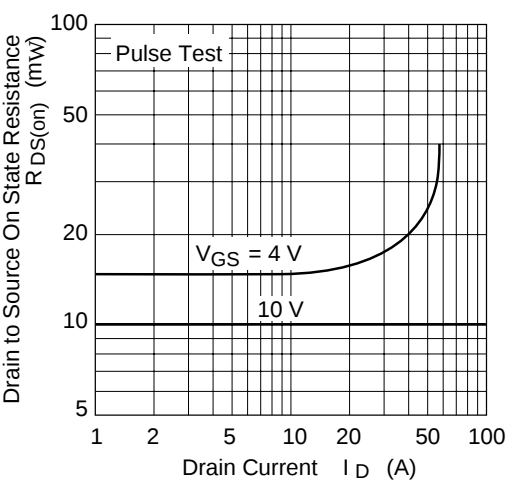
Main Characteristics



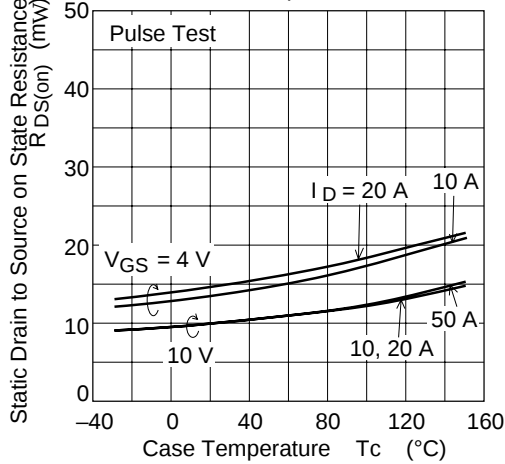
Drain to Source Saturation Voltage vs. Gate to Source Voltage



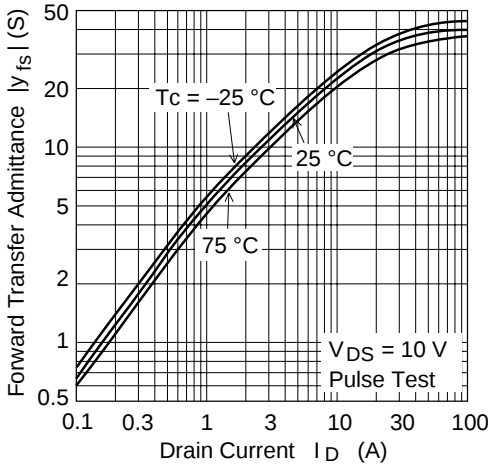
Static Drain to Source on State Resistance vs. Drain Current

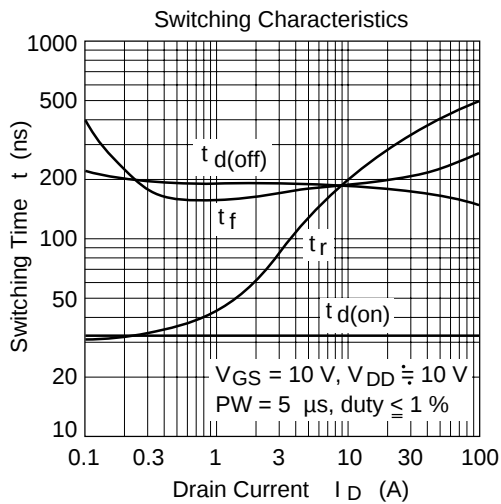
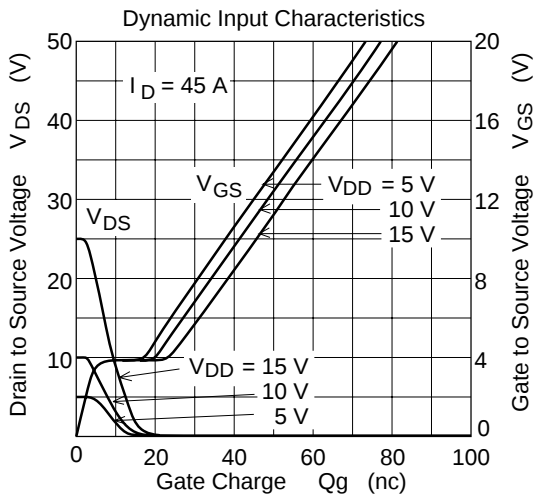
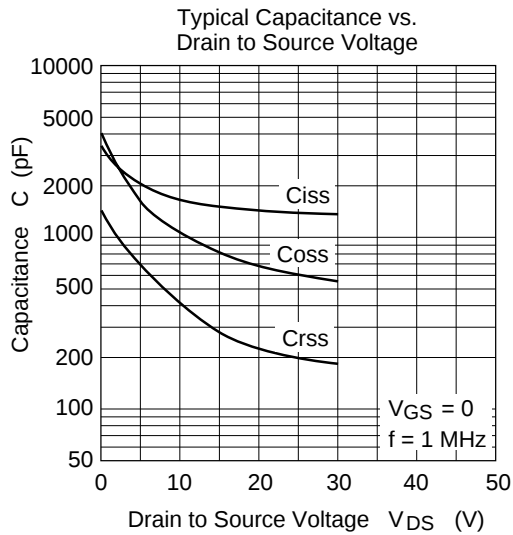
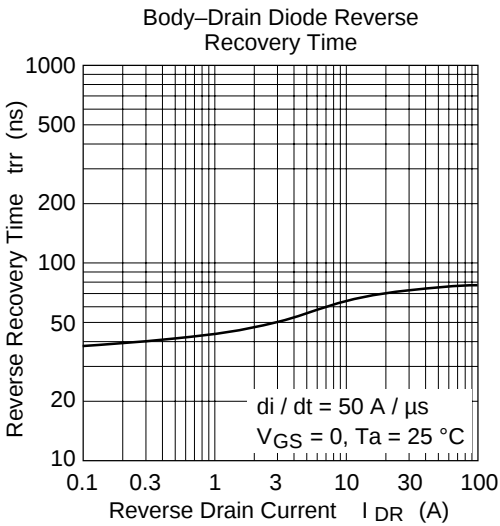


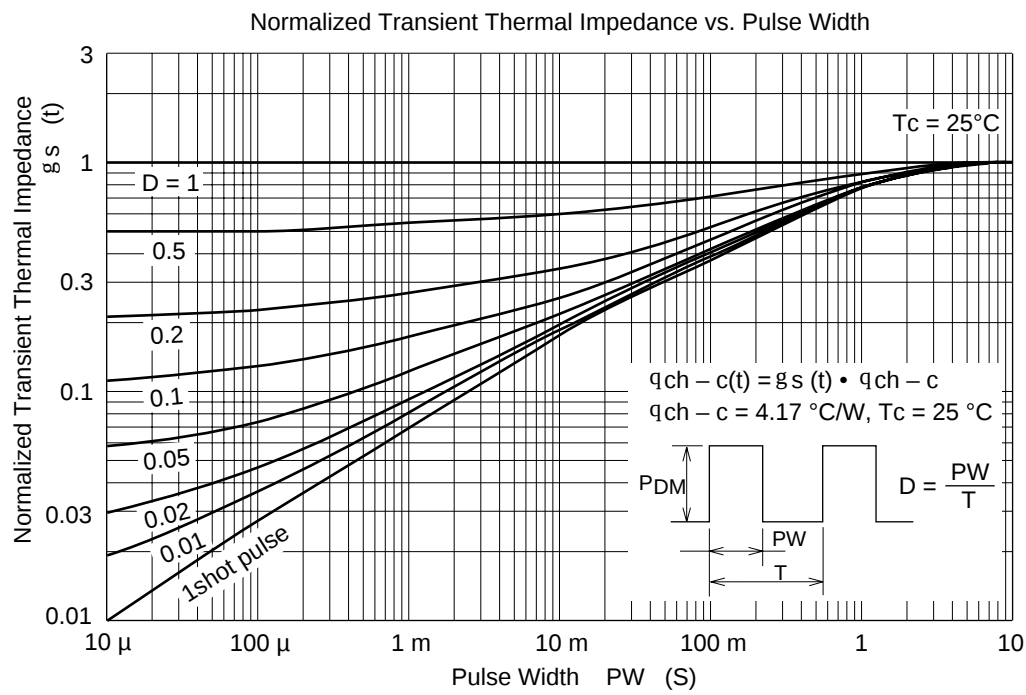
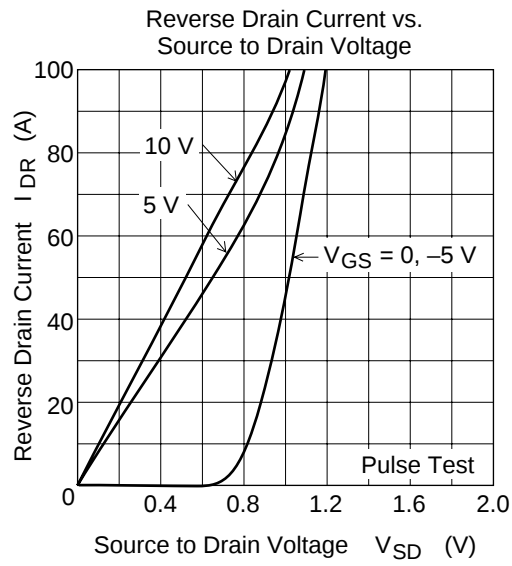
Static Drain to Source on State Resistance vs. Temperature



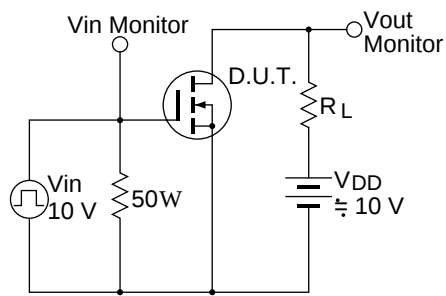
Forward Transfer Admittance vs. Drain Current



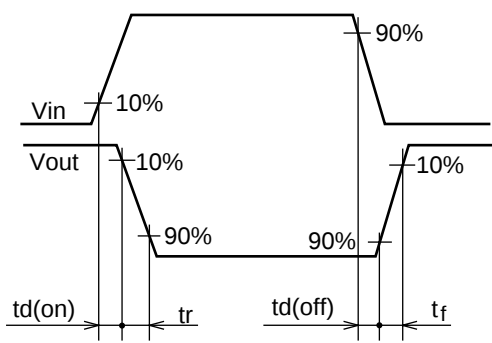




Switching Time Test Circuit

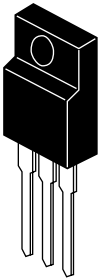
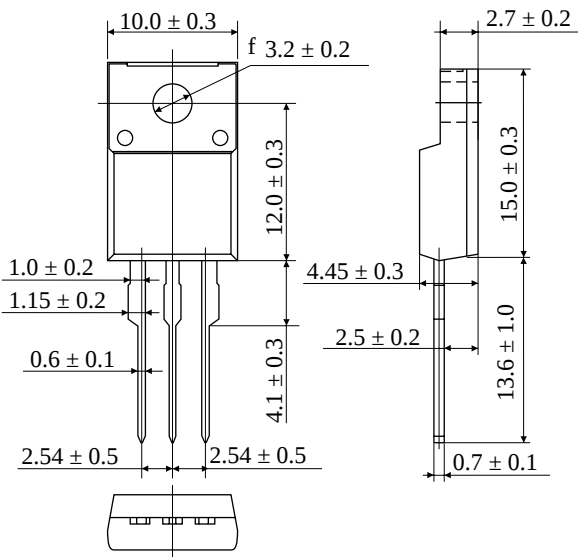


Waveform



Package Dimensions

Unit: mm



Hitachi Code	TO-220CFM
EIAJ	—
JEDEC	—

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Hitachi, Ltd.

Semiconductor & Integrated Circuits.
Nippon Bldg., 2-6-2, Ohte-machi, Chiyoda-ku, Tokyo 100-0004, Japan
Tel: Tokyo (03) 3270-2111 Fax: (03) 3270-5109

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For further information write to:

Hitachi Semiconductor (America) Inc. 179 East Tasman Drive, San Jose, CA 95134 Tel: <1> (408) 433-1990 Fax: <1> (408) 433-0223	Hitachi Europe GmbH Electronic components Group Dornacher Straße 3 D-85622 Feldkirchen, Munich Germany Tel: <49> (89) 9 9180-0 Fax: <49> (89) 9 29 30 00 Hitachi Europe Ltd. Electronic Components Group. Whitebrook Park Lower Cookham Road Maidenhead Berkshire SL6 8YA, United Kingdom Tel: <44> (1628) 585000 Fax: <44> (1628) 778322
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Hitachi Asia Pte. Ltd.
16 Collyer Quay #20-00
Hitachi Tower
Singapore 049318
Tel: 535-2100
Fax: 535-1533

Hitachi Asia Ltd.
Taipei Branch Office
3F, Hung Kuo Building, No.167,
Tun-Hwa North Road, Taipei (105)
Tel: <886> (2) 2718-3666
Fax: <886> (2) 2718-8180

Hitachi Asia (Hong Kong) Ltd.
Group III (Electronic Components)
7/F., North Tower, World Finance Centre,
Harbour City, Canton Road, Tsim Sha Tsui,
Kowloon, Hong Kong
Tel: <852> (2) 735 9218
Fax: <852> (2) 730 0281
Telex: 40815 HITEC HX

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