TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (π -MOSV)

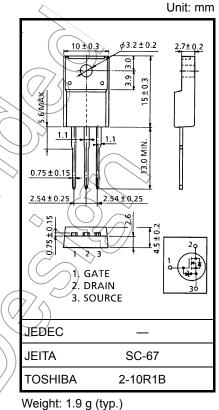
2SK2843

Chopper Regulator, DC–DC Converter and Motor Drive Applications

- Low drain-source ON resistance : $R_{DS (ON)} = 0.54 \Omega$ (typ.)
- High forward transfer admittance : |Y_{fs}| = 9.0 S (typ.)
- Low leakage current : I_{DSS} = 100 μA (max) (V_{DS} = 600 V)
- Enhancement mode : V_{th} = 2.0 to 4.0 V (V_{DS} = 10 V, I_D = 1 mA)

Absolute Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit
Drain-source voltage		V _{DSS}	600	V V
Drain-gate voltage (R _{GS} = 20 kΩ)		V _{DGR}	600	y
Gate-source voltage		V _{GSS}	±30	> v
Drain current	DC (Note 1)	۱ _D	10	А
	Pulse (Note 1)	I _{DP}	40	A
Drain power dissipatio	n (Tc = 25°C)	PD <	45	W
Single pulse avalanche energy (Note 2)		EAS	363	Cm
Avalanche current		IAR) 10	А
Repetitive avalanche e	energy (Note 3)	EAR	5.0 <	mJ
Channel temperature		Tch	150)°C
Storage temperature ra	ange ((∕∕T _{stg}	-55 to 150	⊃°C



Note: Using continuous/ under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Thermal Characteristics

Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	Rth (ch-c)	2.78	°C / W
Thermal resistance, channel to ambient		62.5	°C / W

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: V_{DD} = 90 V, T_{ch} = 25°C (initial), L = 6.36 mH, R_G = 25 Ω , I_{AR} = 10 A

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device. Please handle with caution.

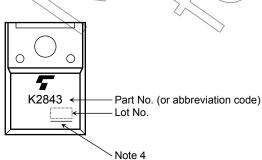
Electrical Characteristics (Ta = 25°C)

Charao	cteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cu	urrent	I _{GSS}	V_{GS} = ±25 V, V_{DS} = 0 V	_	—	±10	μA
Gate-source bre	eakdown voltage	V (BR) GSS	I _G = ±10 μA, V _{DS} = 0 V	±30	_		V
Drain cut-off cu	rrent	I _{DSS}	V _{DS} = 600 V, V _{GS} = 0 V	Ķ	_	100	μA
Drain-source br	eakdown voltage	V (BR) DSS	I _D = 10 mA, V _{GS} = 0 V	600	-	_	V
Gate threshold v	voltage	V _{th}	V _{DS} = 10 V, I _D = 1 mA	2.0	-7(4.0	V
Drain-source O	N resistance	R _{DS (ON)}	$V_{GS} = 10 \text{ V}, \text{ I}_{D} = 5 \text{ A}$		0.54	0.75	Ω
Forward transfe	r admittance	Y _{fs}	V _{DS} = 10 V, I _D = 5 A	3.0	9.0		S
Input capacitance	ce	C _{iss}			2040		
Reverse transfer capacitance		C _{rss}	V _{DS} = 10 V, V _{GS} = 0 V, f = 1 MHz		230	_	pF
Output capacitance		Coss			590		
Switching time	Rise time	tr	$v_{\rm GS}^{10V}_{0V} \prod V_{\rm OUT}^{I_{\rm D}=5A}_{V_{\rm OUT}}$	- (22	\geq	
	Turn-on time	t _{on}	$\begin{array}{c} 000 \\ 0V \\ 0 \\ 0 \\ 0 \\ 0 \\ 0 \\ 0 \\ 0 \\ $		58) —	
	Fall time	t _f			36	_	ns
	Turn-off time	t _{off}	$V_{DD} = 200V$ $Duty \leq 1\%, t_w = 10\mu s$) -	190	_	
Total gate charg plus gate-drain)		Qg		_	45	_	
Gate-source charge		Q _{gs}	$V_{DD} \approx 400 \text{ V}, V_{GS} = 10 \text{ V}, D = 10 \text{ A}$	_	25	_	nC
Gate-drain ("miller") Charge		Q _{gd}		_	20	_	

Source-Drain Ratings and Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)			_	_	10	А
Pulse drain reverse current (Note 1)		-	_		40	А
Forward voltage (diode)	V _{DSF}	I _{DR} = 10 A, V _{GS} = 0 V	_	_	-1.7	V
Reverse recovery time	trr	I _{DR} = 10 A, V _{GS} = 0 V		1300		ns
Reverse recovery charge	Q _{rr}	dl _{DR} / dt = 100 Å / μs	_	16	_	μC

Marking

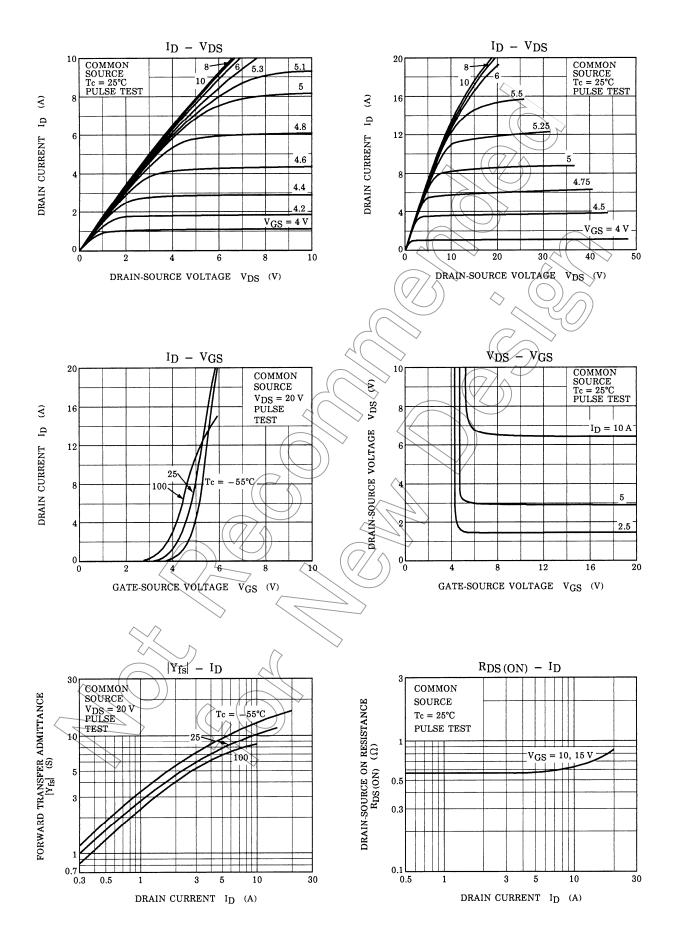


Note 4: A line under a Lot No. identifies the indication of product Labels. Not underlined: [[Pb]]/INCLUDES > MCV

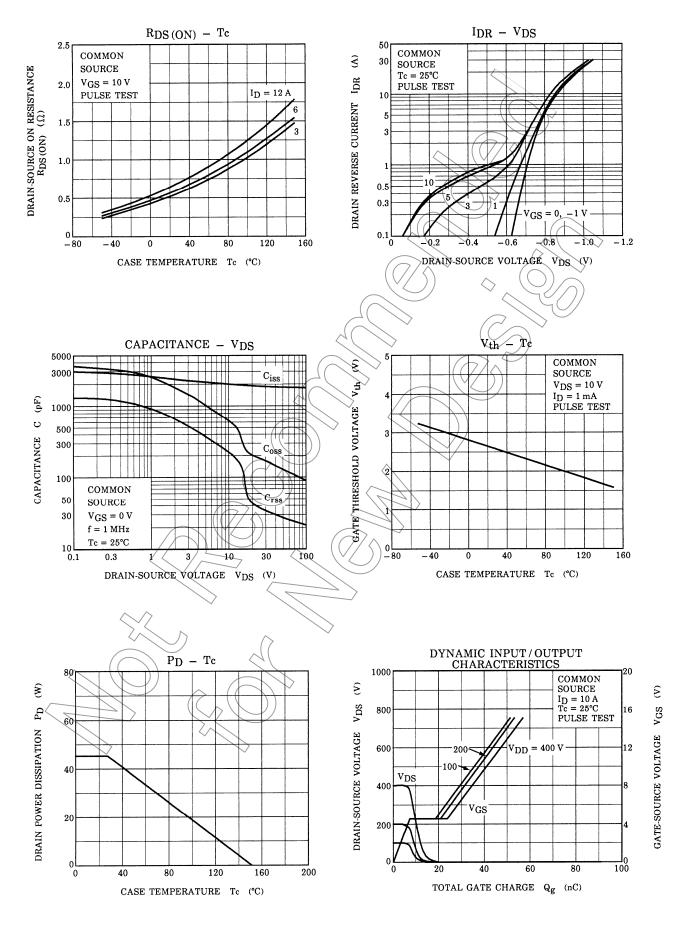
Underlined: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

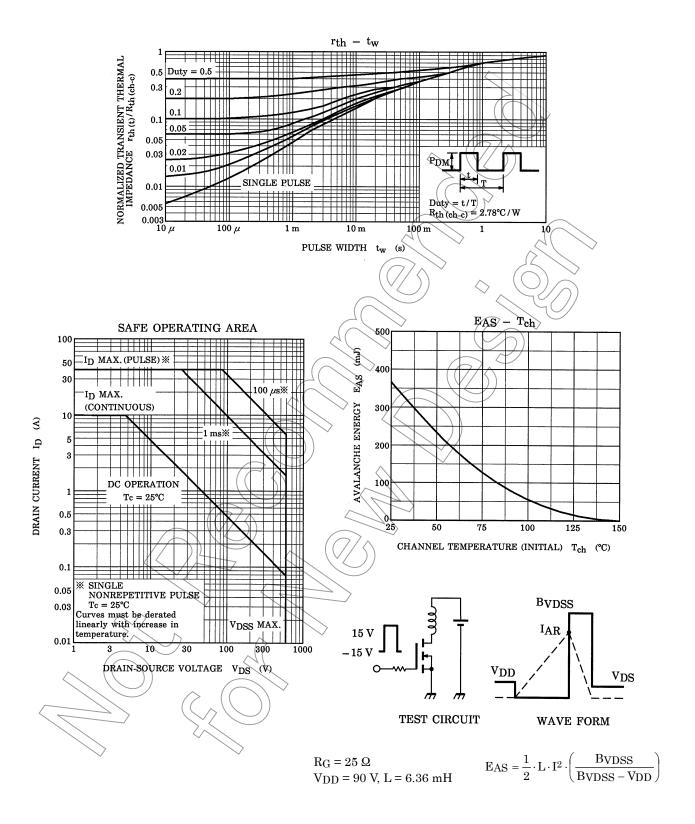
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