

# MOS FIELD EFFECT TRANSISTOR

## 2SK3115

### SWITCHING

### N-CHANNEL POWER MOS FET

### INDUSTRIAL USE

**DESCRIPTION**

The 2SK3115 is N-Channel DMOS FET device that features a low gate charge and excellent switching characteristics, and designed for high voltage applications such as switching power supply, AC adapter.

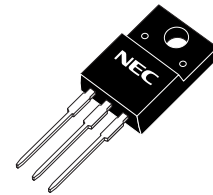
**FEATURES**

- Low gate charge  
 $Q_G = 26 \text{ nC TYP. (} V_{DD} = 450 \text{ V, } V_{GS} = 10 \text{ V, } I_D = 6.0 \text{ A)}$
- Gate voltage rating  $\pm 30 \text{ V}$
- Low on-state resistance  
 $R_{DS(on)} = 1.2 \Omega \text{ MAX. (} V_{GS} = 10 \text{ V, } I_D = 3.0 \text{ A)}$
- Avalanche capability ratings

**ORDERING INFORMATION**

PART NUMBER	PACKAGE
2SK3115	Isolated TO-220

★ (Isolated TO-220)

**ABSOLUTE MAXIMUM RATINGS (T<sub>A</sub> = 25°C)**

Drain to Source Voltage (V <sub>GS</sub> = 0 V)	V <sub>DSS</sub>	600	V
Gate to Source Voltage (V <sub>DS</sub> = 0 V)	V <sub>GSS</sub>	±30	V
Drain Current (DC) (T <sub>C</sub> = 25°C)	I <sub>D(DC)</sub>	±6.0	A
Drain Current (pulse) <sup>Note1</sup>	I <sub>D(pulse)</sub>	±24	A
Total Power Dissipation (T <sub>A</sub> = 25°C)	P <sub>T1</sub>	2.0	W
Total Power Dissipation (T <sub>C</sub> = 25°C)	P <sub>T2</sub>	35	W
Channel Temperature	T <sub>ch</sub>	150	°C
Storage Temperature	T <sub>stg</sub>	-55 to +150	°C
Single Avalanche Current <sup>Note2</sup>	I <sub>AS</sub>	6.0	A
Single Avalanche Energy <sup>Note2</sup>	E <sub>AS</sub>	24	mJ

**Notes 1.** PW ≤ 10 μs, Duty Cycle ≤ 1%

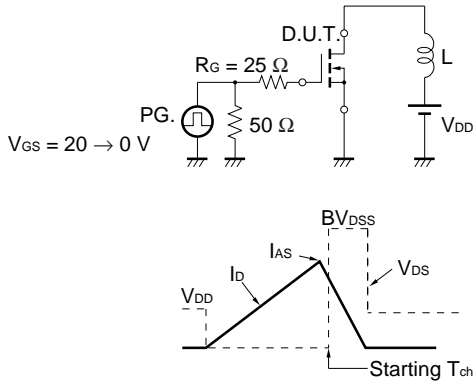
**2.** Starting T<sub>ch</sub> = 25°C, V<sub>DD</sub> = 150 V, R<sub>G</sub> = 25 Ω, V<sub>GS</sub> = 20 → 0 V

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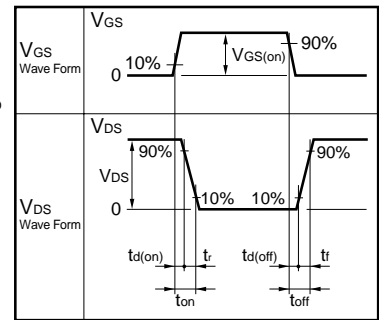
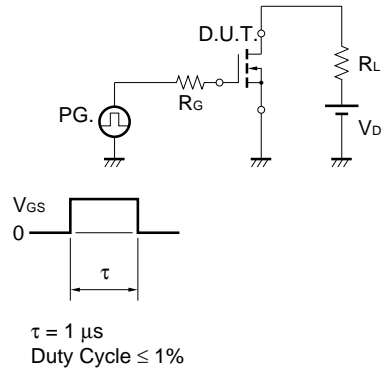
**ELECTRICAL CHARACTERISTICS (T<sub>A</sub> = 25°C)**

Characteristics	Symbol	Test Conditions	MIN.	TYP.	MAX.	Unit
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = 600 V, V <sub>GS</sub> = 0 V			100	μA
Gate Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> = ±30 V, V <sub>DS</sub> = 0 V			±100	nA
Gate Cut-off Voltage	V <sub>GS(off)</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 1 mA	2.5		3.5	V
Forward Transfer Admittance	y <sub>fs</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 3.0 A	2.0			S
Drain to Source On-state Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 3.0 A		0.9	1.2	Ω
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> = 10 V		1100		pF
Output Capacitance	C <sub>oss</sub>	V <sub>GS</sub> = 0 V		200		pF
Reverse Transfer Capacitance	C <sub>rss</sub>	f = 1 MHz		20		pF
Turn-on Delay Time	t <sub>d(on)</sub>	V <sub>DD</sub> = 150 V, I <sub>D</sub> = 3.0 A		18		ns
Rise Time	t <sub>r</sub>	V <sub>GS(on)</sub> = 10 V		12		ns
Turn-off Delay Time	t <sub>d(off)</sub>	R <sub>G</sub> = 10 Ω, R <sub>L</sub> = 50 Ω		50		ns
Fall Time	t <sub>f</sub>			15		ns
Total Gate Charge	Q <sub>G</sub>	V <sub>DD</sub> = 450 V		26		nC
Gate to Source Charge	Q <sub>GS</sub>	V <sub>GS</sub> = 10 V		6		nC
Gate to Drain Charge	Q <sub>GD</sub>	I <sub>D</sub> = 6.0 A		10		nC
Body Diode Forward Voltage	V <sub>F(S-D)</sub>	I <sub>F</sub> = 6.0 A, V <sub>GS</sub> = 0 V		1.0		V
Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> = 6.0 A, V <sub>GS</sub> = 0 V		1.4		μs
Reverse Recovery Charge	Q <sub>rr</sub>	di/dt = 50 A/μs		6.5		μC

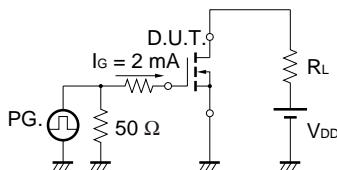
★ **TEST CIRCUIT 1 AVALANCHE CAPABILITY**



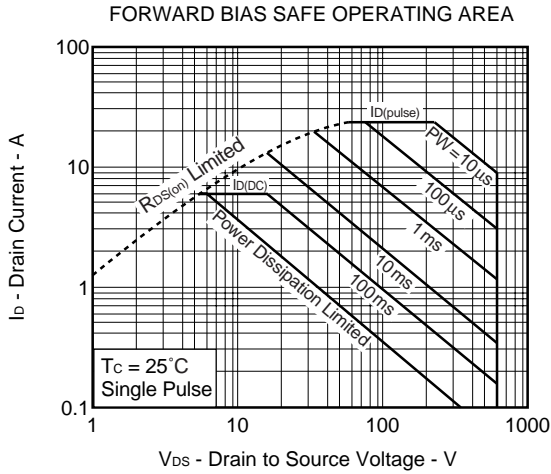
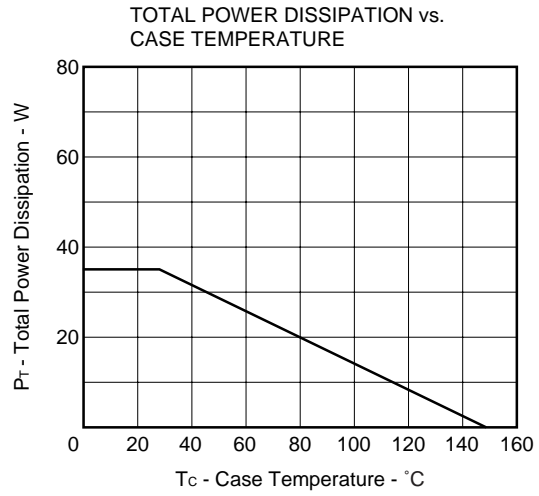
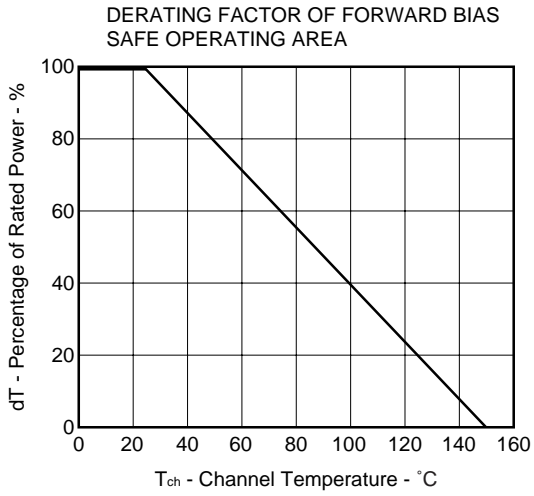
**TEST CIRCUIT 2 SWITCHING TIME**



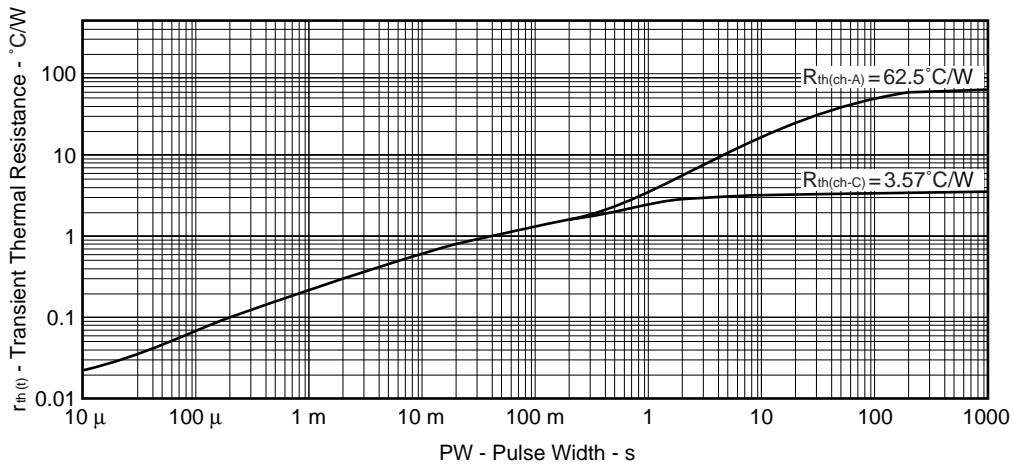
**TEST CIRCUIT 3 GATE CHARGE**



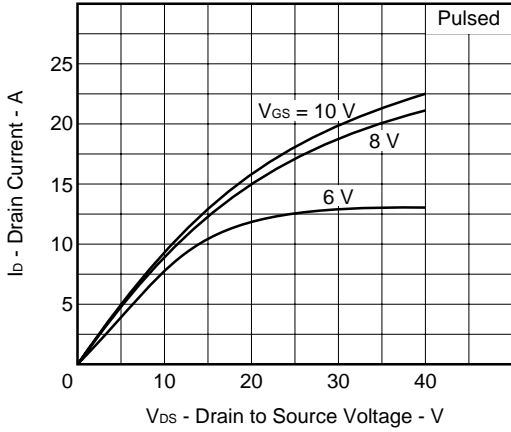
★ TYPICAL CHARACTERISTICS (T<sub>A</sub> = 25°C)



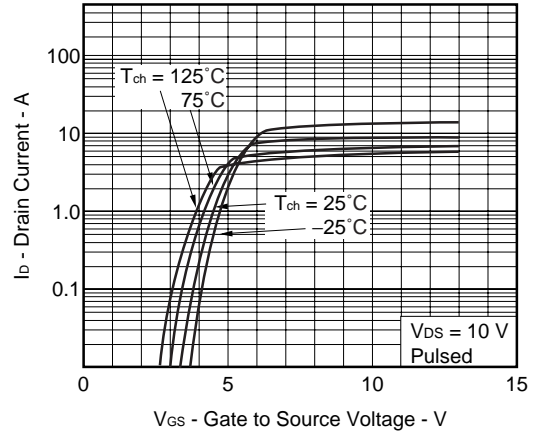
TRANSIENT THERMAL RESISTANCE vs. PULSE WIDTH



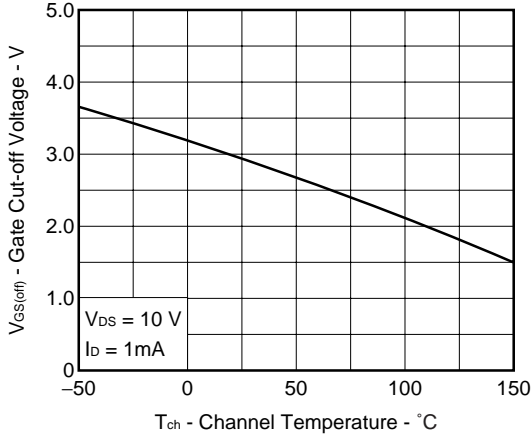
DRAIN CURRENT vs. DRAIN TO SOURCE VOLTAGE



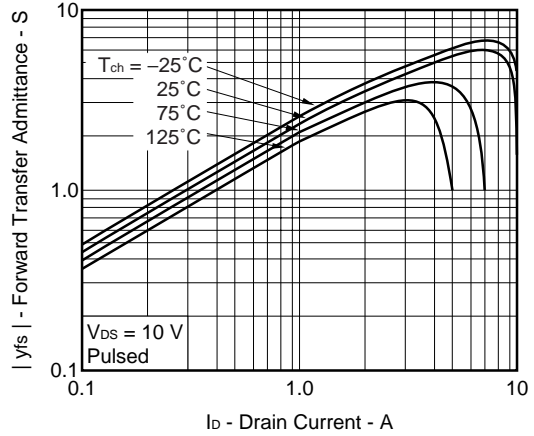
FORWARD TRANSFER CHARACTERISTICS



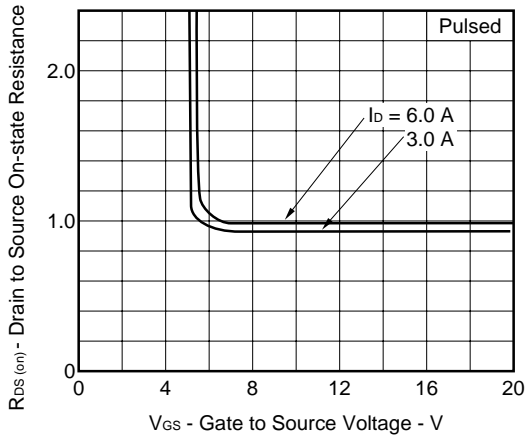
GATE CUT-OFF VOLTAGE vs. CHANNEL TEMPERATURE



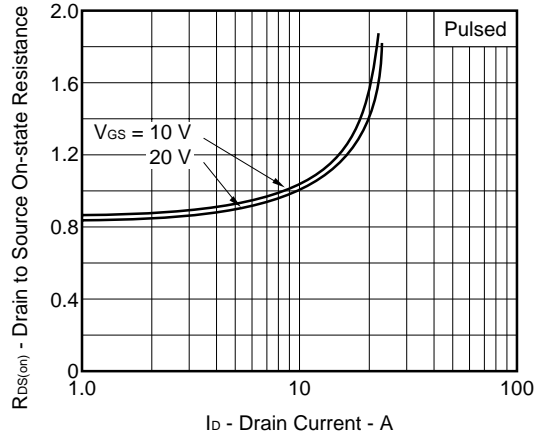
FORWARD TRANSFER ADMITTANCE vs. DRAIN CURRENT

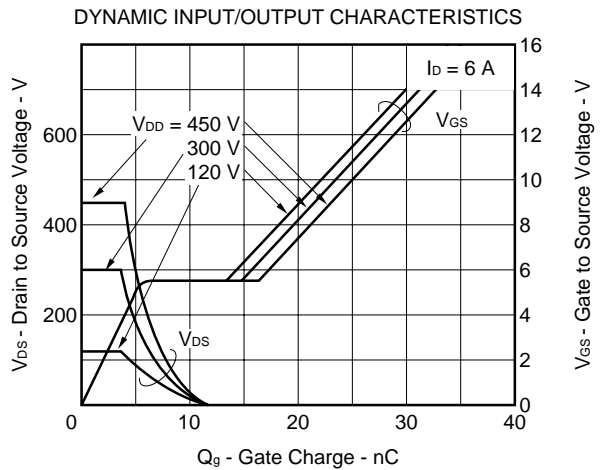
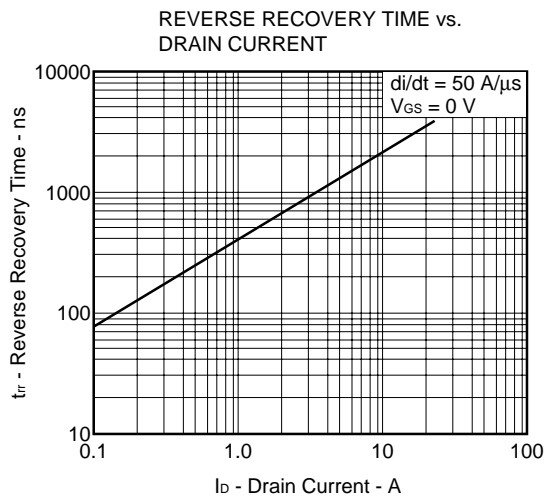
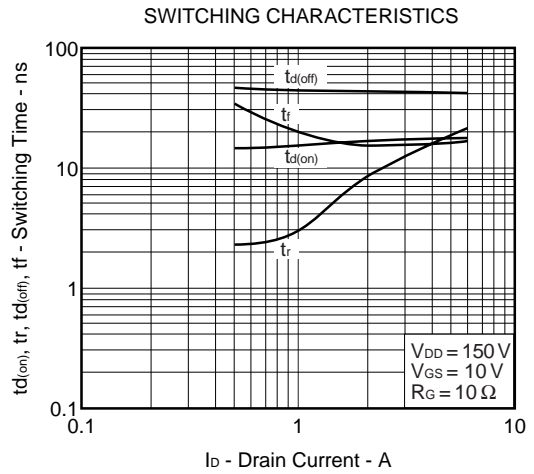
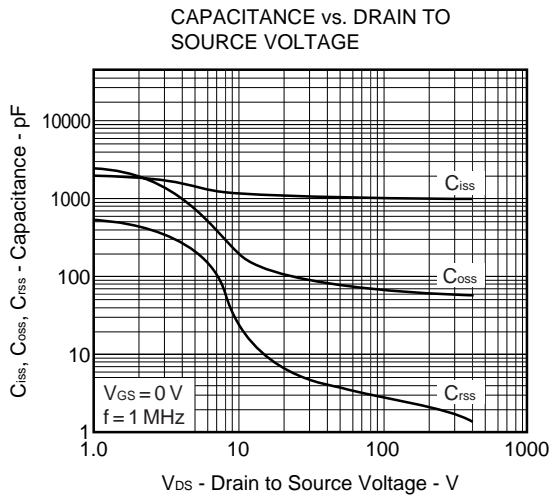
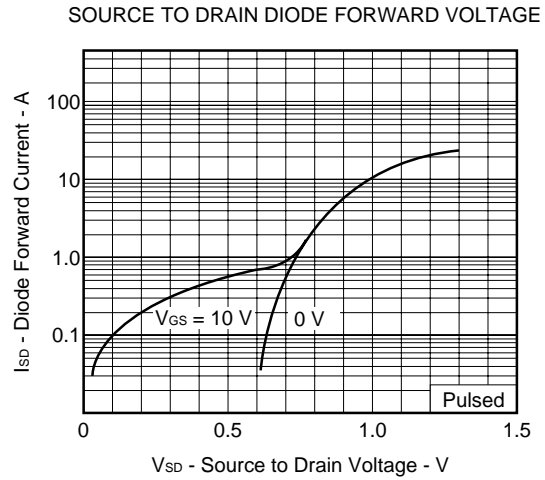
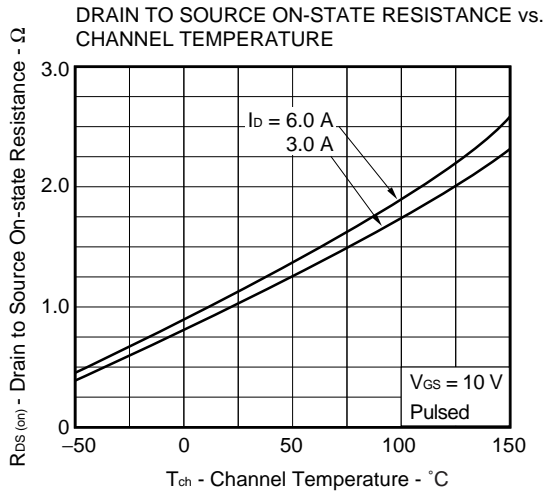


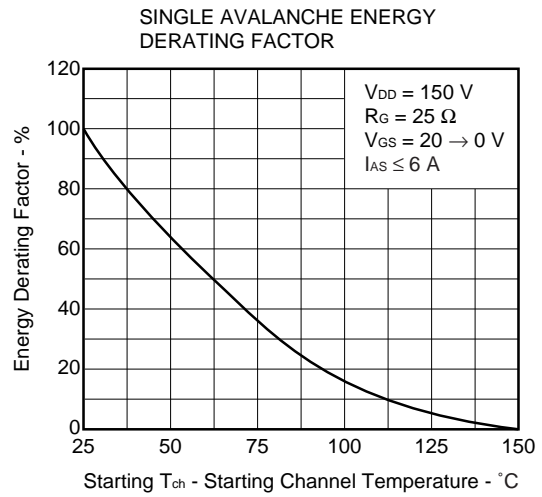
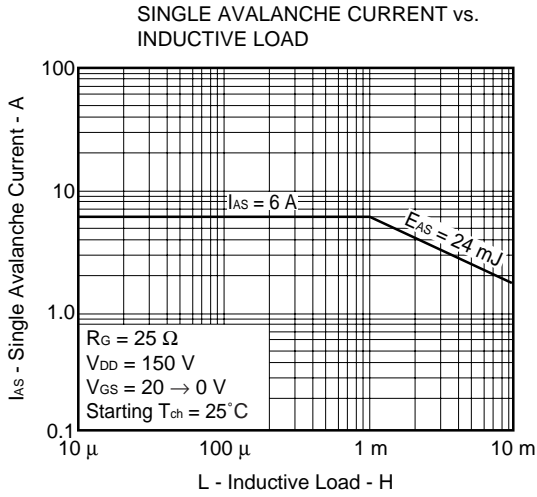
DRAIN TO SOURCE ON-STATE RESISTANCE vs. GATE TO SOURCE VOLTAGE



DRAIN TO SOURCE ON-STATE RESISTANCE vs. DRAIN CURRENT

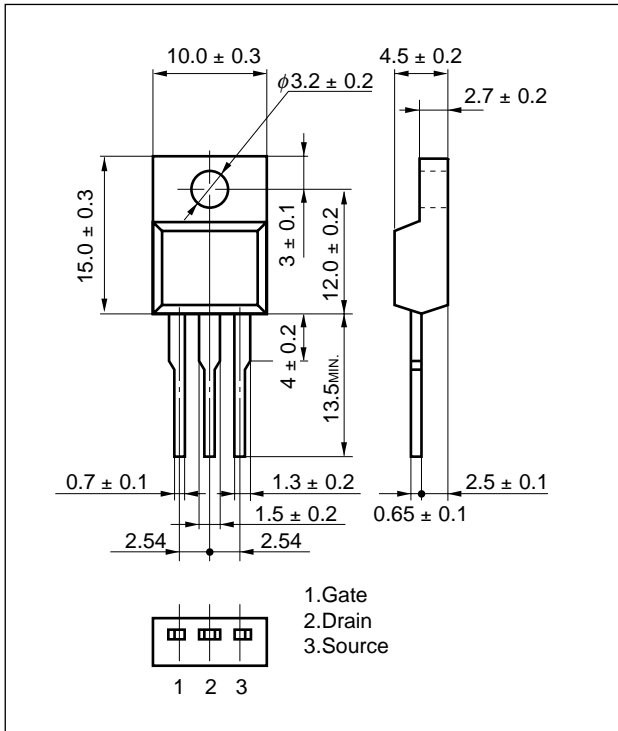




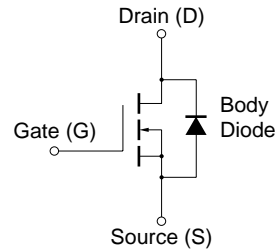


**PACKAGE DRAWING (Unit: mm)**

Isolated TO-220(MP-45F)



**EQUIVALENT CIRCUIT**



**Remark** Strong electric field, when exposed to this device, can cause destruction of the gate oxide and ultimately degrade the device operation. Steps must be taken to stop generation of static electricity as much as possible, and quickly dissipate it once, when it has occurred.

[MEMO]

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