

2SK3207

Silicon N Channel MOS FET
High Speed Power Switching

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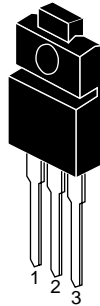
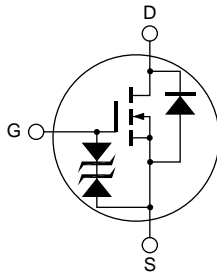
ADE-208-758A(Z)
Target Specification 2nd. Edition
Feb 1999

Features

- Low on-resistance
 $R_{DS} = 70 \text{ m}\Omega$ typ.
- High speed switching
- 4V gate drive device can be driven from 5V source

Outline

TO-220FM



1. Gate
2. Drain
3. Source

Absolute Maximum Ratings ($T_a = 25^\circ\text{C}$)

Item	Symbol	Ratings	Unit
Drain to source voltage	V_{DSS}	150	V
Gate to source voltage	V_{GSS}	± 20	V
Drain current	I_D	18	A
Drain peak current	$I_{D(pulse)}^{*1}$	72	A
Body-drain diode reverse drain current	I_{DR}	18	A
Avalanche current	I_{AP}^{*3}	18	A
Avalanche energy	E_{AR}^{*3}	24	mJ
Channel dissipation	P_{ch}^{*2}	35	W
Channel temperature	T_{ch}	150	$^\circ\text{C}$
Storage temperature	T_{stg}	-55 to +150	$^\circ\text{C}$

- Note:
1. $PW \leq 10\mu\text{s}$, duty cycle $\leq 1\%$
 2. Value at $T_c = 25^\circ\text{C}$
 3. Value at $T_{ch} = 25^\circ\text{C}$, $R_g \geq 50\Omega$

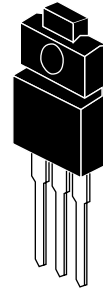
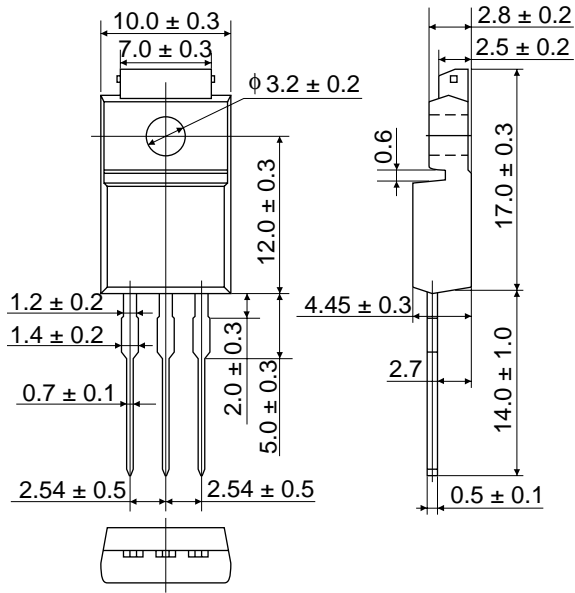
Electrical Characteristics (Ta = 25°C)

Item	Symbol	Min	Typ	Max	Unit	Test Conditions
Drain to source breakdown voltage	$V_{(BR)DSS}$	150	—	—	V	$I_D = 10\text{mA}$, $V_{GS} = 0$
Gate to source breakdown voltage	$V_{(BR)GSS}$	± 20	—	—	V	$I_G = \pm 100\mu\text{A}$, $V_{DS} = 0$
Gate to source leak current	I_{GSS}	—	—	± 10	μA	$V_{GS} = \pm 16\text{V}$, $V_{DS} = 0$
Zero gate voltage drain current	I_{DSS}	—	—	10	μA	$V_{DS} = 150\text{V}$, $V_{GS} = 0$
Gate to source cutoff voltage	$V_{GS(off)}$	1.0	—	2.5	V	$I_D = 1\text{mA}$, $V_{DS} = 10\text{V}$
Static drain to source on state resistance	$R_{DS(on)}$	—	70	90	$\text{m}\Omega$	$I_D = 9\text{A}$, $V_{GS} = 10\text{V}^{*4}$
	$R_{DS(on)}$	—	85	120	$\text{m}\Omega$	$I_D = 9\text{A}$, $V_{GS} = 4\text{V}^{*4}$
Forward transfer admittance	$ y_{fs} $	11	18	—	S	$I_D = 9\text{A}$, $V_{DS} = 10\text{V}^{*4}$
Input capacitance	C_{iss}	—	1100	—	pF	$V_{DS} = 10\text{V}$
Output capacitance	C_{oss}	—	350	—	pF	$V_{GS} = 0$
Reverse transfer capacitance	C_{rss}	—	170	—	pF	$f = 1\text{MHz}$
Turn-on delay time	$t_{d(on)}$	—	15	—	ns	$I_D = 9\text{A}$, $V_{GS} = 10\text{V}$
Rise time	t_r	—	110	—	ns	$R_L = 3.33\Omega$
Turn-off delay time	$t_{d(off)}$	—	270	—	ns	
Fall time	t_f	—	130	—	ns	
Body-drain diode forward voltage	V_{DF}	—	0.9	—	V	$I_F = 18\text{A}$, $V_{GS} = 0$
Body-drain diode reverse recovery time	t_{rr}	—	150	—	ns	$I_F = 18\text{A}$, $V_{GS} = 0$ $di_F/dt = 50\text{A}/\mu\text{s}$

Note: 4. Pulse test

Package Dimensions

Unit: mm



Hitachi Code	TO-220FM
EIAJ	SC-67
JEDEC	—

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