TOSHIBA Field Effect Transistor Silicon N-Channel MOS Type (π-MOSV)

2SK3371

Switching Regulator Applications

Features

- Low drain-source ON-resistance: $R_{DS\ (ON)}$ = 6.4 Ω (typ.)
- High forward transfer admittance: $|Y_{fs}|$ = 0.85 S (typ.)
- Low leakage current: I_{DSS} = 100 μA (max) (V_{DSS} = 600 V)
- Enhancement mode: V_{th} = 2.0 to 4.0 V (V_{DS} = 10 V, I_D = 1 mA)

Absolute Maximum Ratings (Ta = 25°C)

Characteristic		Symbol	Rating	Unit
Drain-source voltage		V_{DSS}	600	V
Drain-gate voltage (R_{GS} = 20 kΩ)		V_{DGR}	600	V
Gate-source voltage		V_{GSS}	±30	V
Drain current	DC (Note 1)	I_D	1	A
	Pulse (Note 1)	I_{DP}	2	
Drain power dissipation (Tc = 25°C)		P_D	20	W
Single-pulse avalanche energy (Note 2)		E_{AS}	56	mJ
Avalanche current		I_{AR}	1	A
Repetitive avalanche energy (Note 3)		E_{AR}	2	mJ
Channel temperature		T_{ch}	150	°C
Storage temperature range		T_{stg}	−55 to 150	°C

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook (“Handling Precautions”/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Thermal Characteristics

Characteristic	Symbol	Max	Unit
Thermal resistance, channel to case	$R_{th\ (ch\text{-}c)}$	6.25	°C/W
Thermal resistance, channel to ambient	$R_{th\ (ch\text{-}a)}$	125	°C/W

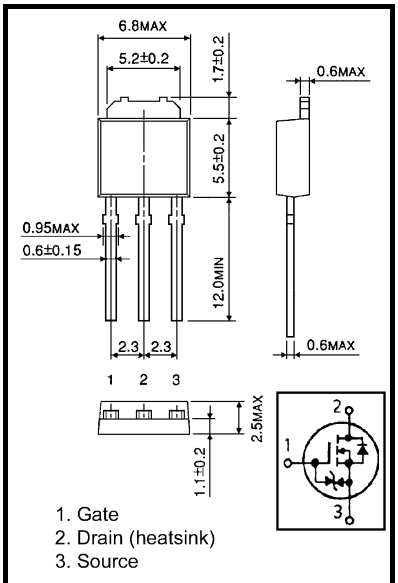
Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: V_{DD} = 90 V, T_{ch} = 25°C, L = 100 mH, I_{AR} = 1 A, R_G = 25 Ω

Note 3: Repetitive rating: pulse width limited by max channel temperature

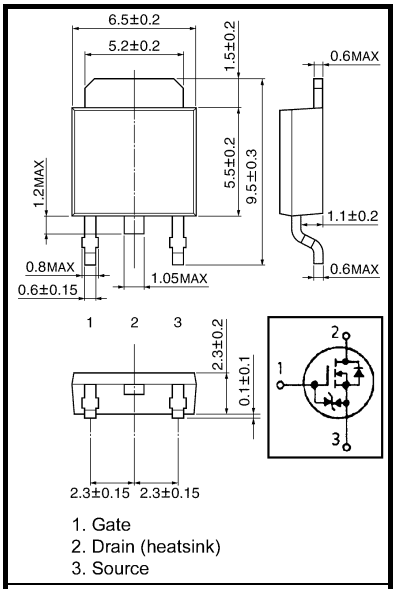
This transistor is an electrostatic-sensitive device. Handle with care.

Unit: mm



JEDEC	—
JEITA	SC-64
TOSHIBA	2-7B1B

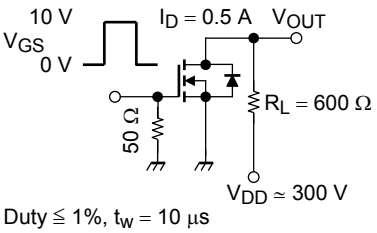
Weight: 0.36 g (typ.)



JEDEC	—
JEITA	—
TOSHIBA	2-7J1B

Weight: 0.36 g (typ.)

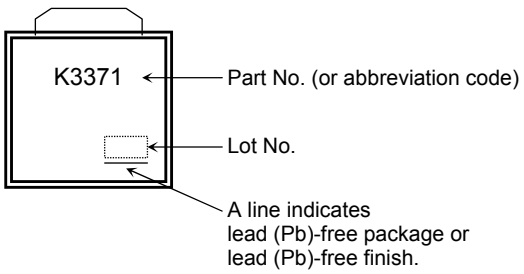
Electrical Characteristics (Ta = 25°C)

Characteristic		Symbol	Test Condition	Min	Typ.	Max	Unit
Gate leakage current		I_{GSS}	V_{GS} = ±25 V, V_{DS} = 0 V	—	—	±10	μA
Gate-source breakdown voltage		$V_{(BR)\ GSS}$	I_G = ±10 μA, V_{DS} = 0 V	±30	—	—	V
Drain cutoff current		I_{DSS}	V_{DS} = 600 V, V_{GS} = 0 V	—	—	100	μA
Drain-source breakdown voltage		$V_{(BR)\ DSS}$	I_D = 10 mA, V_{GS} = 0 V	600	—	—	V
Gate threshold voltage		V_{th}	V_{DS} = 10 V, I_D = 1 mA	2.0	—	4.0	V
Drain-source ON-resistance		$R_{DS\ (ON)}$	V_{GS} = 10 V, I_D = 0.5 A	—	6.4	9.0	Ω
Forward transfer admittance		$\|Y_{fs}\|$	V_{DS} = 10 V, I_D = 0.5 A	0.4	0.85	—	S
Input capacitance		C_{iss}	V_{DS} = 10 V, V_{GS} = 0 V, f = 1 MHz	—	190	—	pF
Reverse transfer capacitance		C_{rss}		—	15	—	
Output capacitance		C_{oss}		—	55	—	
Switching time	Rise time	t_r		—	12	—	ns
	Turn-on time	t_{on}		—	55	—	
	Fall time	t_f		—	40	—	
	Turn-off time	t_{off}	Duty ≦ 1%, t_w = 10 μs	—	90	—	
Total gate charge (gate-source plus gate-drain)		Q_g	V_{DD} ≃ 400 V, V_{GS} = 10 V, I_D = 1 A	—	9	—	nC
Gate-source charge		Q_{gs}		—	3.5	—	
Gate-drain (“Miller”) charge		Q_{gd}		—	5.5	—	

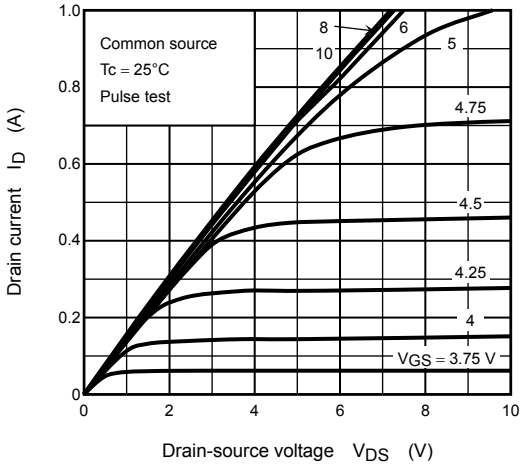
Source-Drain Diode Ratings and Characteristics (Ta = 25°C)

Characteristic	Symbol	Test Condition	Min	Typ.	Max	Unit
Continuous drain reverse current	I_{DR}	—	—	—	1	A
Pulse drain reverse current	I_{DRP}	—	—	—	2	A
Diode forward voltage	V_{DSF}	I_{DR} = 1 A, V_{GS} = 0 V	—	—	−1.7	V
Reverse recovery time	t_{rr}	I_{DR} = 1 A, V_{GS} = 0 V, dI_{DR}/dt = 100 A/μs	—	400	—	ns
Reverse recovery charge	Q_{rr}		—	1.4	—	μC

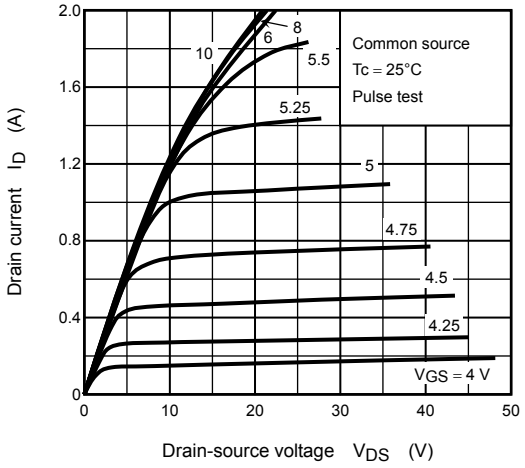
Marking



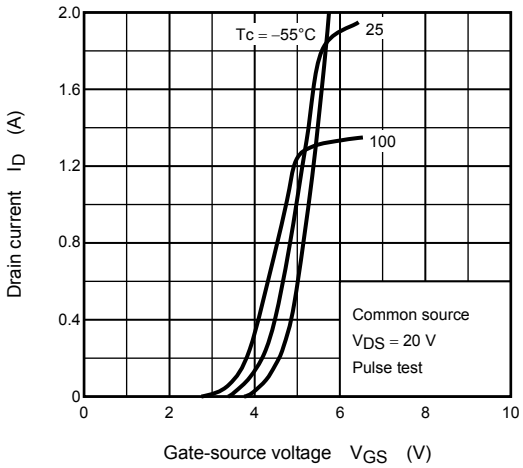
$I_D - V_{DS}$



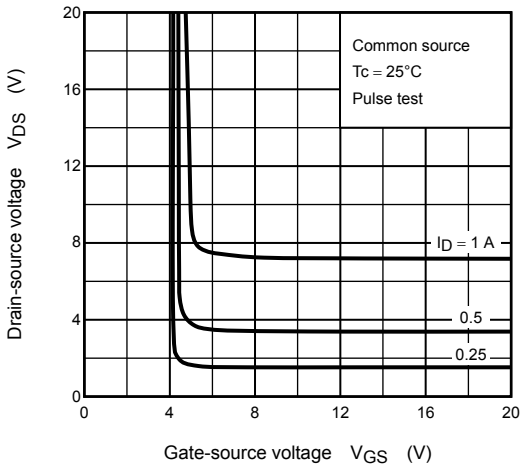
$I_D - V_{DS}$



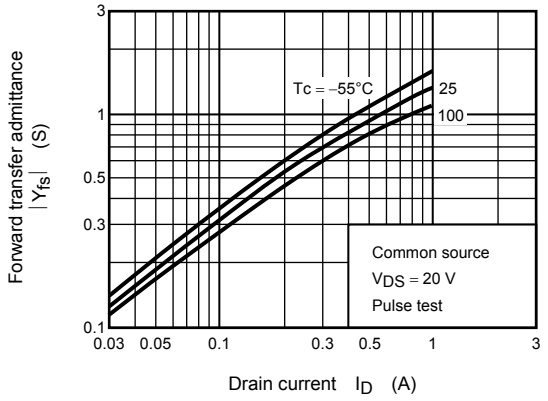
$I_D - V_{GS}$



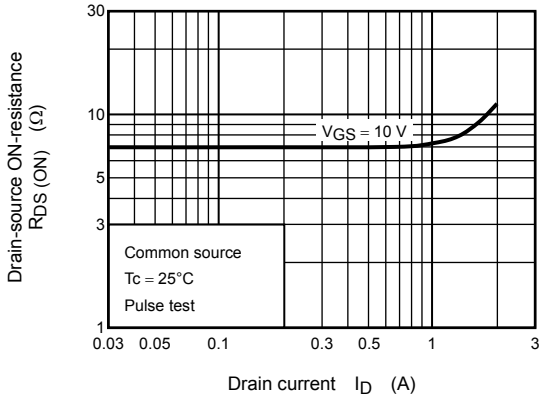
$V_{DS} - V_{GS}$



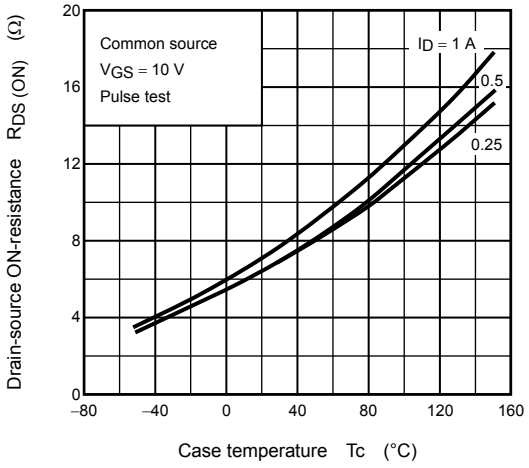
$|Y_{fs}| - I_D$



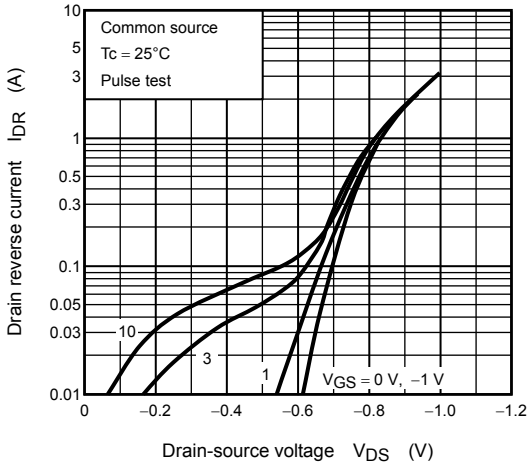
$R_{DS\ (ON)} - I_D$



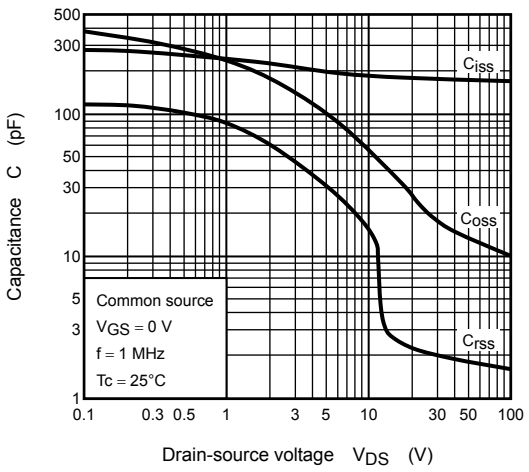
$R_{DS\ (ON)}$ – Tc



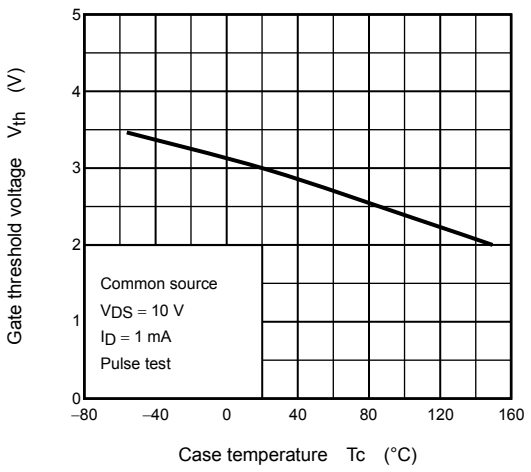
I_{DR} – V_{DS}



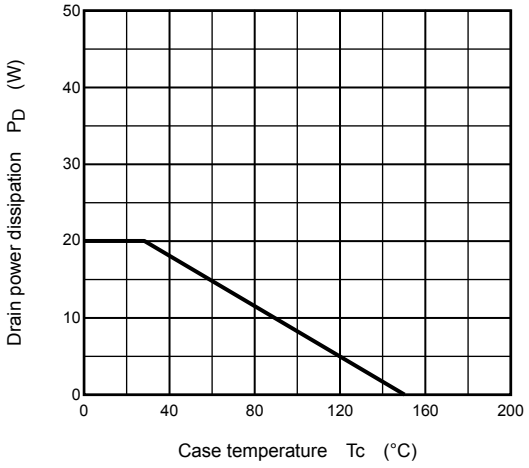
Capacitance – V_{DS}



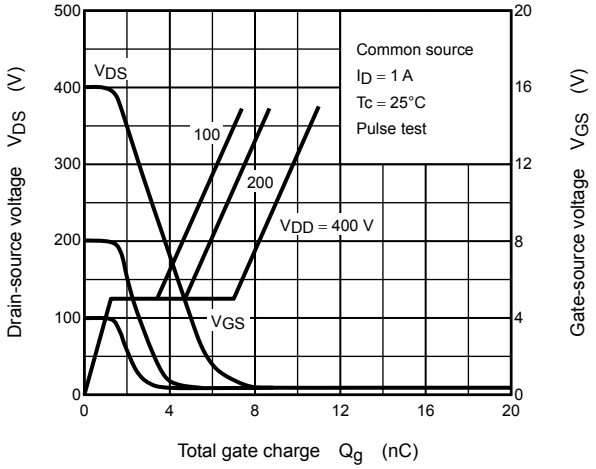
V_{th} – Tc



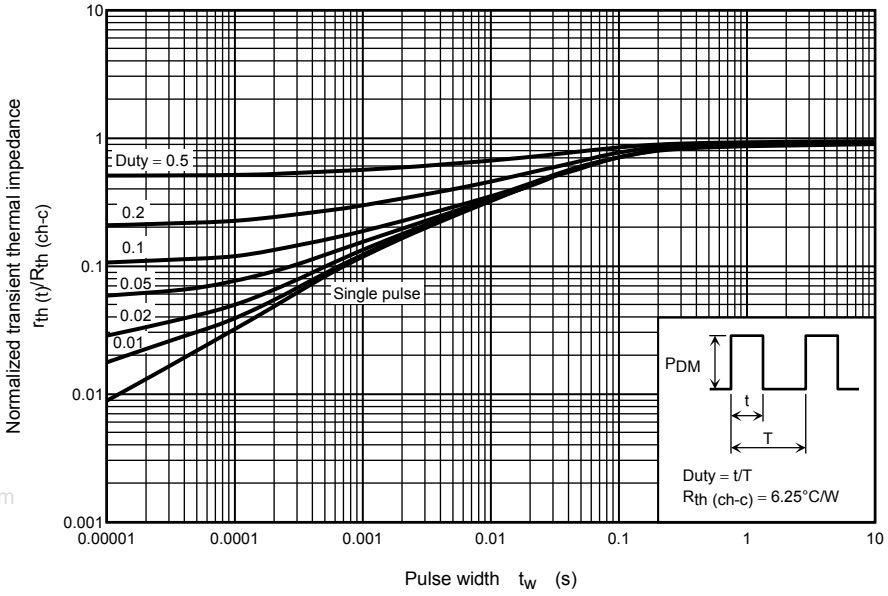
P_D – Tc



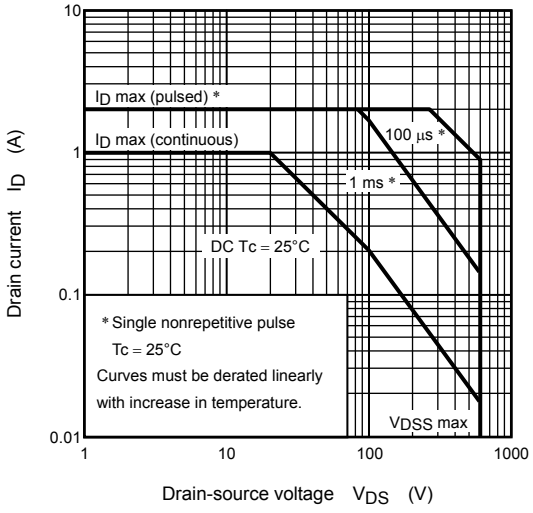
Dynamic input/output characteristics



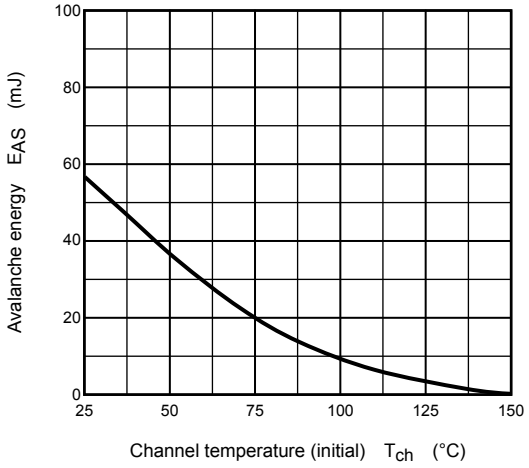
$r_{th} - t_w$

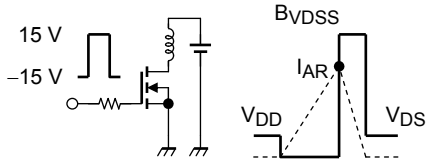


Safe operating area



$E_{AS} - T_{ch}$





Test circuit Waveform

$R_G = 25\ \Omega$
$V_{DD} = 90$ V, L = 100 mH

$$E_{AS} = \frac{1}{2} \cdot L \cdot I^2 \cdot \left(\frac{B_{VDSS}}{B_{VDSS} - V_{DD}} \right)$$

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20070701-EN

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